

PROCESSOR BOARD

I/O BOARD

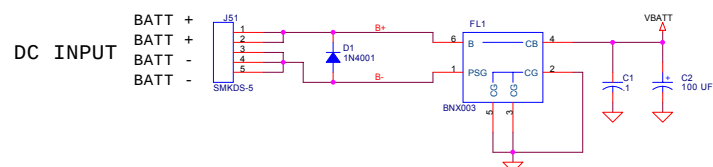
I/O BOARD

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SLOT 5  
(SPARE)

SLOT 6  
(SPARE)



REV A 1/21/03

PCB PART 1001723.PCB  
NOTE: PCB LAYOUT IS REV-C

REV A ADD .1 BYPASS CAPS, CHANGE TO POL CAP C2  
, CHANGE SMKDS5 FOOTPRINT HOLE SIZE TO .052  
CORRECTED SLOT 6 SPI BUSS CONNECTION LOCATION

|                                                                             |                         |              |
|-----------------------------------------------------------------------------|-------------------------|--------------|
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| Monterey Bay Aquarium Research Institute                                    |                         |              |
| File                                                                        | OASIS3 BACKPLANE BOARD  | Rev          |
| Size                                                                        | Document Number         | A            |
| D                                                                           | 1001718.SCH             |              |
| Date                                                                        | Monday, August 09, 2010 | Sheet 0 of 1 |