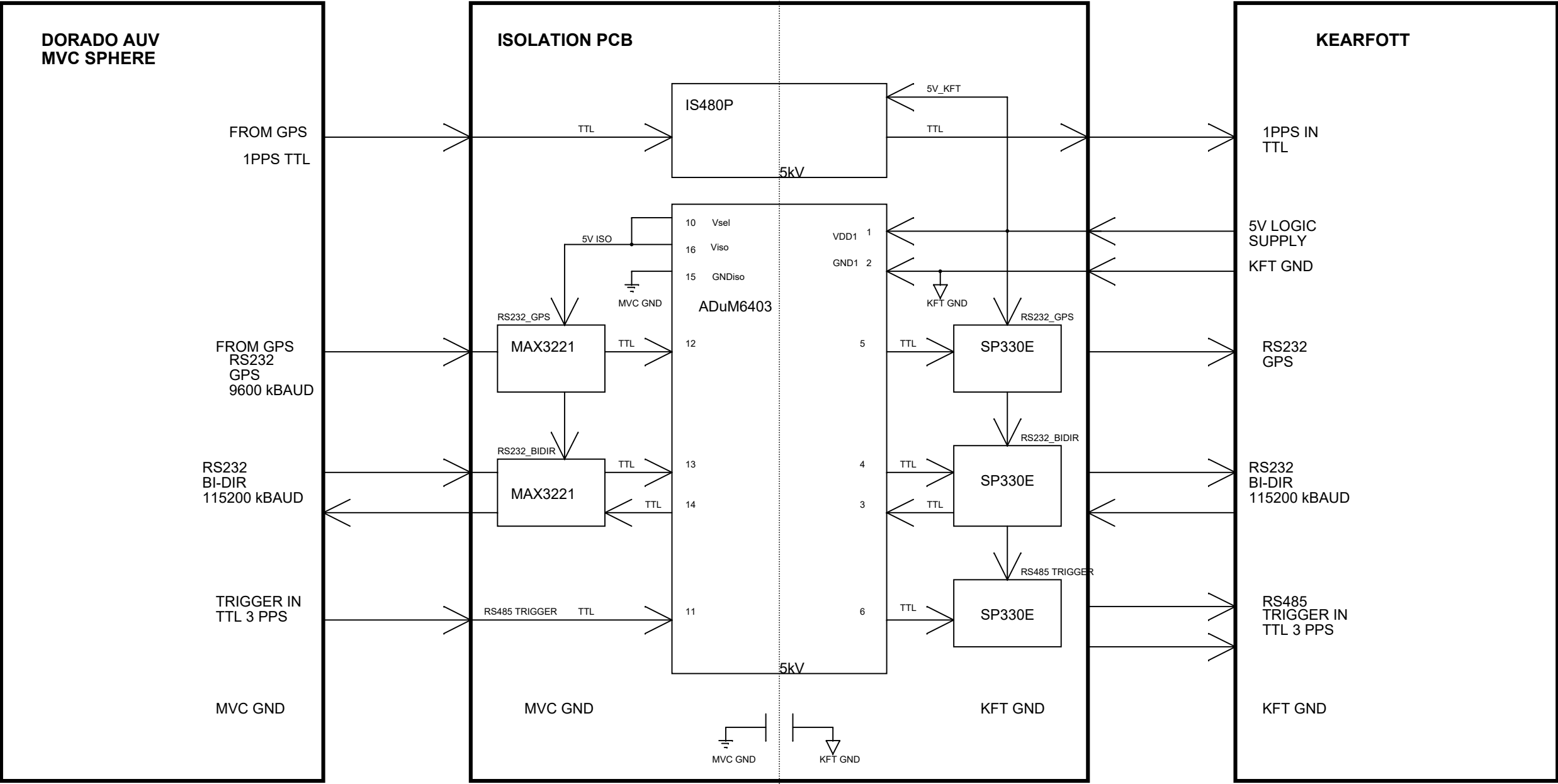




GALVANIC ISOLATION

MULTIPROTOTOL SERIAL TRANSCEIVERS ON KEARFOTT SIDE; CIRCUIT FROM MOTHERBOARD CPU
SIX LAYERS

\\atlas\ProjectLibrary\368000_DMO_AUV\KEARFOTT_ISOLATION

KEARFOTT POWER DRAW

DEVICE	Amps @ 5VDC	
IS480P	0.003	
MAX3221 x 2	0.120	(34% EFF ASSUMED)
ADUM6403	0.004	
SP330E x 3	0.042	
TOTAL	0.169	

Title		
BD_KEARFOTT_ISOLATION		
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