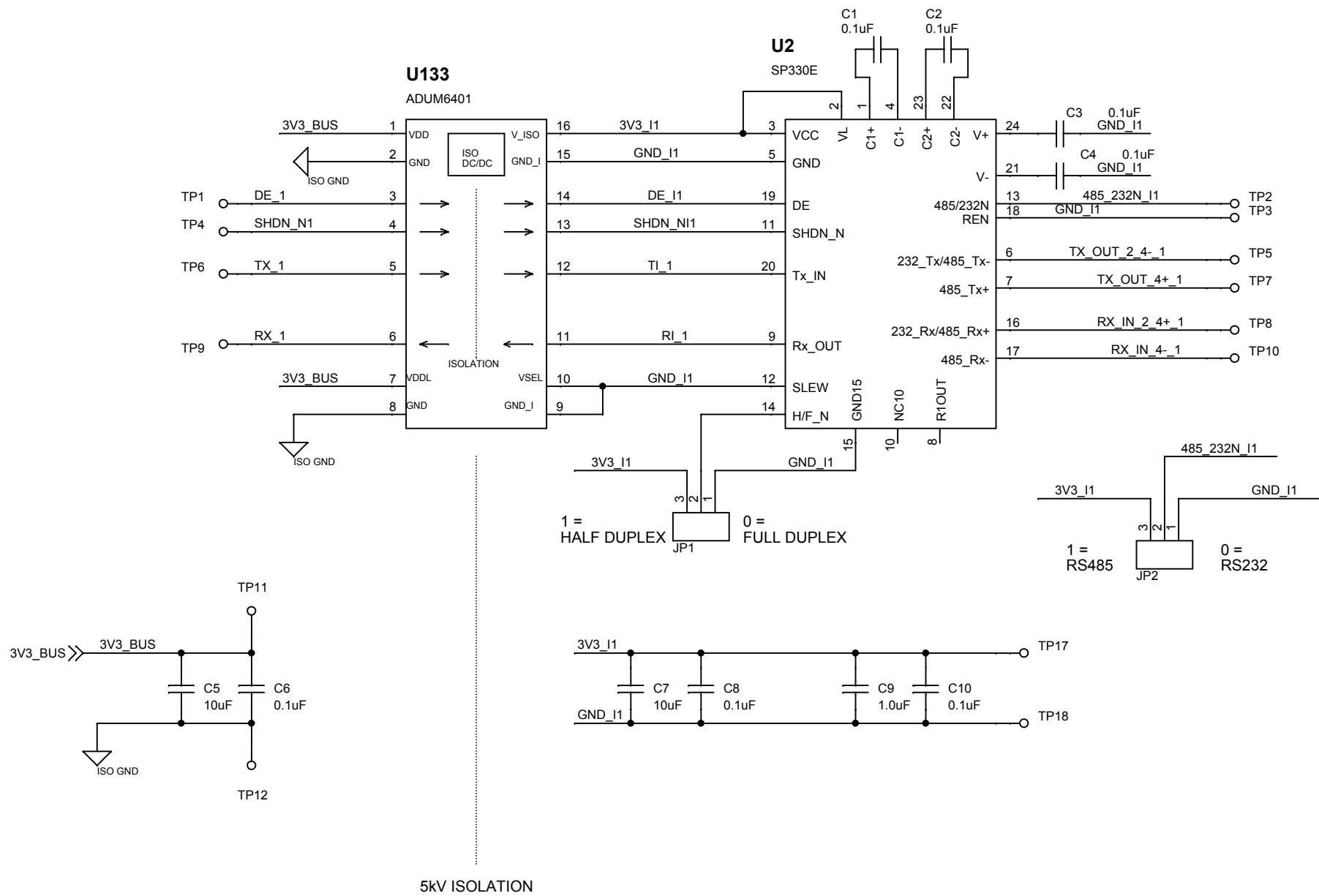
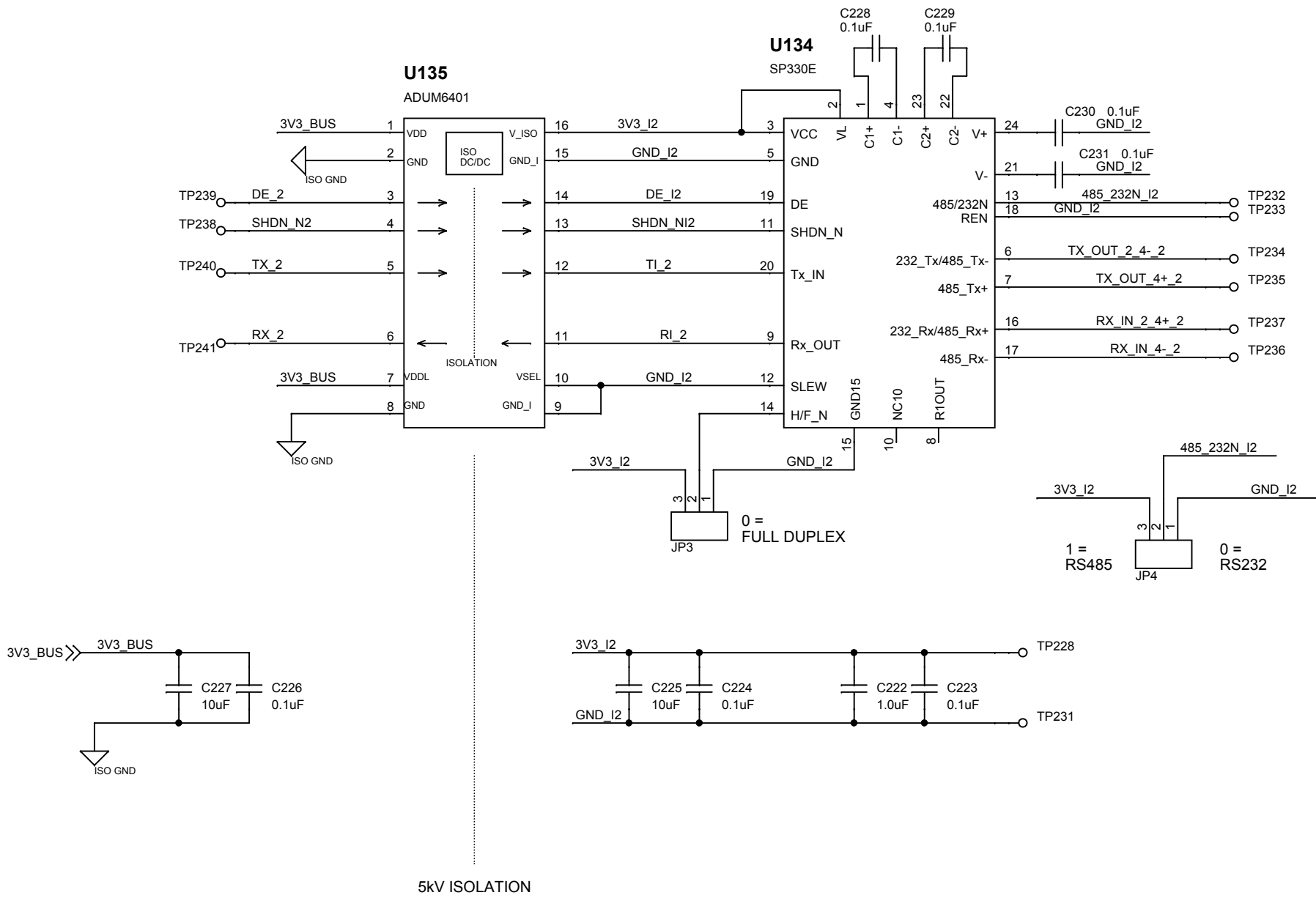
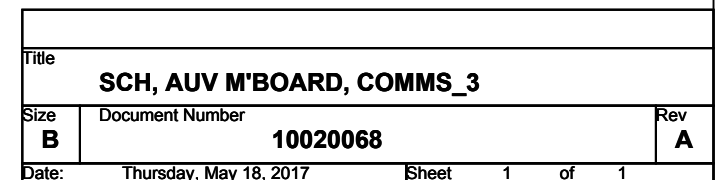


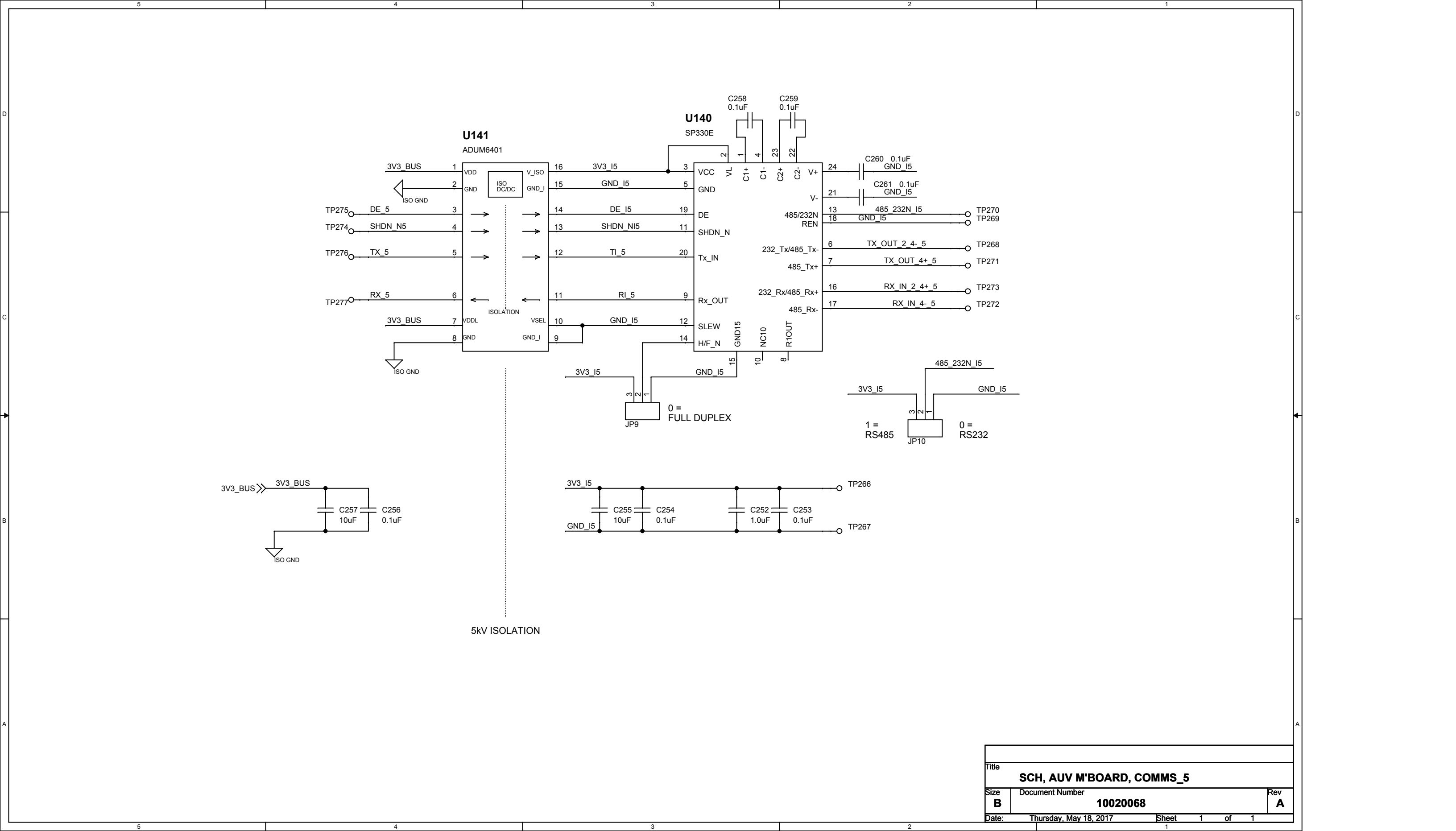


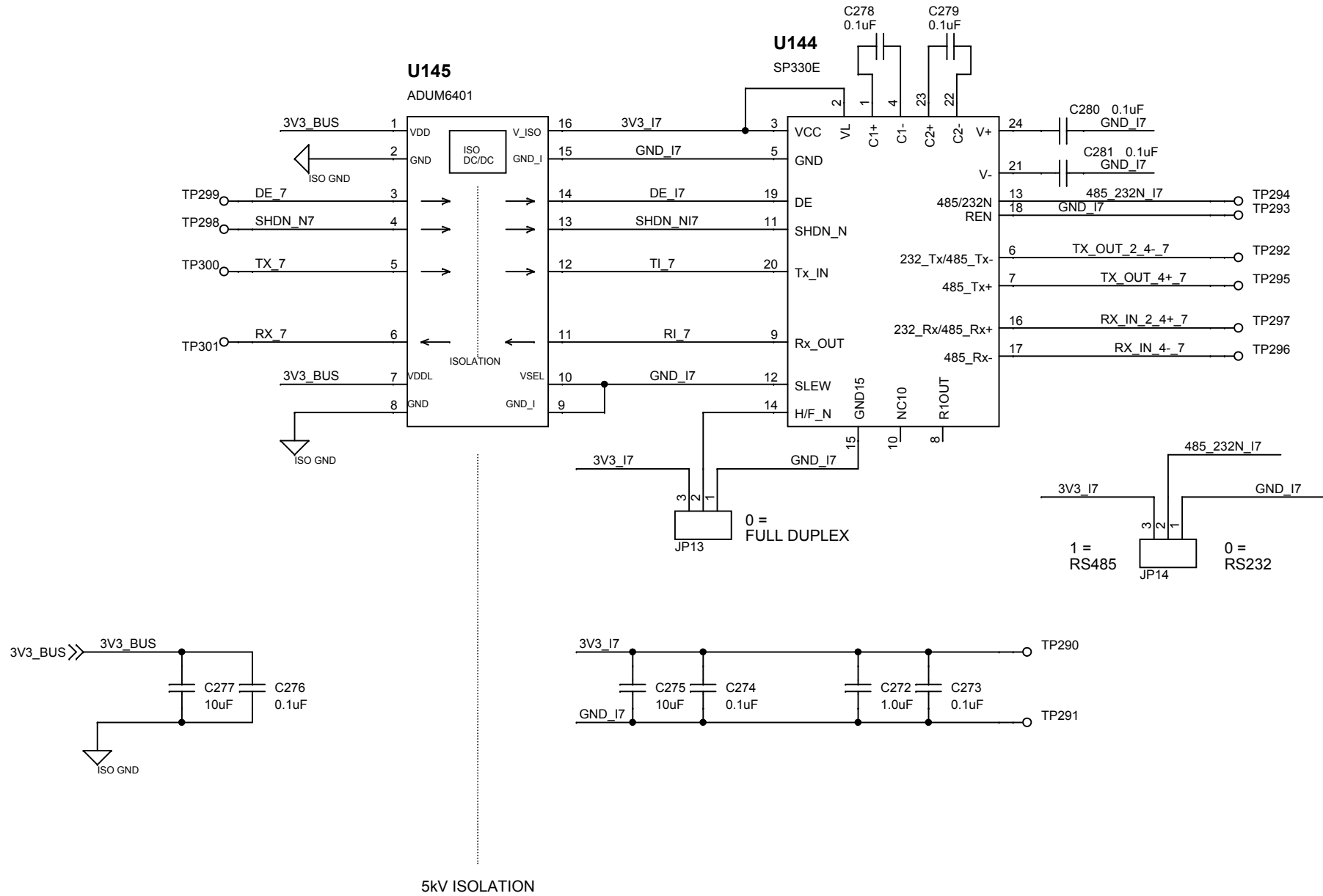
Title		
SCH, AUV M'BOARD, COMMS_1		
Size	Document Number	Rev
B	10020068	A
Date:	Thursday, May 18, 2017	Sheet 1 of 1



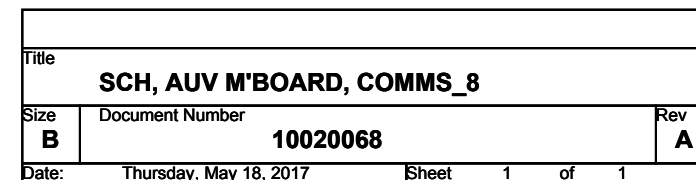
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SCH, AUV M'BOARD, COMMS_2			
Size	Document Number		Rev
B	10020068		A
Date:	Thursday, May 18, 2017	Sheet	1 of 1

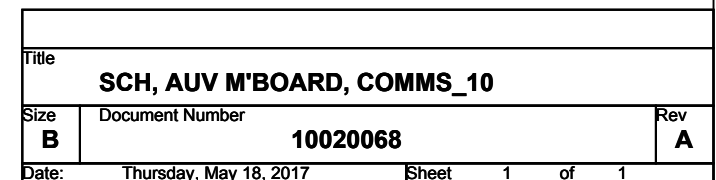


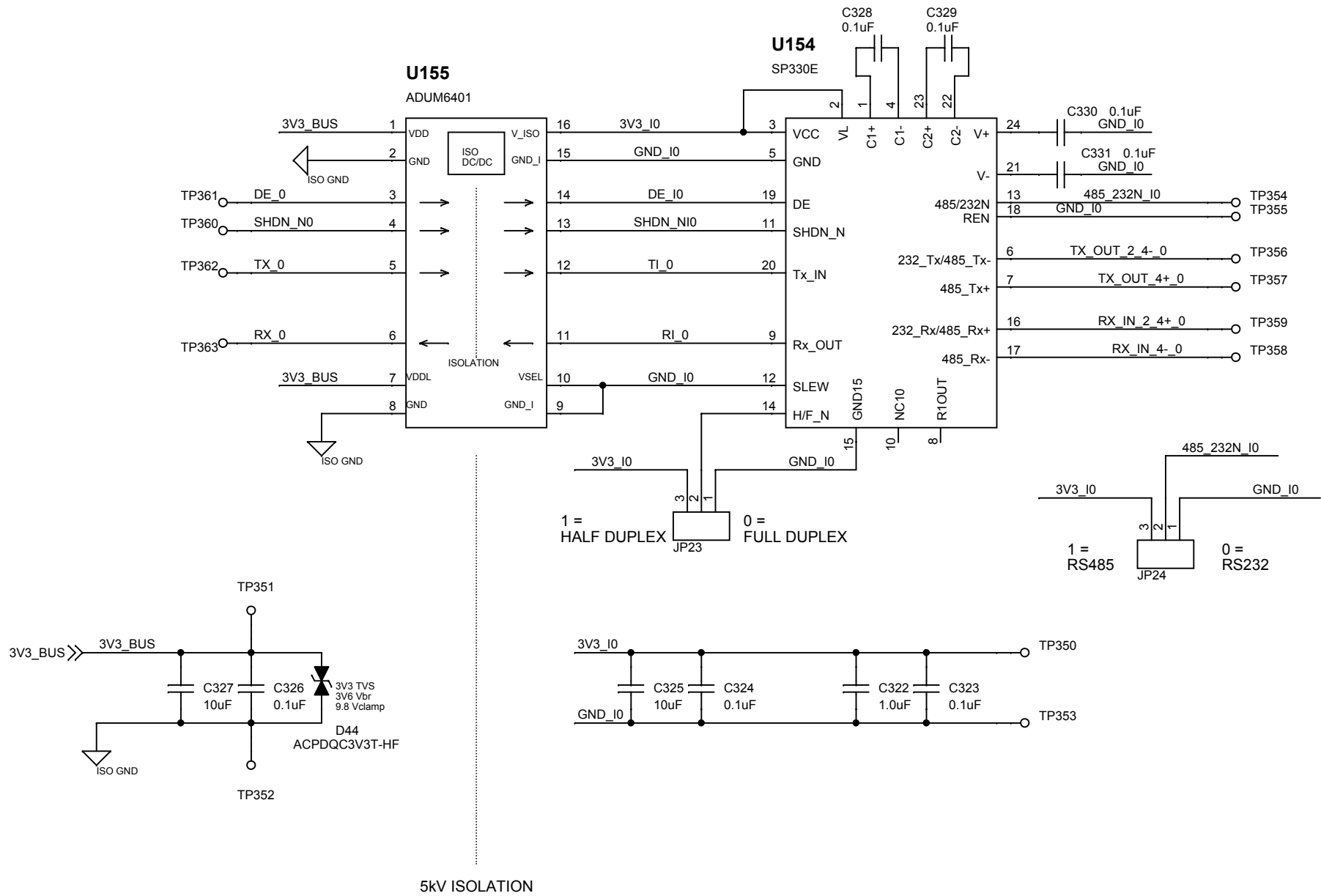




Title			
SCH, AUV M'BOARD, COMMS_7			
Size	Document Number		Rev
B	10020068		A
Date:	Thursday, May 18, 2017	Sheet	1 of 1



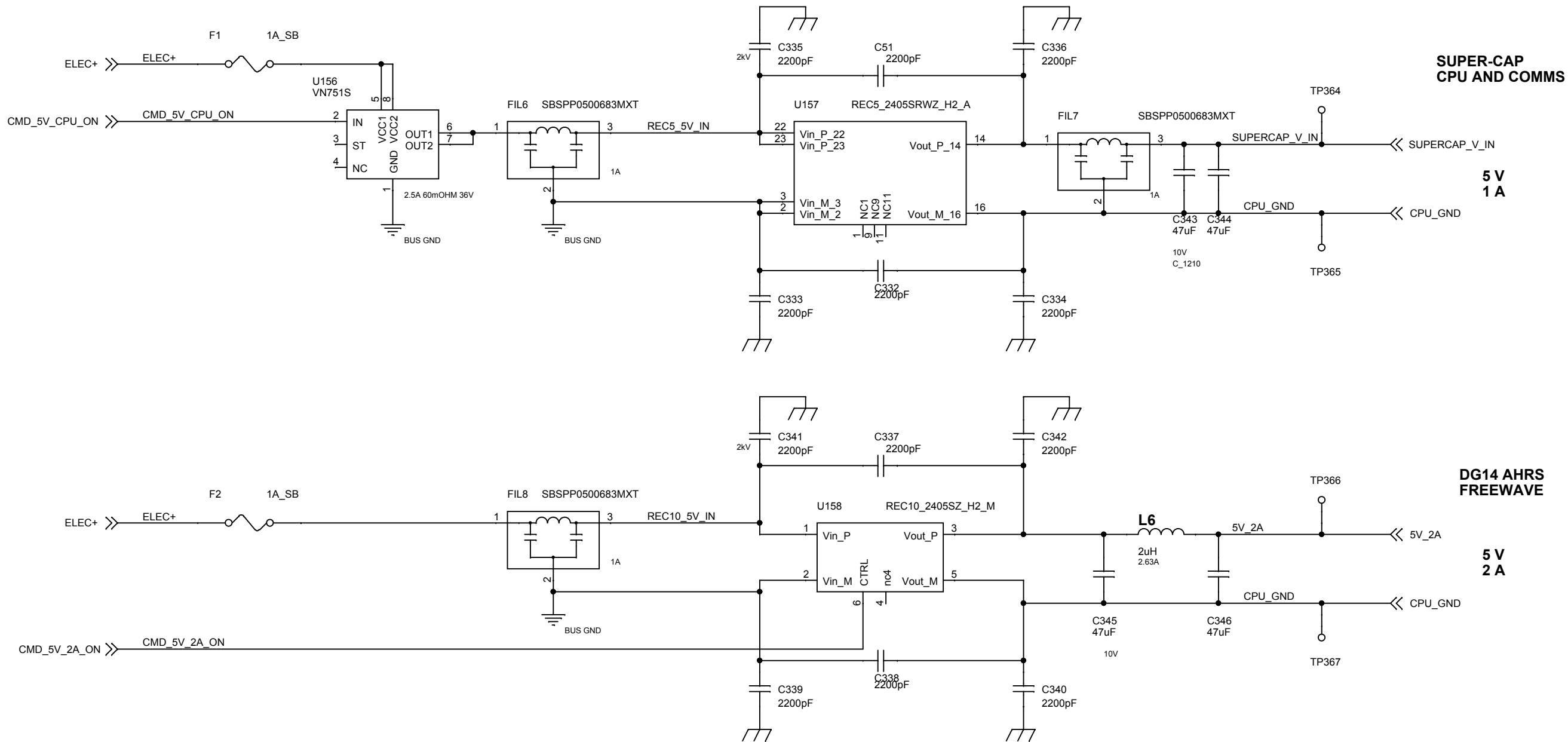




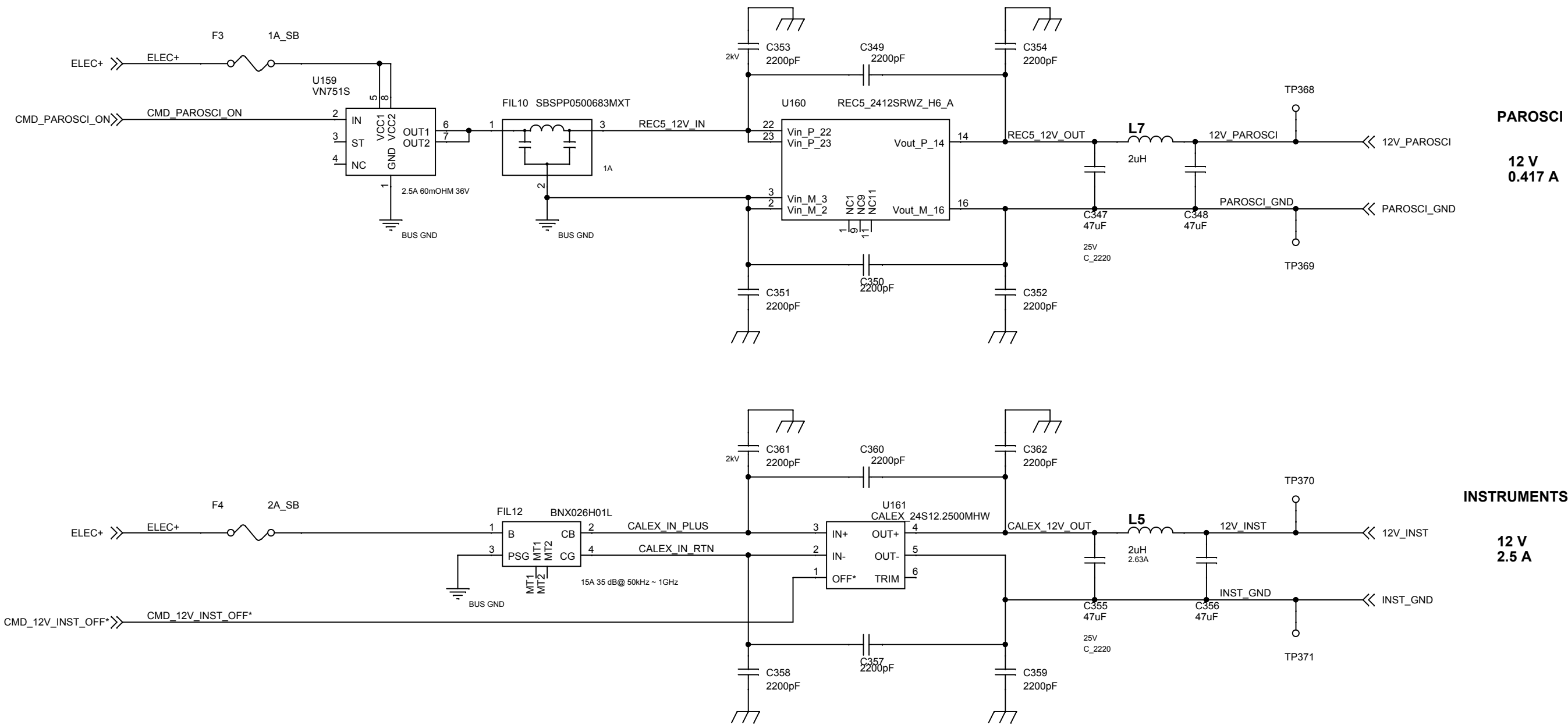
**SINGLE CHANNEL
POWER: 46 mW
.014A at 3V3**

TP13 TP14 TP15 TP16
○ ○ ○ ○
mt_hole_no_4_w_washer

Title		
SCH, AUV M'BOARD, COMMS_0		
Size	Document Number	Rev
B	10020068	A
Date:	Thursday, May 18, 2017	Sheet 1 of 1



Title			
SCH, AUV M'BOARD, DC_DC 5V			
Size	Document Number		Rev
B	10020068		A
Date:	Thursday, May 18, 2017	Sheet	1 of 1



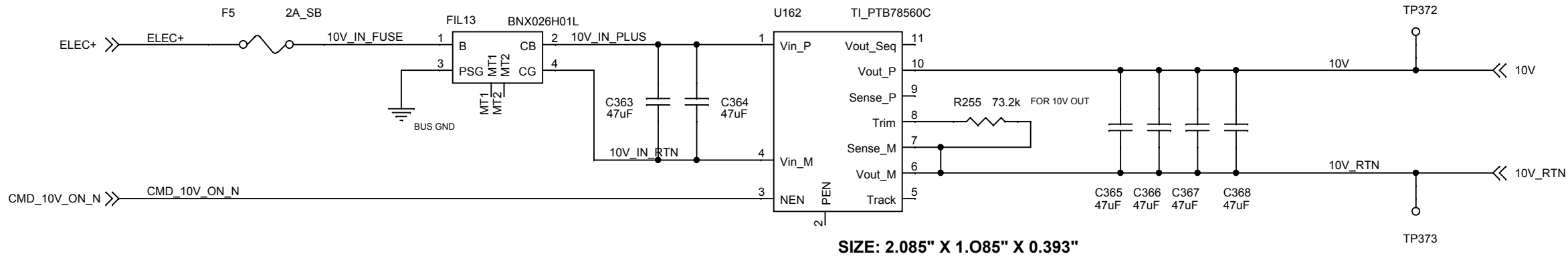
PAROSCI

12 V
0.417 A

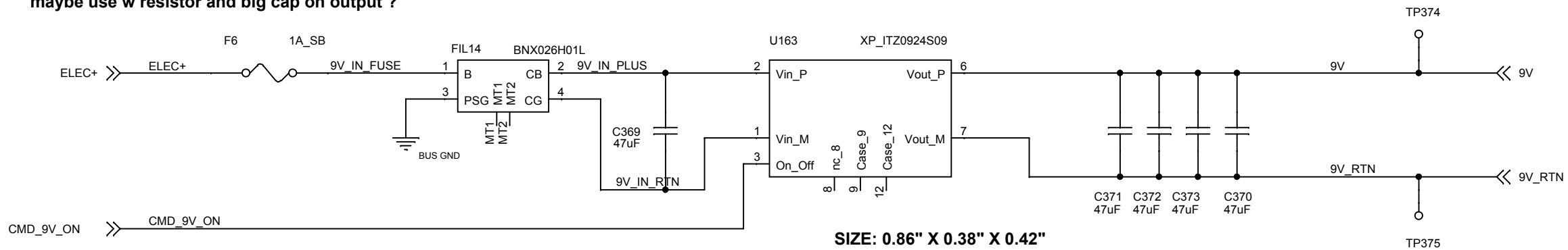
INSTRUMENTS

12 V
2.5 A

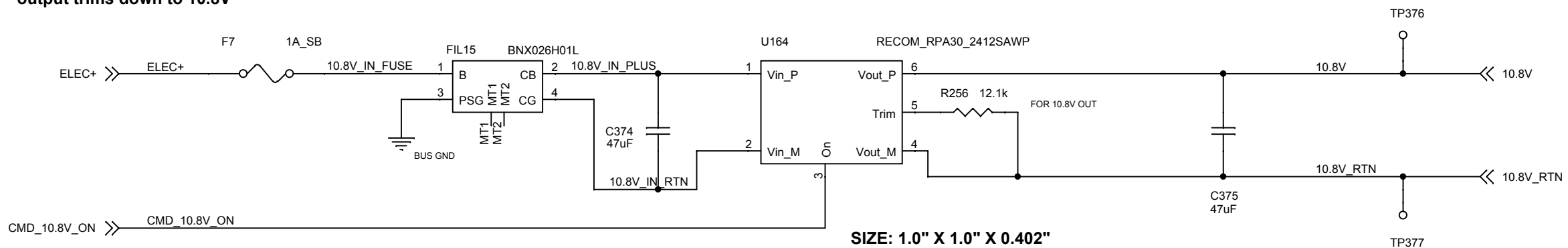
Title			
SCH, AUV M'BOARD, DC_DC 12V			
Size	Document Number		Rev
B	10020068		A
Date:	Thursday, May 18, 2017	Sheet	1 of 1



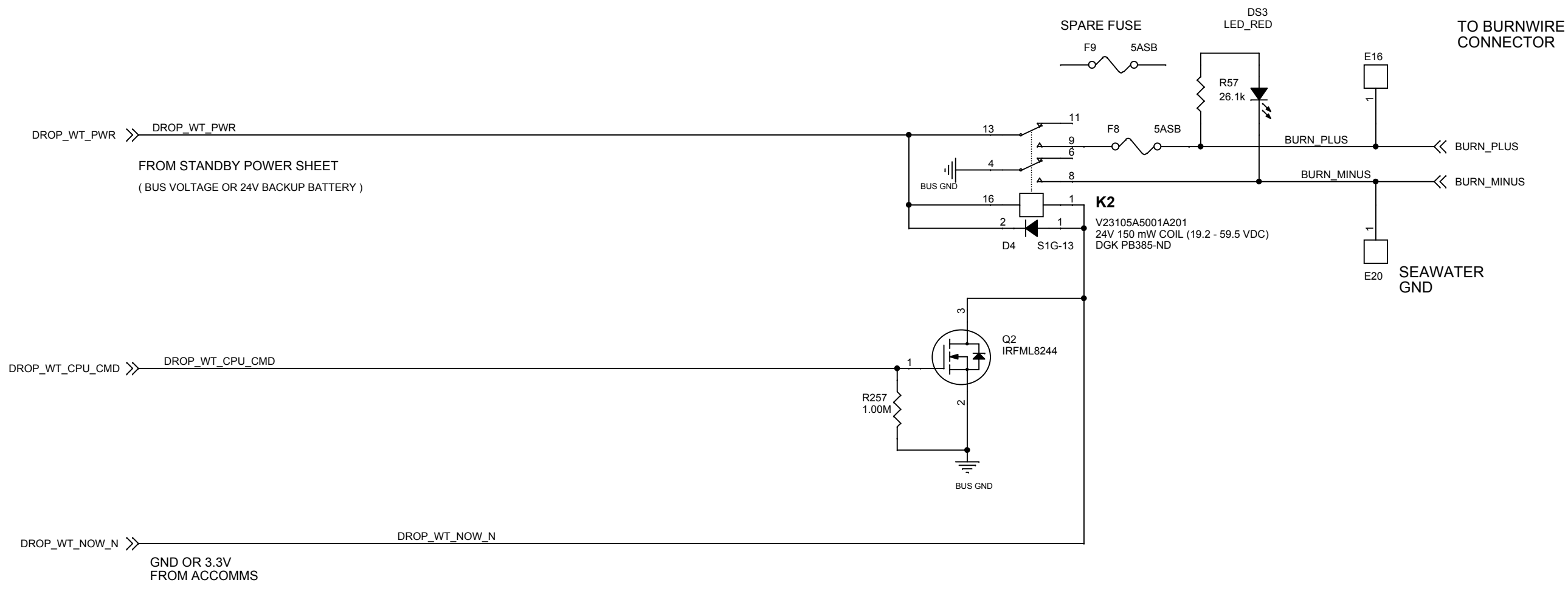
Possibility: XP ITZ0924S09
only 1 amp output
maybe use w resistor and big cap on output ?



Possibility: Recom RPA30-2412SAW
output trims down to 10.8V

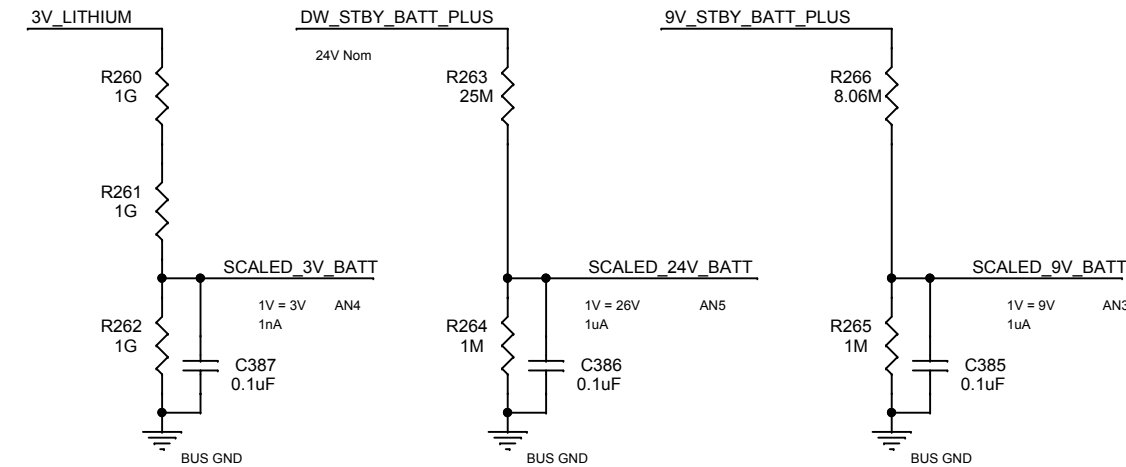
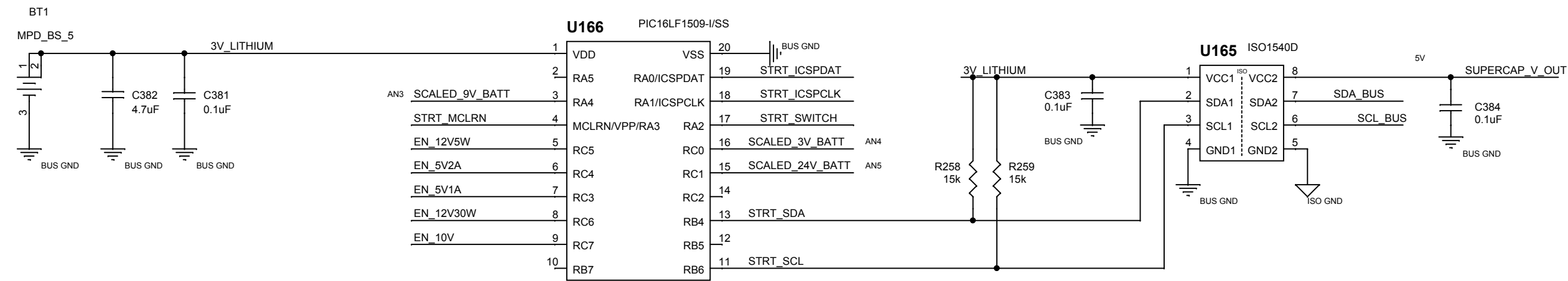


Title			
SCH, AUV M'BOARD, DC_DC 10V			
Size	Document Number		Rev
B	10020068		A
Date:	Thursday, May 18, 2017	Sheet	1 of 1



Title			
SCH, AUV M'BOARD, DROP WEIGHT			
Size	Document Number		Rev
B	10020068		A
Date:	Thursday, May 18, 2017	Sheet	1 of 1

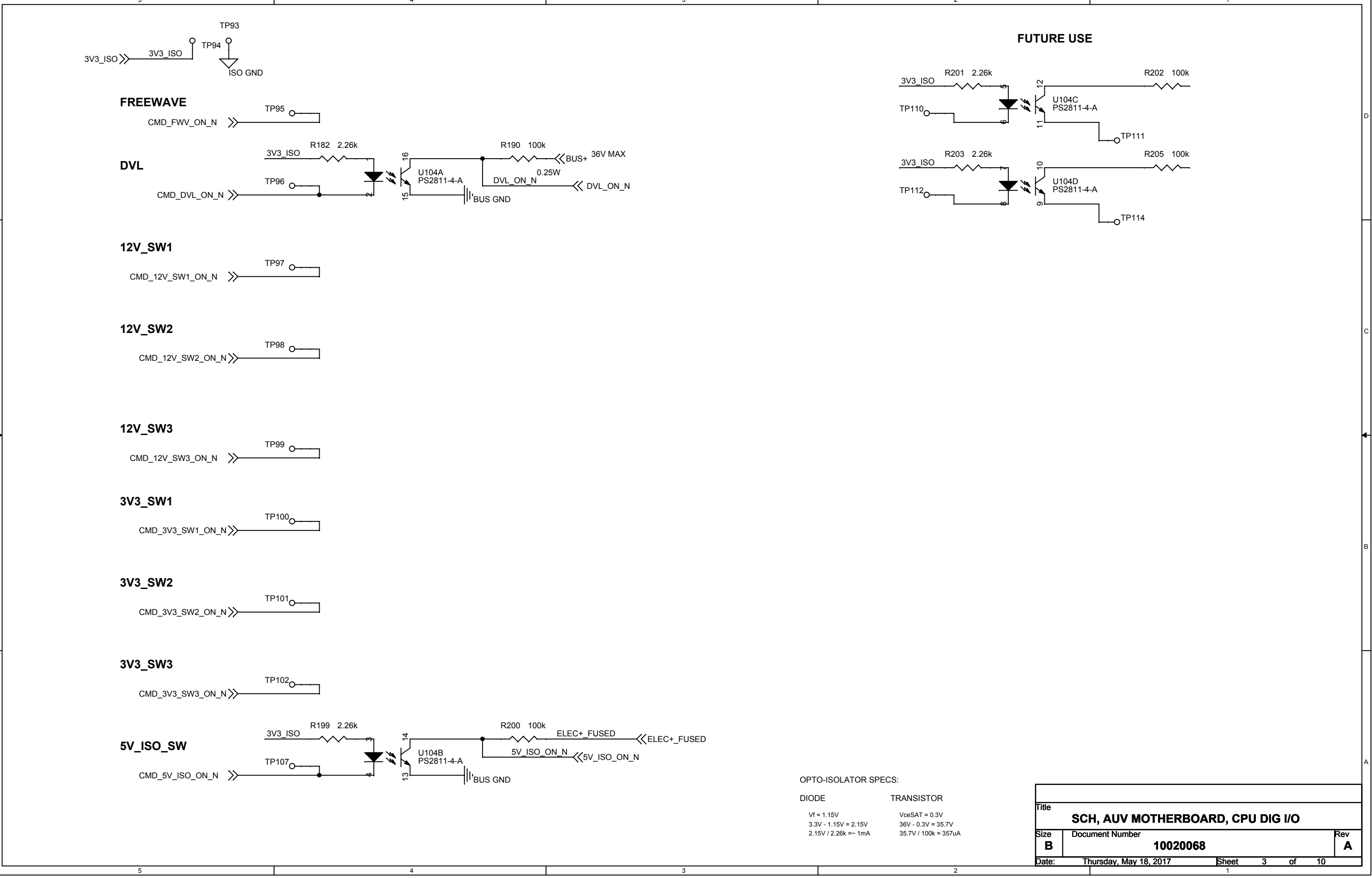
PRELIMINARY SUGGESTION



OFF-PAGE CONNECTORS

SUPERCAP_V_OUT	⇐	SUPERCAP_V_OUT
SDA_BUS	⇐	SDA_BUS
SCL_BUS	⇐	SCL_BUS
STRT_ICSPDAT	⇐	STRT_ICSPDAT
STRT_ICSPCLK	⇐	STRT_ICSPCLK
STRT_MCLR/VPP/RA3	⇐	STRT_MCLR/VPP/RA3
STRT_SWITCH	⇐	STRT_SWITCH
DW_STBY_BATT_PLUS	⇐	DW_STBY_BATT_PLUS
9V_STBY_BATT_PLUS	⇐	9V_STBY_BATT_PLUS
DW_STBY_BATT_PLUS	⇐	DW_STBY_BATT_PLUS
EN_5V1A	⇐	EN_5V1A
EN_5V2A	⇐	EN_5V2A
EN_12V5W	⇐	EN_12V5W
EN_12V30W	⇐	EN_12V30W
EN_10V	⇐	EN_10V

Title		
SCH, AUV M'BOARD, uCHIP XLP		
Size	Document Number	Rev
B	10020068	A
Date:	Thursday, May 18, 2017	Sheet 1 of 1



D

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C

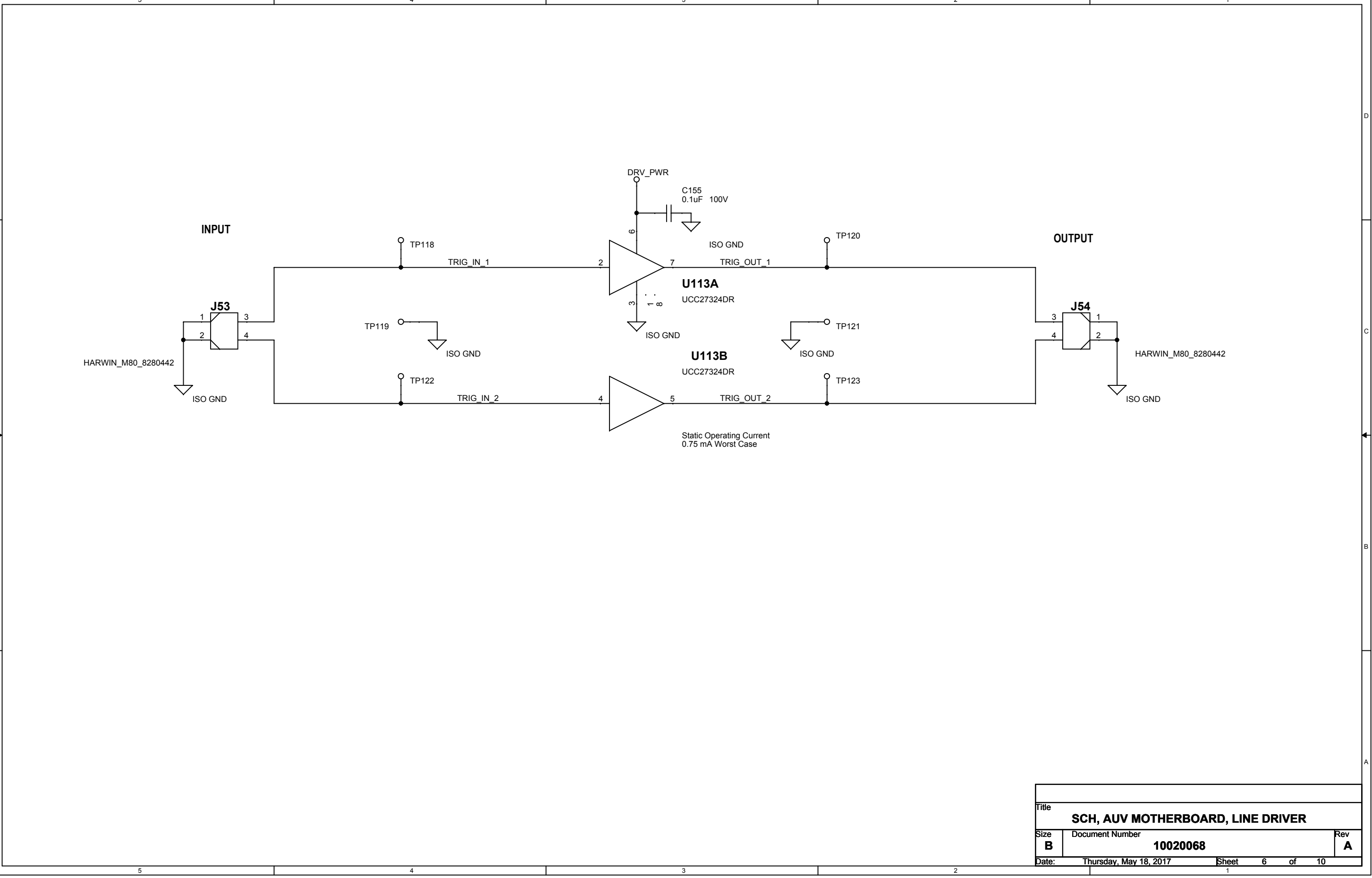
C

B

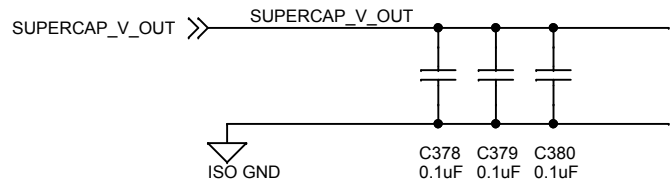
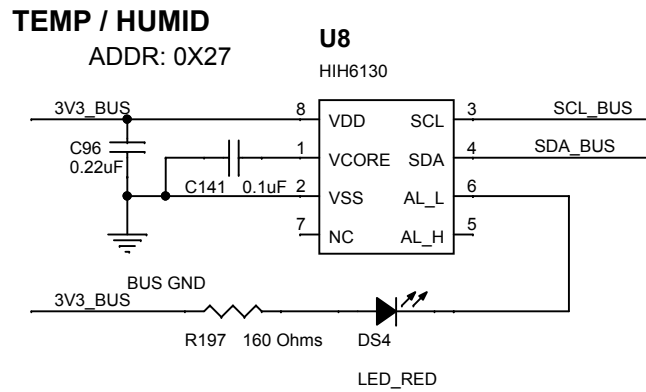
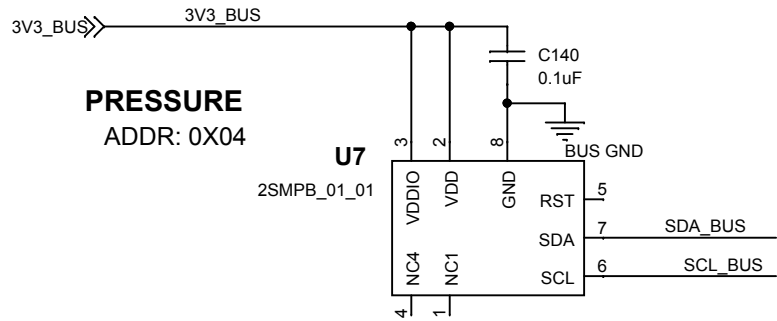
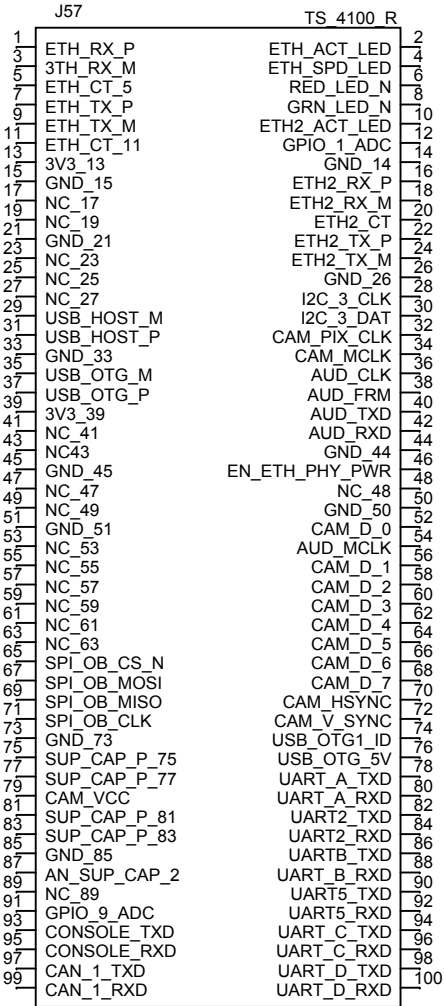
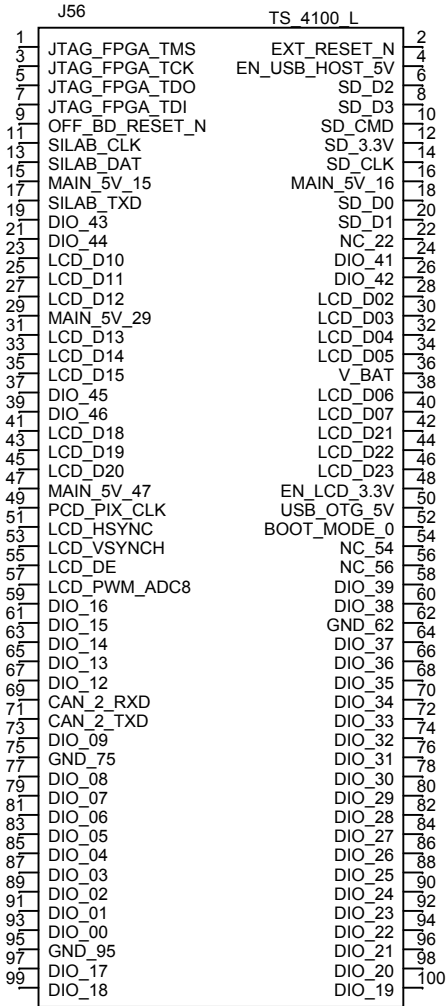
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Title			
SCH, AUV MOTHERBOARD, LINE DRIVER			
Size	Document Number		Rev
B	10020068		A
Date:	Thursday, May 18, 2017	Sheet	6 of 10



LOCATE CLOSE TO CPU PINS

Title		
SCH, AUV MOTHERBOARD, TS_4100, ENV SENS		
Size	Document Number	Rev
B	10020068	A
Date:	Thursday, May 18, 2017	Sheet 7 of 10

NO MAGNET=
SWITCH CLOSED=
ENABLED

Reed Switch:
SPST-NC 0.5A
Digikey
59140-040-ND

J4

HARWIN_M80_8280442
VERT S.M.

R61 97.6k

ENABLE_ACOMMS_SOFT_START

J22

MT3 MT4

DW_STBY_BATT_PLUS

MT2 HARWIN_M80_8510642
MT1 VERT S.M.

DW_STBY_BATT_MINUS

BUS GND

FROM BUS+

ELEC+

C162
0.1uF

BUS GND

K1

V23105A5005A201
24V 150 mW COIL (19.2 - 59.5 VDC)
DGGK PB385-ND

D3
S1G-E3

BUS GND

R54 23.7k

R70 3.65k

BUS GND

R69 41.2k

VREF

PROG

SENSE

GATE

OUT

PG

EN

VCC

10

9

8

7

6

5

4

3

2

1

0

-1

-2

-3

-4

-5

-6

-7

-8

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