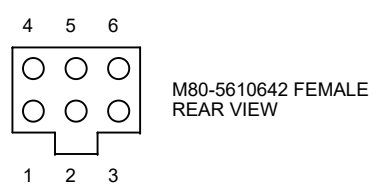
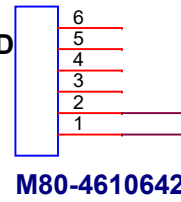


PAROSCI



OVERALL LENGTH

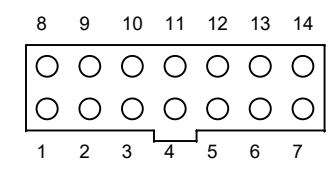
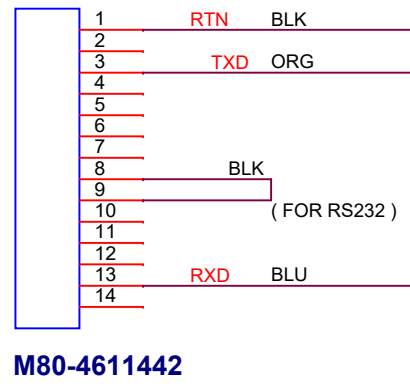
TO CPU BOARD
J11



+12V RED
RTN BLK

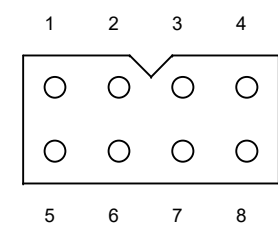
TO CPU BOARD
J1

SER_0
NATIVE UART 1



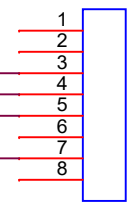
POSITRONIC
PLB 08F 0000

REAR VIEW



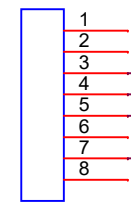
J_2

MC-BH-8F



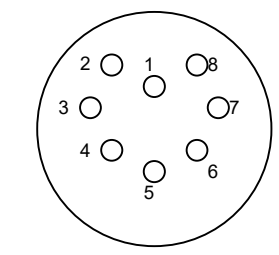
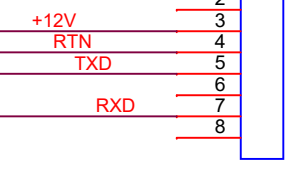
J_

PLB 08F 0000

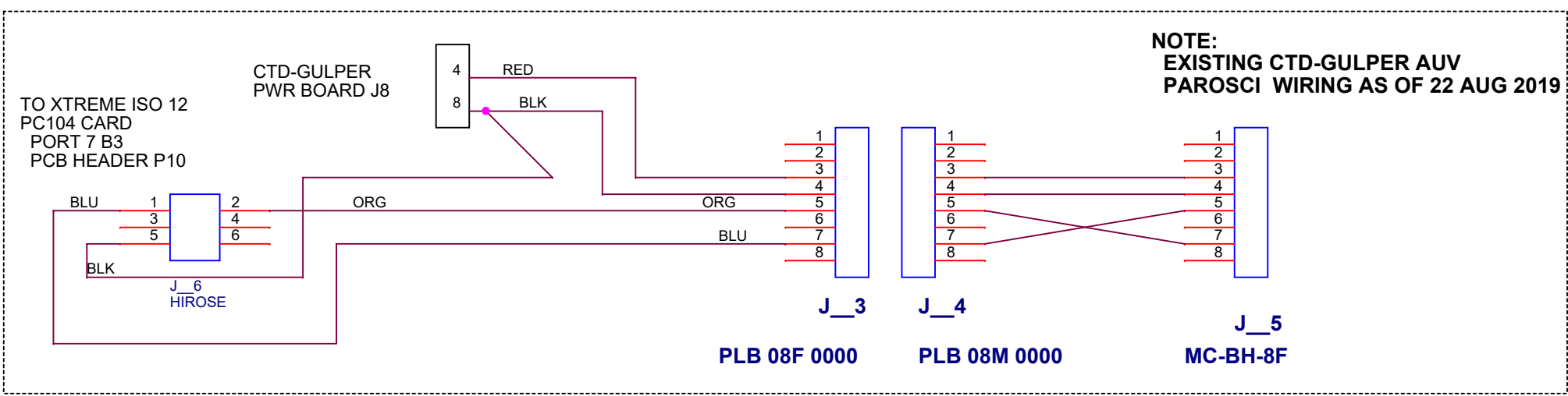


J_1

PLB 08M 0000



MC-BH-8F
FACE VIEW



Title		
SCH, DORADO CPU / PAROSCI RS232		
Size	Document Number	Rev
B	100221594	A
Date:	Friday, April 30, 2021	Sheet 1 of 1