

Pin Name	Functional Group	Function 00	Function 01 Alt Func 1	Function 10 Alt Func 2	Function 11 Alt Func 3	Medusa 1 Primary Function	Medusa 2 Primary Function	Func Code	I/O	Init Val
P0.0	UART0	GPIO0.0	TxD0	PWM1	Reserved	Console TX	Console TX	01		
P0.1		GPIO0.1	RxD0	PWM3	EINT0	Console RX	Console RX	01		
P0.2	I2C	GPIO0.2	SCL0	CAP0.0	Reserved	SCL	SCL	01		
P0.3		GPIO0.3	SDA0	MAT0.0	EINT1	SDA	SDA	01		
P0.4	SPI	GPIO0.4	SCK0	CAP0.1	AD0.6	SPI_CLOCK	SW_ADDR_00	00	Out	0
P0.5		GPIO0.5	MISO0	MAT0.1	AD0.7	SPI_MISO	SW_ADDR_01	00	Out	0
P0.6		GPIO0.6	MOSI0	CAP0.2	AD1.0	SPI_MOSI	SW_ADDR_02	00	Out	0
P0.7		GPIO0.7	SSEL0	PWM2	EINT2	SPI_CS_N		00		
P0.8	UART1	GPIO0.8	TxD1	PWM4	AD1.1	SPI_INTR_TP	SW_DATA_00	00	Bidir	0
P0.9		GPIO0.9	RxD1	PWM6	EINT3	SW_WR_N	SW_DATA_01	00	Bidir	0
P0.10		GPIO0.10	RTS1	CAP1.0	AD1.2	SW_RD_N	SW_DATA_02	00	Bidir	0
P0.11		GPIO0.11	CTS1	CAP1.1	SCL1	SW_CS_N	SW_DATA_03	00	Bidir	0
P0.12		GPIO0.12	DSR1	MAT1.0	AD1.3	ORANGE_LED_ON	SW_DATA_04	00	Bidir	0
P0.13		GPIO0.13	DTR1	MAT1.1	AD1.4	GREEN_LED_ON	SW_DATA_05	00	Bidir	0
P0.14		GPIO0.14	DCD1	EINT1	SDA1	ISP_DISABLE	SW_DATA_06	00	Bidir	0
P0.15		GPIO0.15	RI1	EINT2	AD1.5	SWITCH_IRQ_N	SW_DATA_07	00	Bidir	0
P0.16	EXT Int	GPIO0.16	EINT0	MAT0.2	CAP0.2	SW_DATA_00	SWITCH_IRQ_N	01		
P0.17		GPIO0.17	CAP1.2	SCK(SSP)	MAT1.2	SW_DATA_01	SPI_CLOCK	10		
P0.18		GPIO0.18	CAP1.3	MISO(SSP)	MAT1.3	SW_DATA_02	SPI_MISO	10		
P0.19		GPIO0.19	MAT1.2	MOSI(SSP)	MAT1.3	SW_DATA_03	SPI_MOSI	10		
P0.20		GPIO0.20	MAT1.3	SSEL(SSP)	EINT3	SW_DATA_04	SPI_CS_N	10		
P0.21		GPIO0.21	PWM5	AD1.6	CAP1.3	SW_DATA_05	SPI_INTR_TP	00	Out	0
P0.22		GPIO0.22	AD1.7	CAP0.0	MAT0.0	SW_DATA_06	SFP_ENABLE_0	00	Out	
P0.23		GPIO0.23	Reserved	Reserved	Reserved	SW_DATA_07	LIGHT_DETECTED	00	In	
P0.24		Reserved	Reserved	Reserved	Reserved	BOOT_EEPROM_SDA		00		
P0.25		GPIO0.25	AD0.4	Aout(DAC)	Reserved	BOOT_EEPROM_SCL		00		
P0.26		Reserved	Reserved	Reserved	Reserved	SW_ADDR_02		00		
P0.27		GPIO0.27	AD0.0	CAP0.1	MAT0.1	SEC_TRST		00		
P0.28		GPIO0.28	AD0.1	CAP0.2	MAT0.2	SEC_TMS	SW_WR_N	00	Out	1
P0.29		GPIO0.29	AD0.2	CAP0.3	MAT0.3	SEC_TCK	SW_RD_N	00	Out	1
P0.30		GPIO0.30	AD0.3	EINT3	CAP0.0	SEC_TDI	SW_CS_N	00	Out	1
P0.31		GPIO0.31	Reserved	Reserved	Reserved	SEC_TDO	***	00		
P1.0		*								
P1.1		*								
P1.2		*								
P1.3		*								
P1.4		*								
P1.5		*								
P1.6		*								
P1.7		*								
P1.8		*								
P1.9		*								
P1.10		*								
P1.11		*								
P1.12		*								
P1.13		*								
P1.14		*								
P1.15		*								
P1.16		GPIO1.16					SFP_INSERT_0			
P1.17		GPIO1.17					SFP_INSERT_1			
P1.18		GPIO1.18					SFP_INSERT_2			
P1.19		GPIO1.19					SFP_INSERT_3			
P1.20		GPIO1.20								
P1.21		GPIO1.21					SFP_ENABLE_1			
P1.22		GPIO1.22					SFP_ENABLE_2			
P1.23		GPIO1.23					SFP_ENABLE_3			
P1.24		GPIO1.24					LED_ON_0			
P1.25		GPIO1.25					LED_ON_1			
P1.26		GPIO1.26					RTCK **			
P1.27		GPIO1.27					TDO			
P1.28		GPIO1.28					TDI			
P1.29		GPIO1.29					TCK			
P1.30		GPIO1.30					TMS			
P1.31		GPIO1.31					TRST			
** - pin P1.26 must be pulled low to enable debug functionality on pins P1.26 – P1.31 !!										
* - P1.0 – P1.15 not available for use as GPIO										

*** - P0.31 is output only								
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