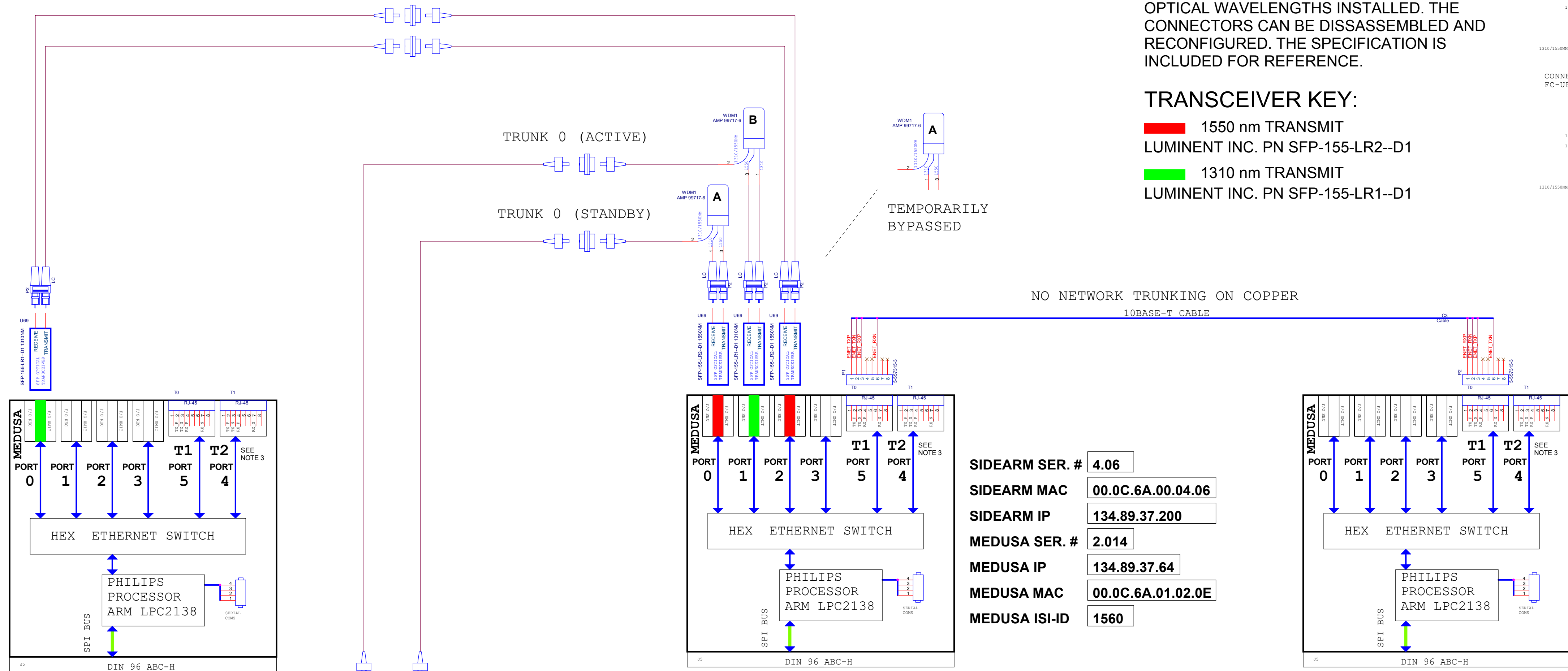
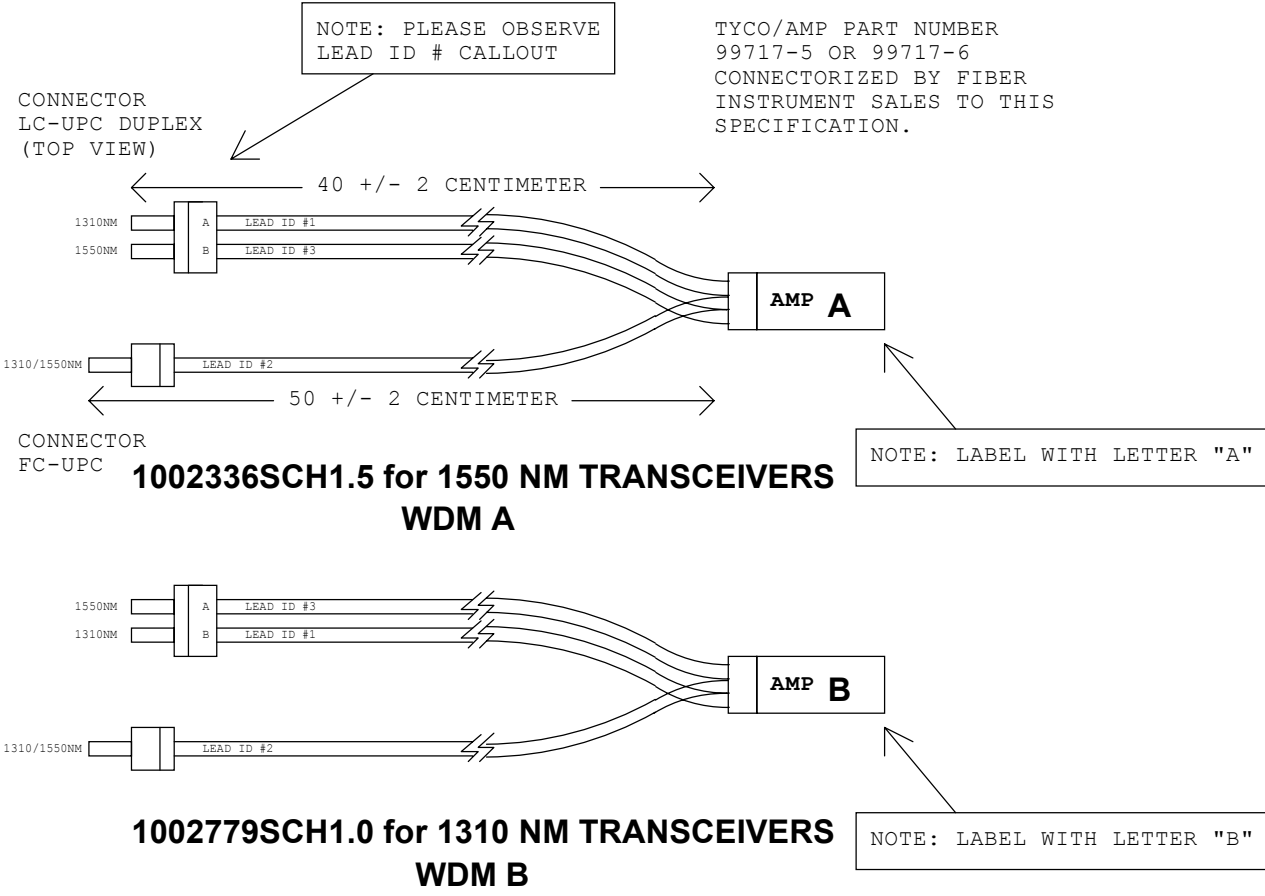




NOTE1B:
THE PROPER WDM CABLE SPECIFICATIONS MUST BE USED FOR THE TWO TRANSCEIVER OPTICAL WAVELENGTHS INSTALLED. THE CONNECTORS CAN BE DISSASSEMBLED AND RECONFIGURED. THE SPECIFICATION IS INCLUDED FOR REFERENCE.

TRANSCEIVER KEY:
1550 nm TRANSMIT
LUMINENT INC. PN SFP-155-LR2--D1
1310 nm TRANSMIT
LUMINENT INC. PN SFP-155-LR1--D1



NOTE1:
THE SPECIFIED TRANSCEIVER OPTICAL WAVELENGTHS MUST BE INSTALLED TO ALLOW IN-SITU OPTICAL LOOPBACK TESTING BETWEEN ADJACENT TRANSCEIVERS (SEE NOTE 1B).

**ISI-ID 1410
GUMDROP
SNUBBER**

SIDEARM SER. # 4.13
SIDEARM MAC 00.0C.6A.00.04.0D
SIDEARM IP 134.89.37.13
MEDUSA SER. # 2.013
MEDUSA IP 134.89.37.63
MEDUSA MAC A 00.0C.6A.01.02.0D
MEDUSA ISI-ID 1571

**ISI-ID 1407
SURTSEY
SPARE SURFACE PRIMARY**

SIDEARM SER. # 4.06
SIDEARM MAC 00.0C.6A.00.04.06
SIDEARM IP 134.89.37.200
MEDUSA SER. # 2.014
MEDUSA IP 134.89.37.64
MEDUSA MAC 00.0C.6A.01.02.0E
MEDUSA ISI-ID 1560

**ISI-ID 1540
VATOA
SPARE SURFACE SECONDARY**

SIDEARM SER. # 4.03
SIDEARM MAC 00.0C.6A.00.04.02
SIDEARM IP 134.89.37.03
MEDUSA SER. # 2.002
MEDUSA IP 134.89.37.52
MEDUSA MAC 00.0C.6A.01.02.02
MEDUSA ISI-ID 1542

NOTE2:
THE MEDUSA AND SIDEARM SERIAL NUMBERS AND MAC ADDRESSES ARE FOR REFERENCE ONLY AND MAY NOT BE UP TO DATE. CHECK MTM4 MASTER CONFIGURATION DOCUMENT FOR CONFIRMATION.

NOTE3:
THE SIDEARM 10BASE-T CONNECTION MUST BE ON MEDUSA PORT 4 (SILKSCREEN REF. T2). THIS PORT ONLY IS SOFTWARE CONFIGURED FOR 10BASET AND HALF-DUPLEX. THE OTHER PORTS AUTO-NEGOIATE.

PORTAL: SEAMONKEY

**NODE5 ISI-ID 1521
NAVITI
SHELF NODE**

SIDEARM SER. # 4.05
SIDEARM MAC 00.0C.6A.00.04.05
SIDEARM IP 134.89.36.05
MEDUSA SER. # 2.003
MEDUSA IP 134.89.36.53
MEDUSA MAC 00.0C.6A.01.02.03
MEDUSA ISI-ID 1528

**ISI-ID 1522
VIWA
AXIS NODE**

SIDEARM SER. # 4.11
SIDEARM MAC 00.0C.6A.00.04.0B
SIDEARM IP 134.89.36.11
MEDUSA SER. # 2.008
MEDUSA IP 134.89.37.58
MEDUSA MAC 00.0C.6A.01.02.08
MEDUSA ISI-ID 1526

MTM4 DEPLOYED TEST SYSTEM MOOS Network 2 (134.89.37.xx)

REVISION HISTORY
1.0 30JAN06 mrc Initial release.
1.5 12SEP06 mrc Updated hardware config.