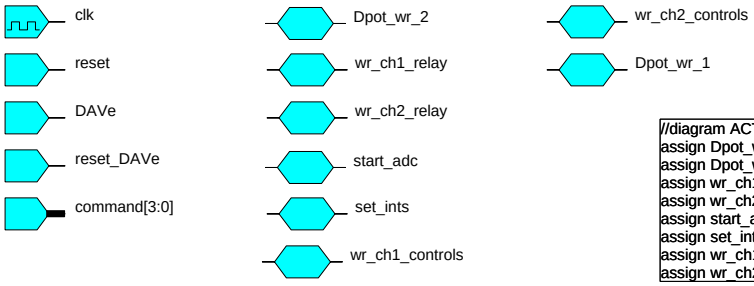
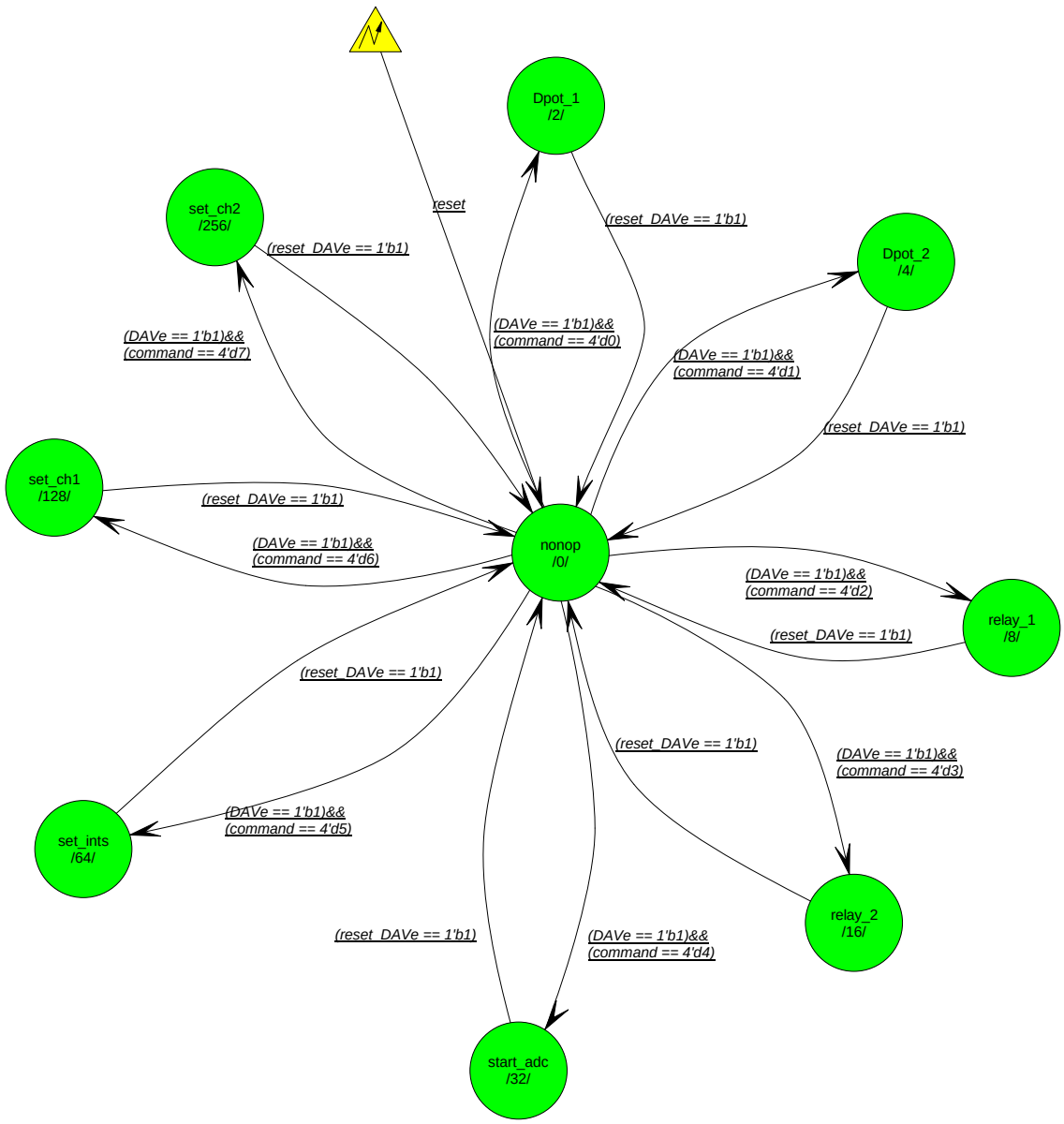
# Module: Write_command




```
//diagram ACTIONS
assign Dpot_wr_1 = CurrState_Sreg0[1];
assign Dpot_wr_2 = CurrState_Sreg0[2];
assign wr_ch1_relay = CurrState_Sreg0[3];
assign wr_ch2_relay = CurrState_Sreg0[4];
assign start_adc = CurrState_Sreg0[5];
assign set_ints = CurrState_Sreg0[6];
assign wr_ch1_controls = CurrState_Sreg0[7];
assign wr_ch2_controls = CurrState_Sreg0[8];
```

Sreg0