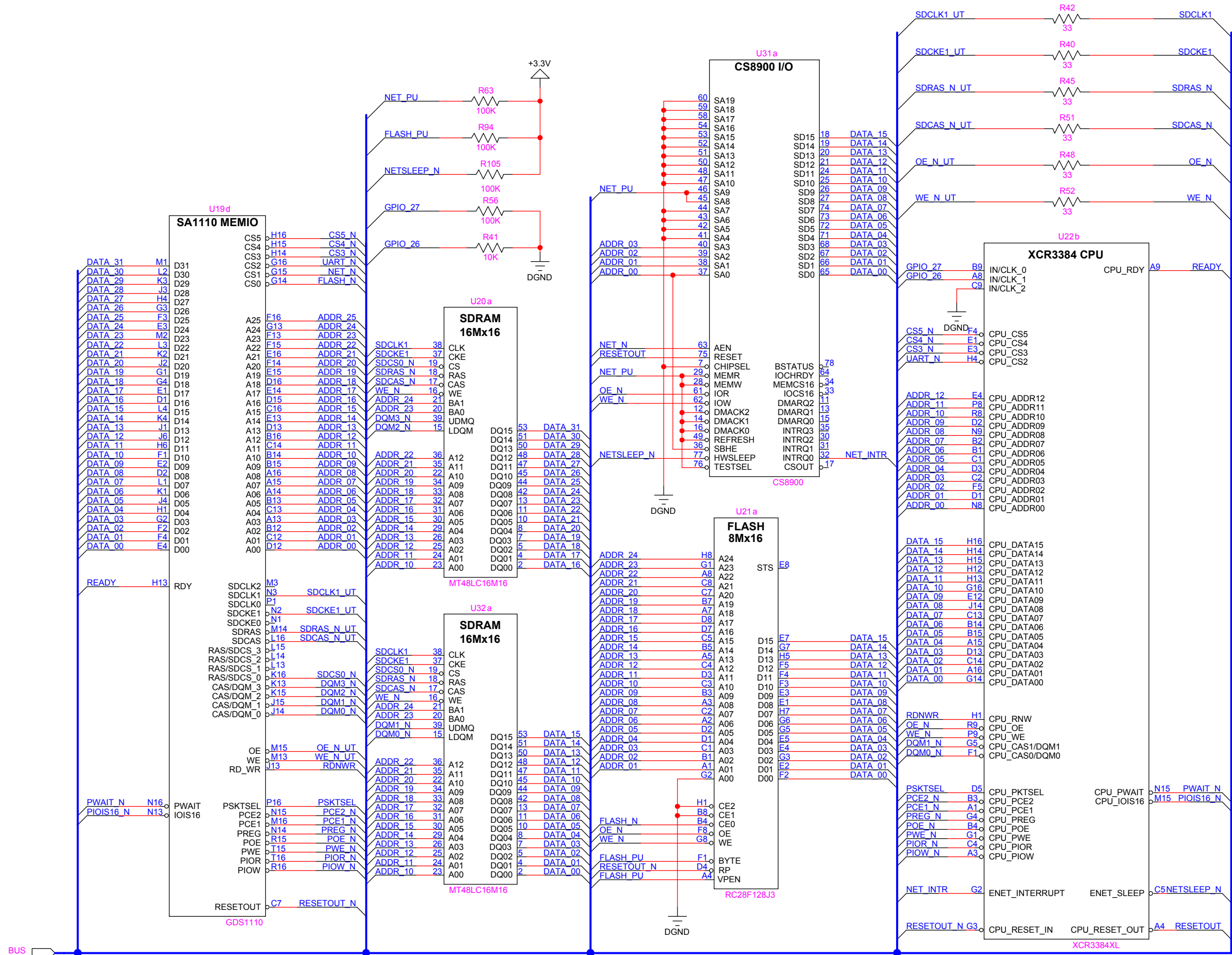
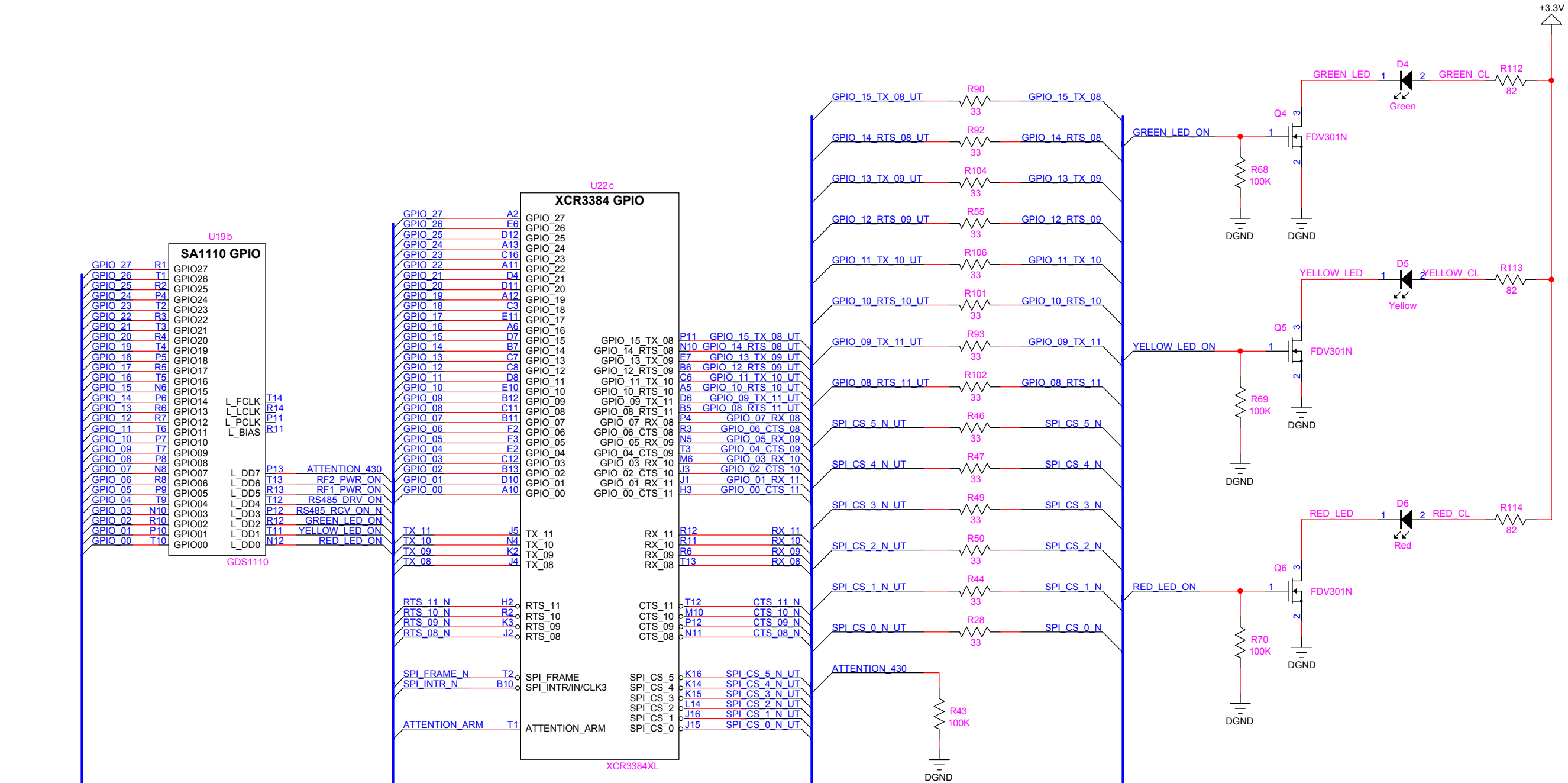


CPU & STORAGE

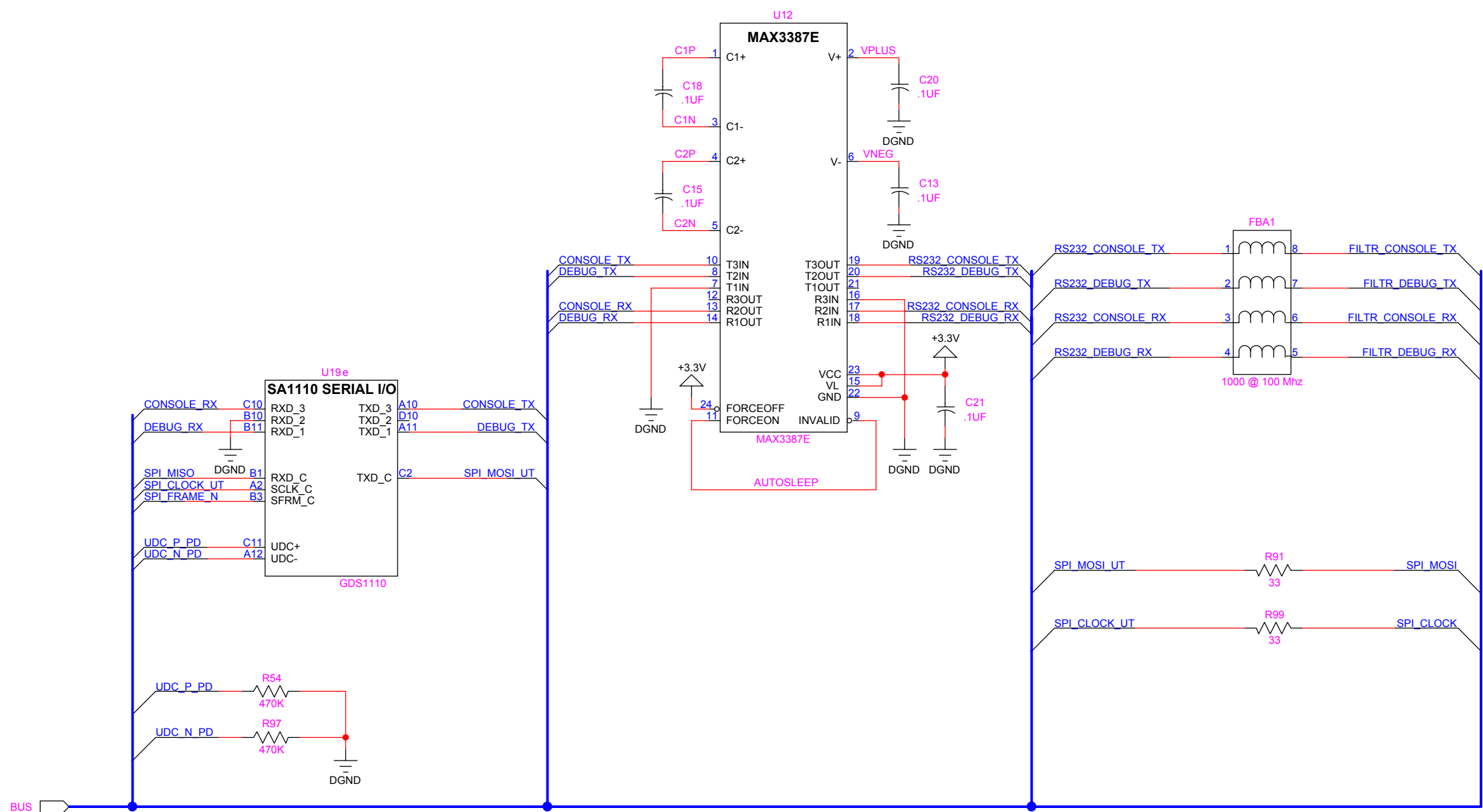


File:SideARM_3P1.cct
Last Mod:3/11/2003
Page: 1 OF 17
Copyright 2002, MBARI
Revision 3.1

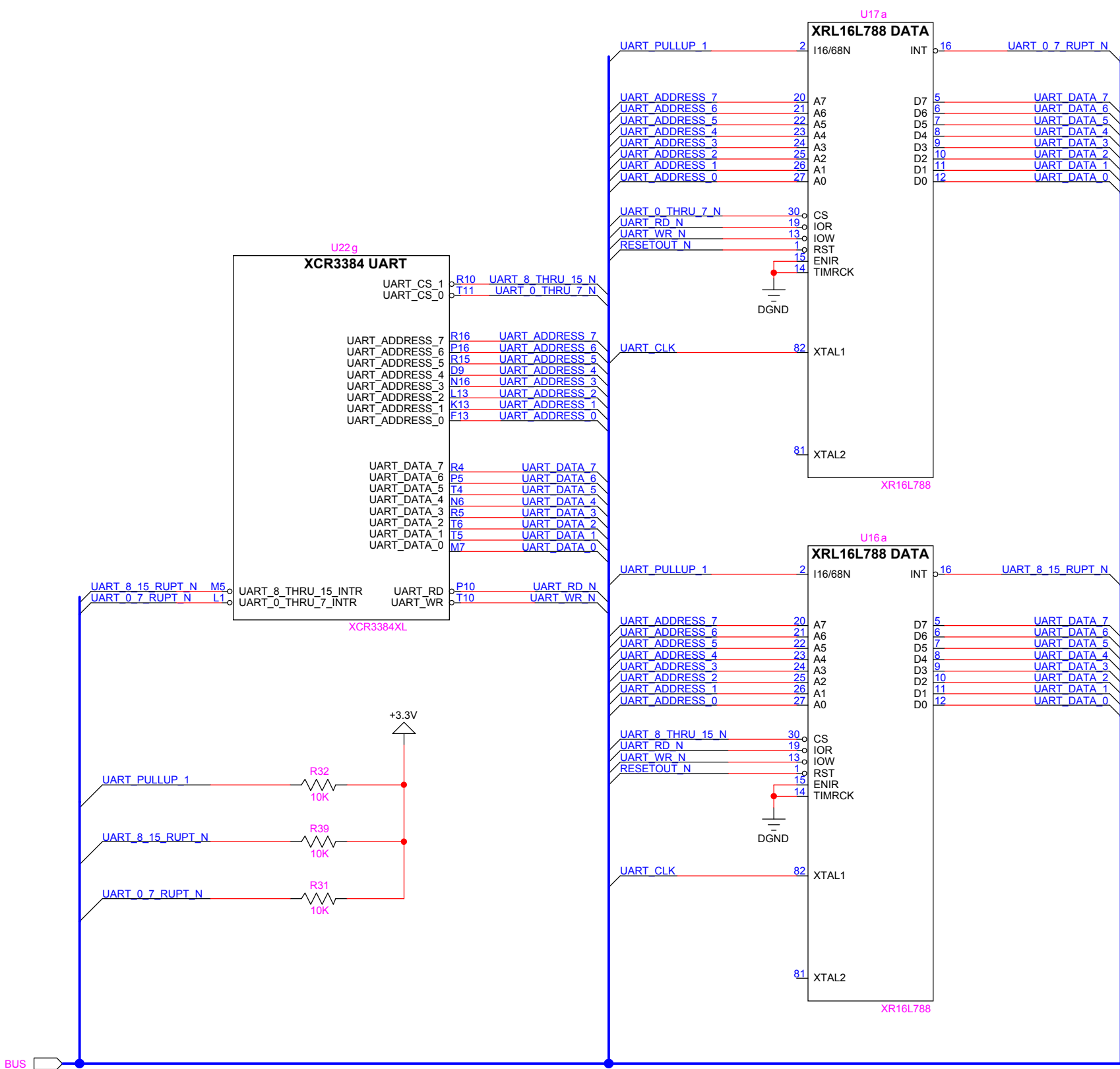
STRONGARM GPIO



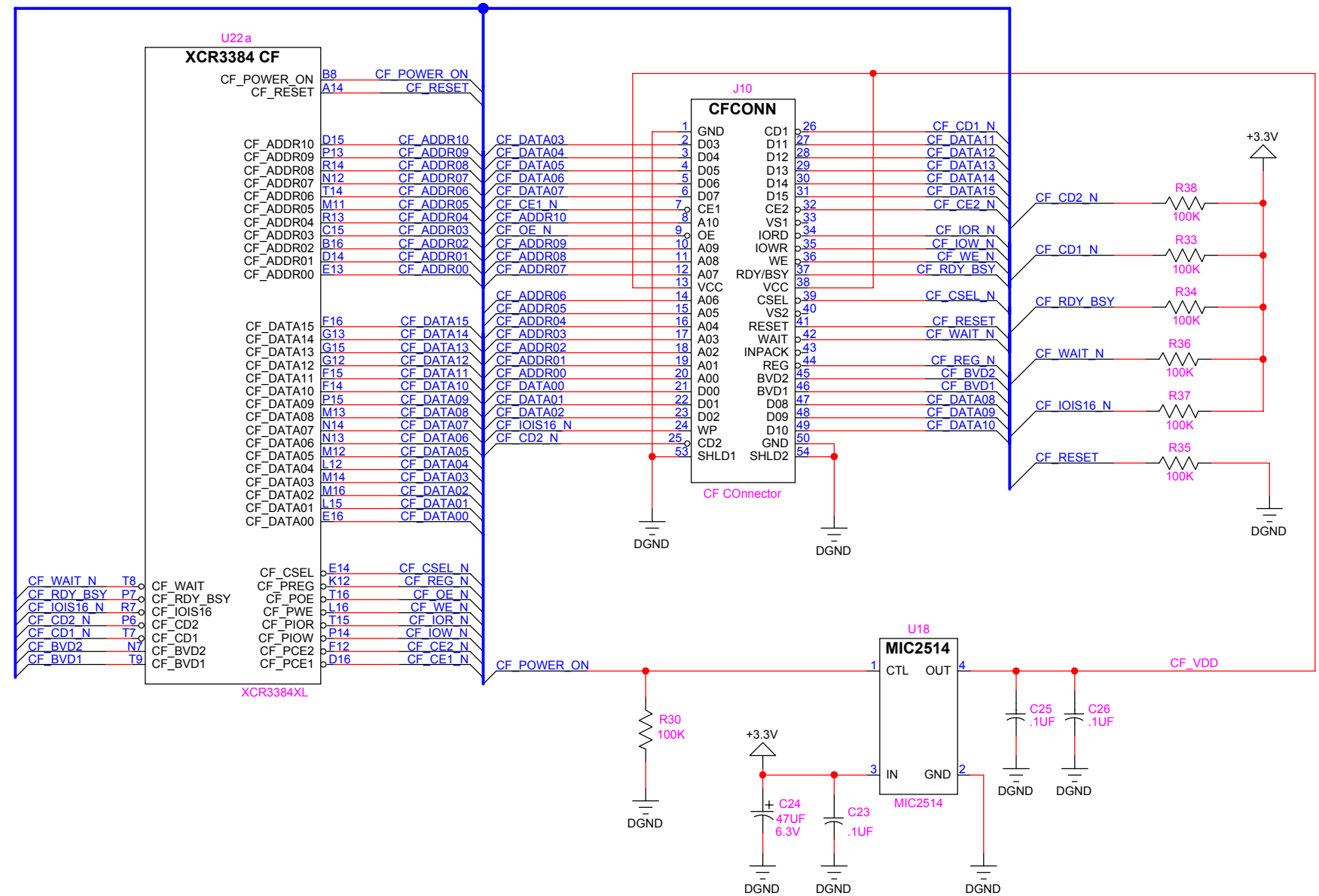
STRONGARM SERIAL I/O



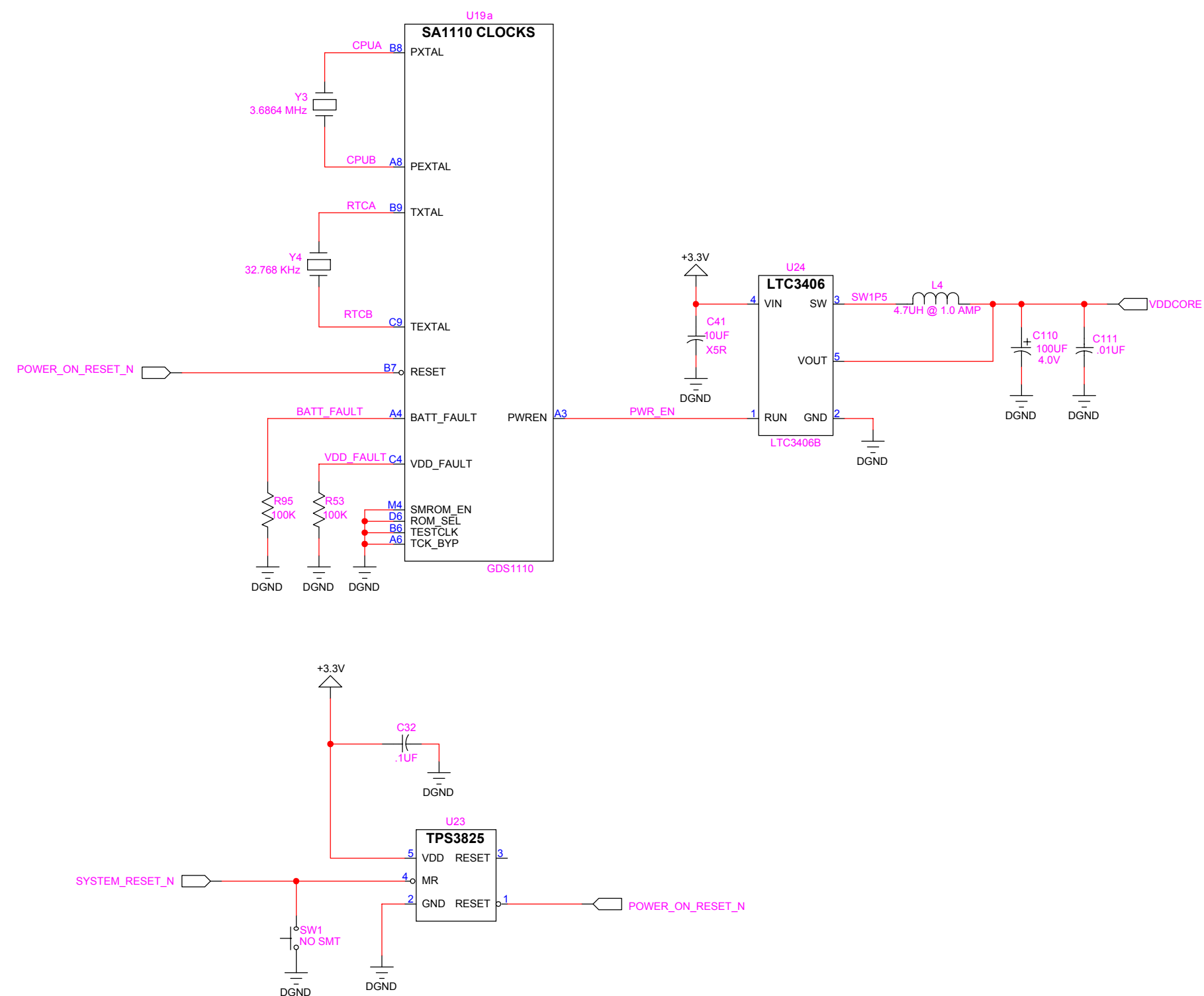
UART BUS INTERFACE



COMPACT FLASH

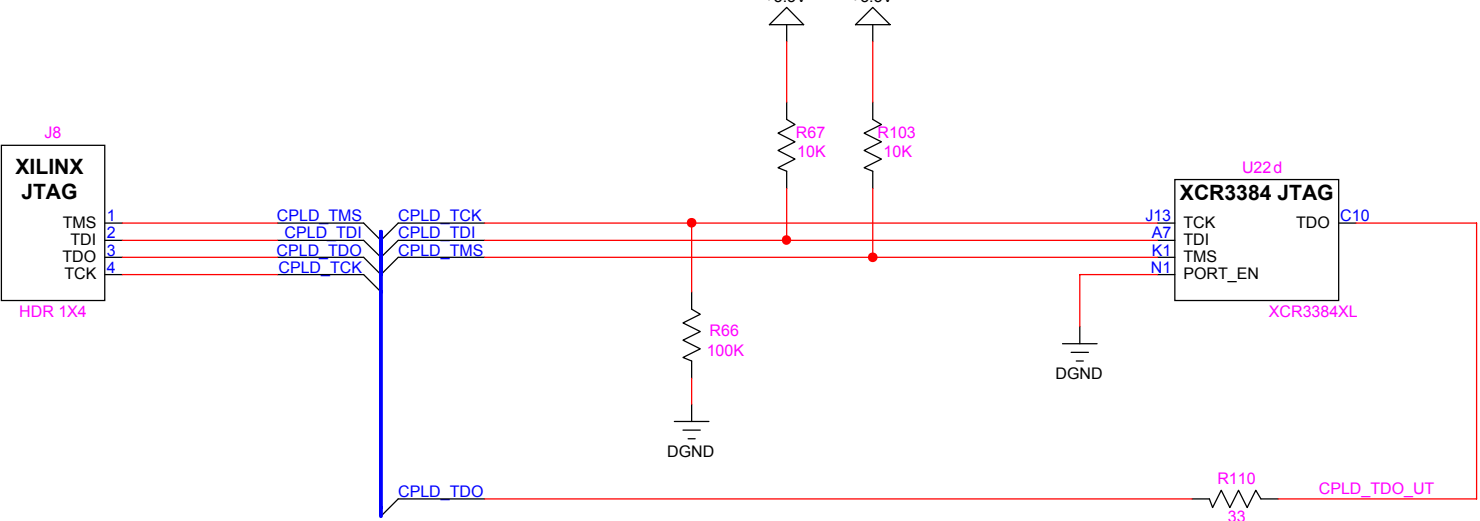
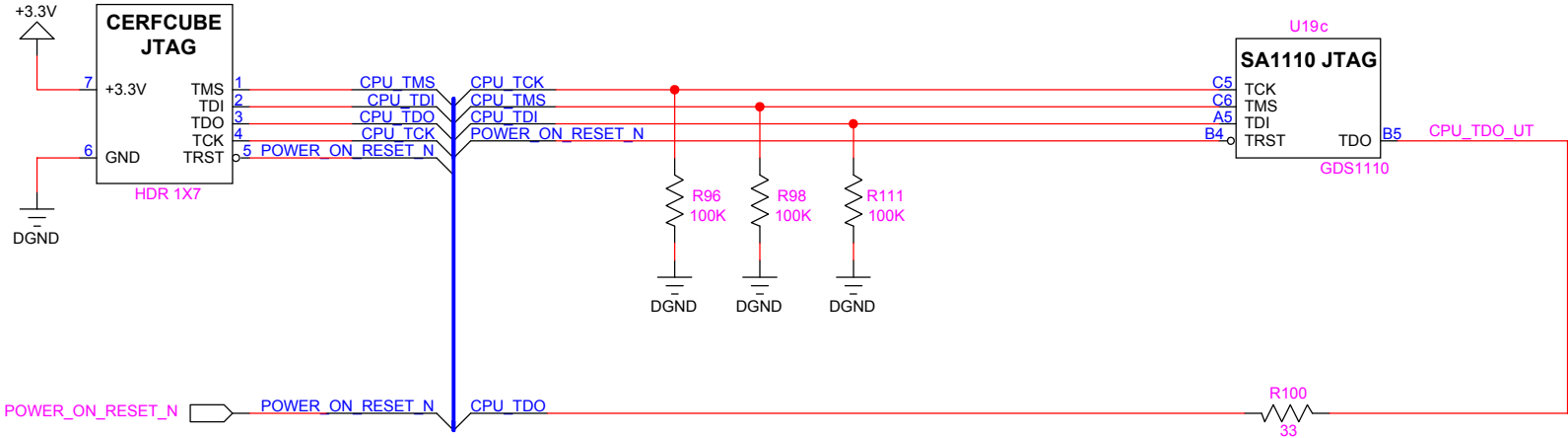


CLOCKS, RESET & CORE POWER

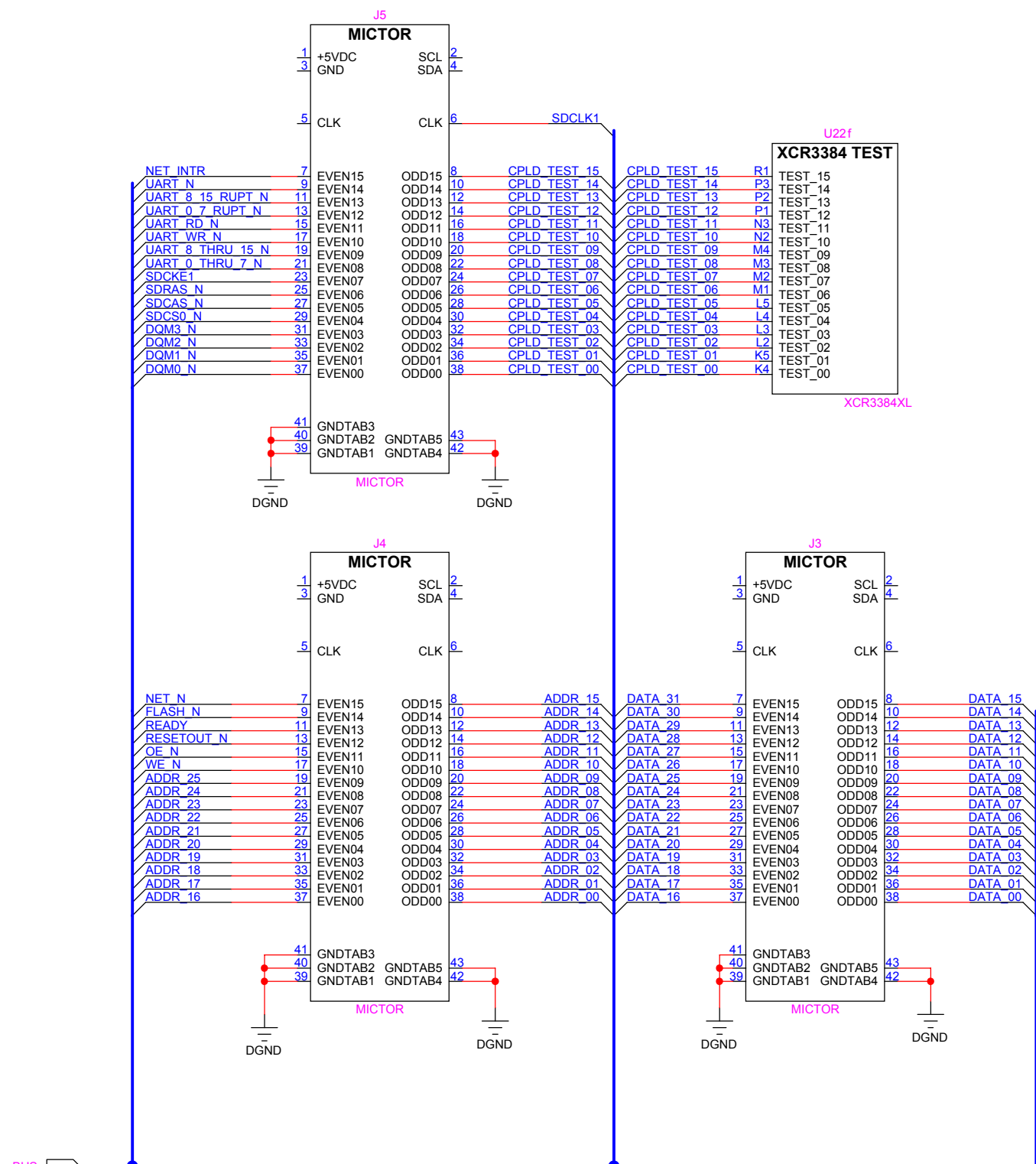


File:SideARM_3P1.cct
Last Mod:3/11/2003
Page: 6 OF 17
Copyright 2002, MBARI
Revision 3.1

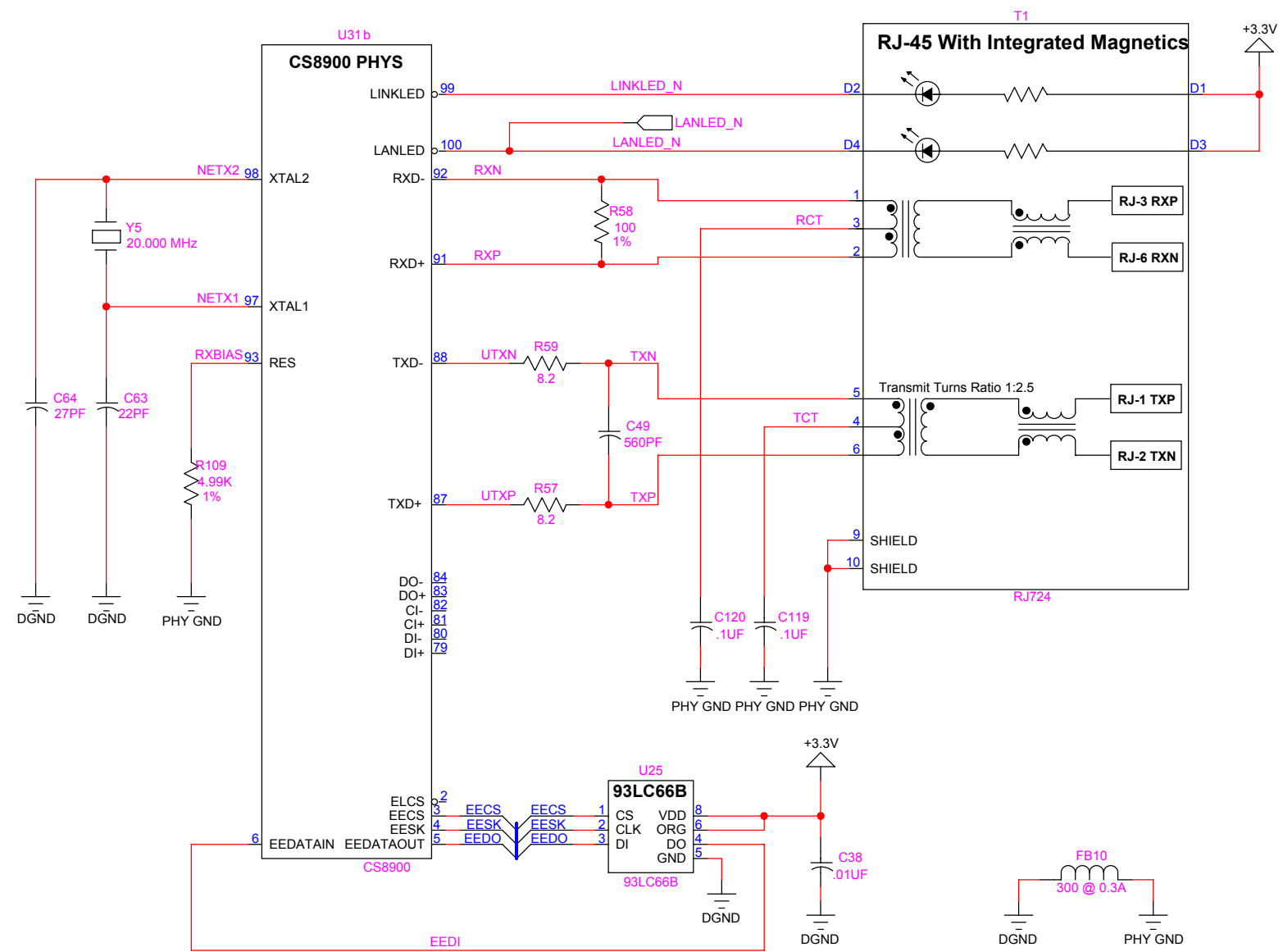
JTAG INTERFACE



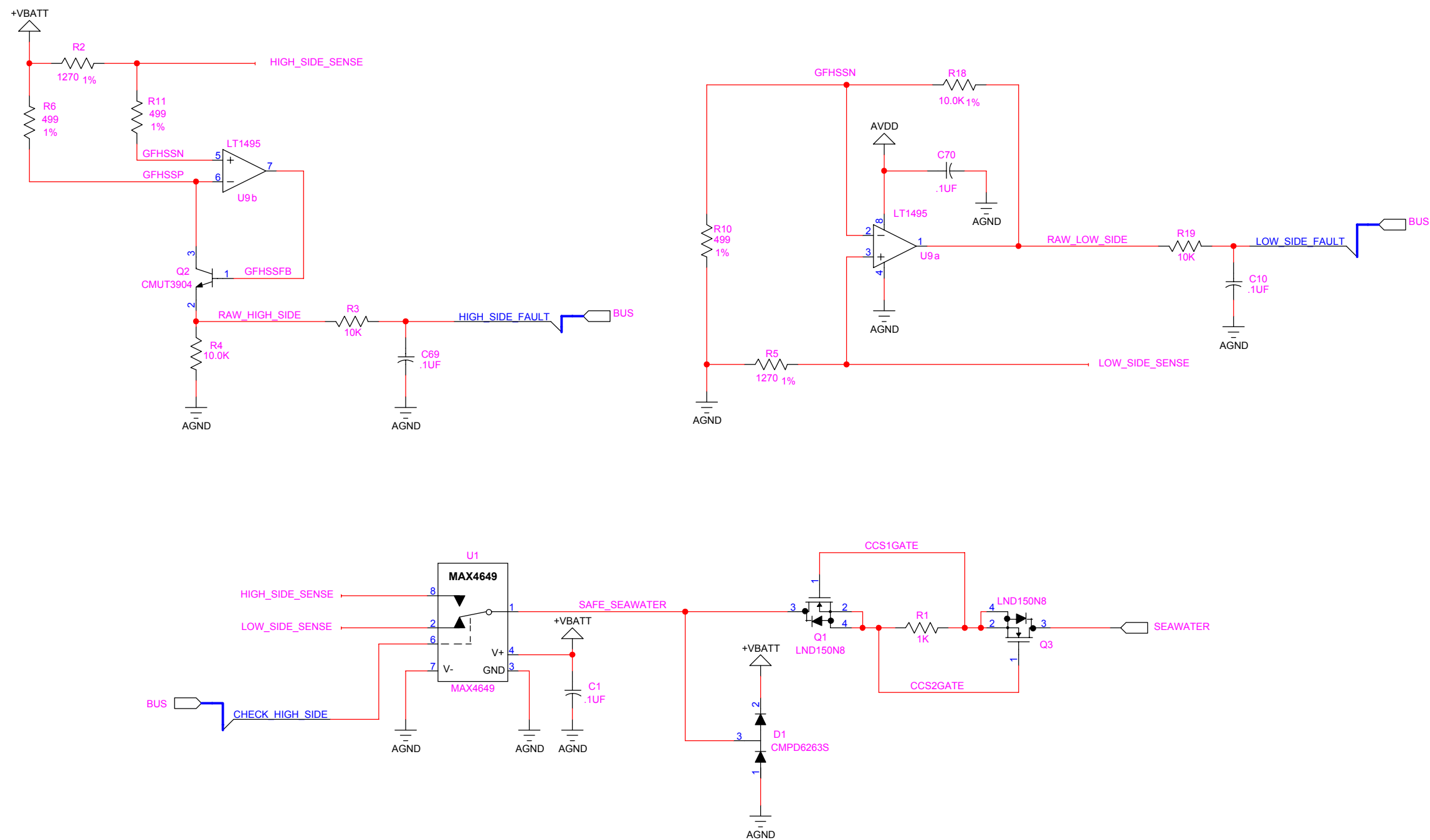
LOGIC ANALYZER TEST POINTS



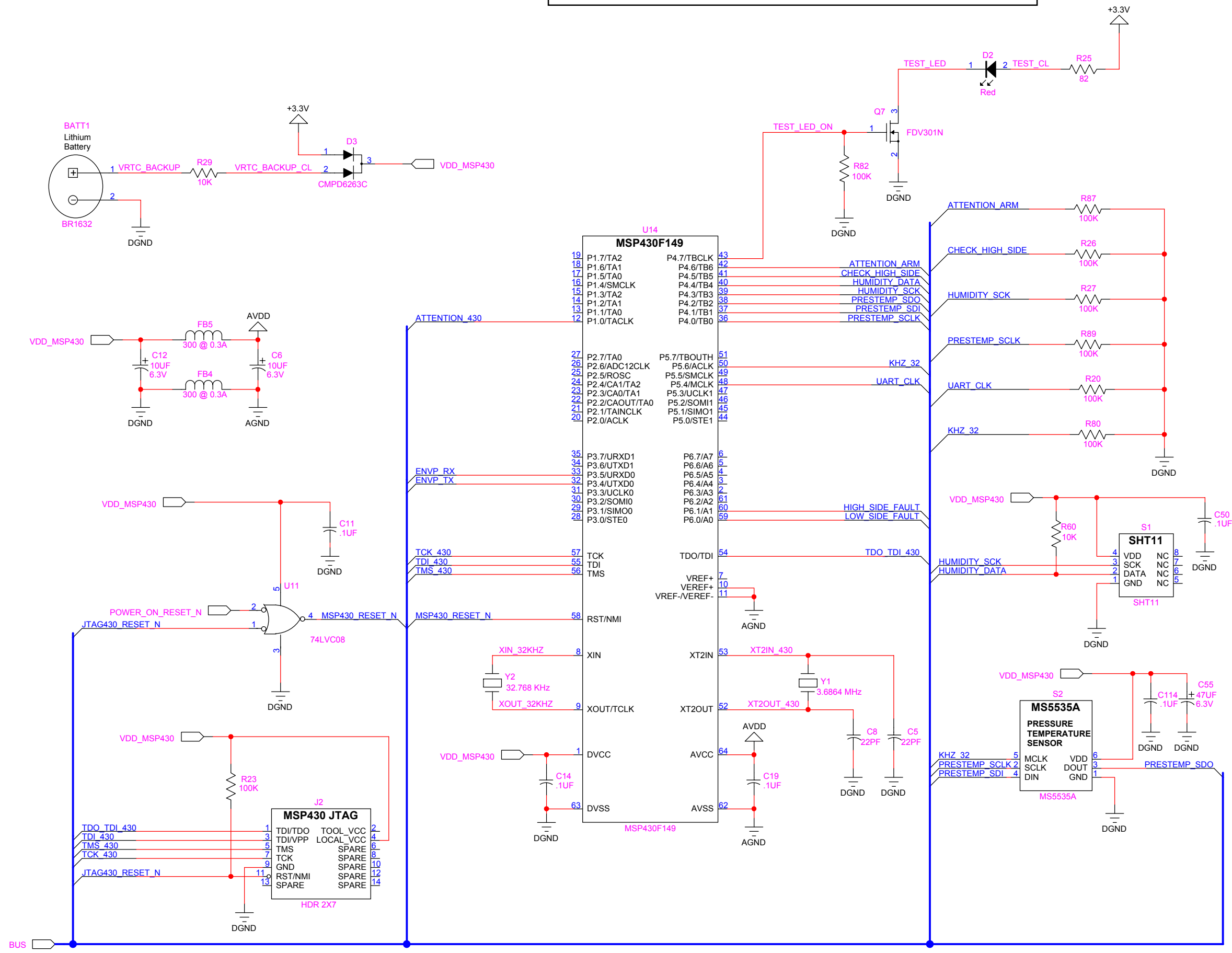
ETHERNET PHYSICAL INTERFACE



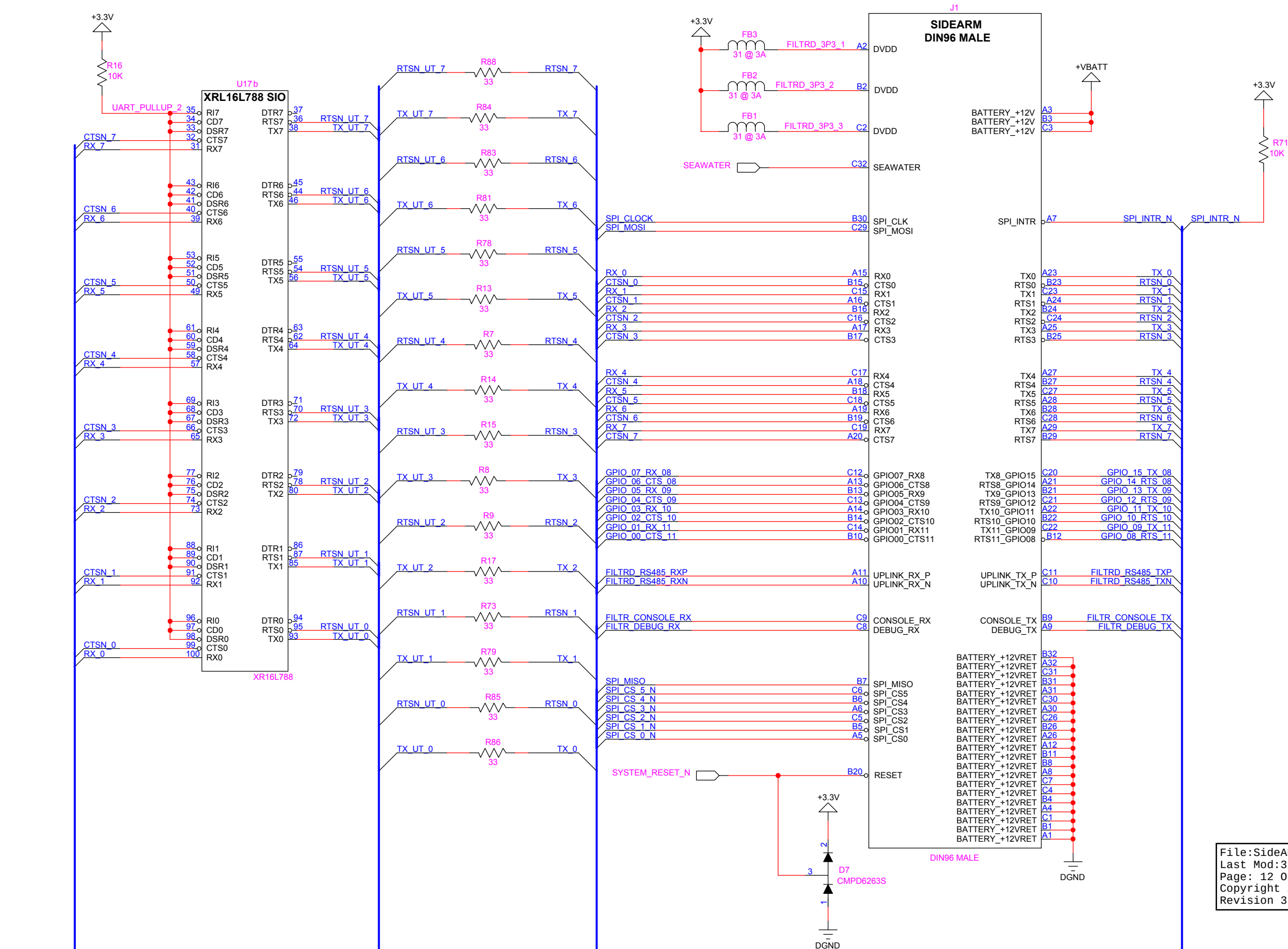
GROUND FAULT CURRENT MEASUREMENT



ENVIRONMENTAL MONITORING

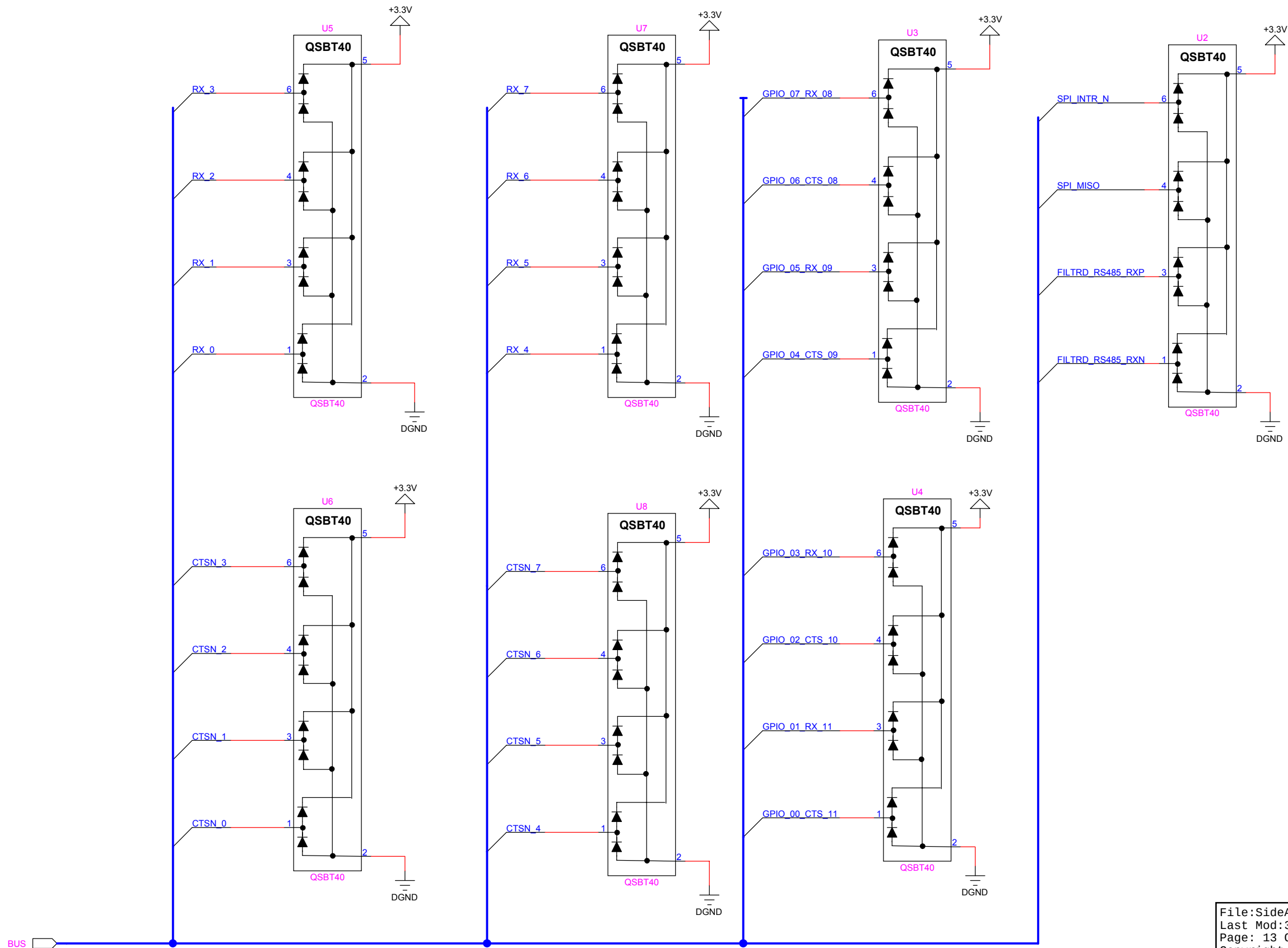


BACKPLANE CONNECTOR

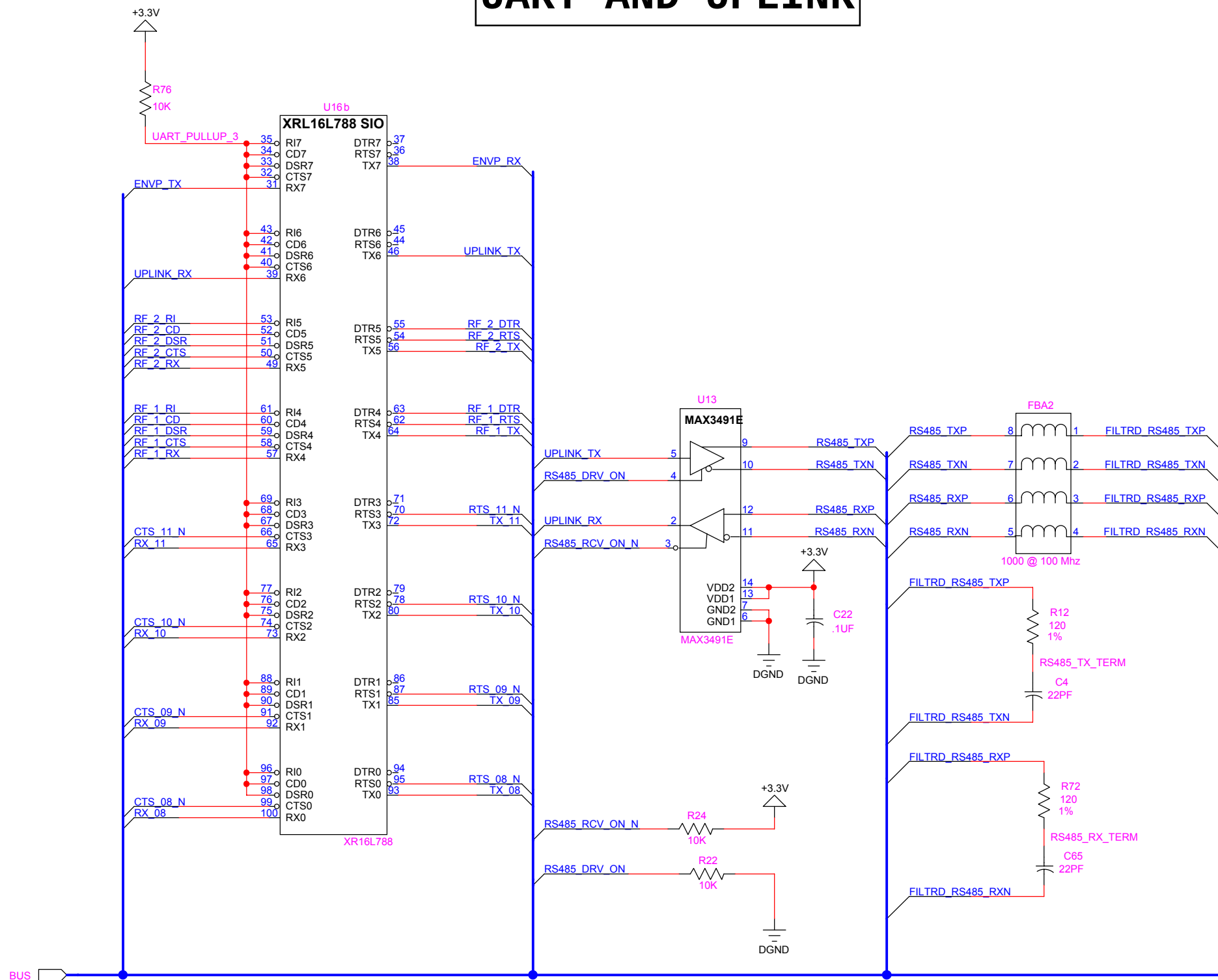


File:SideARM_3P1.cct
Last Mod:3/11/2003
Page: 12 OF 17
Copyright 2002, MBARI
Revision 3.1

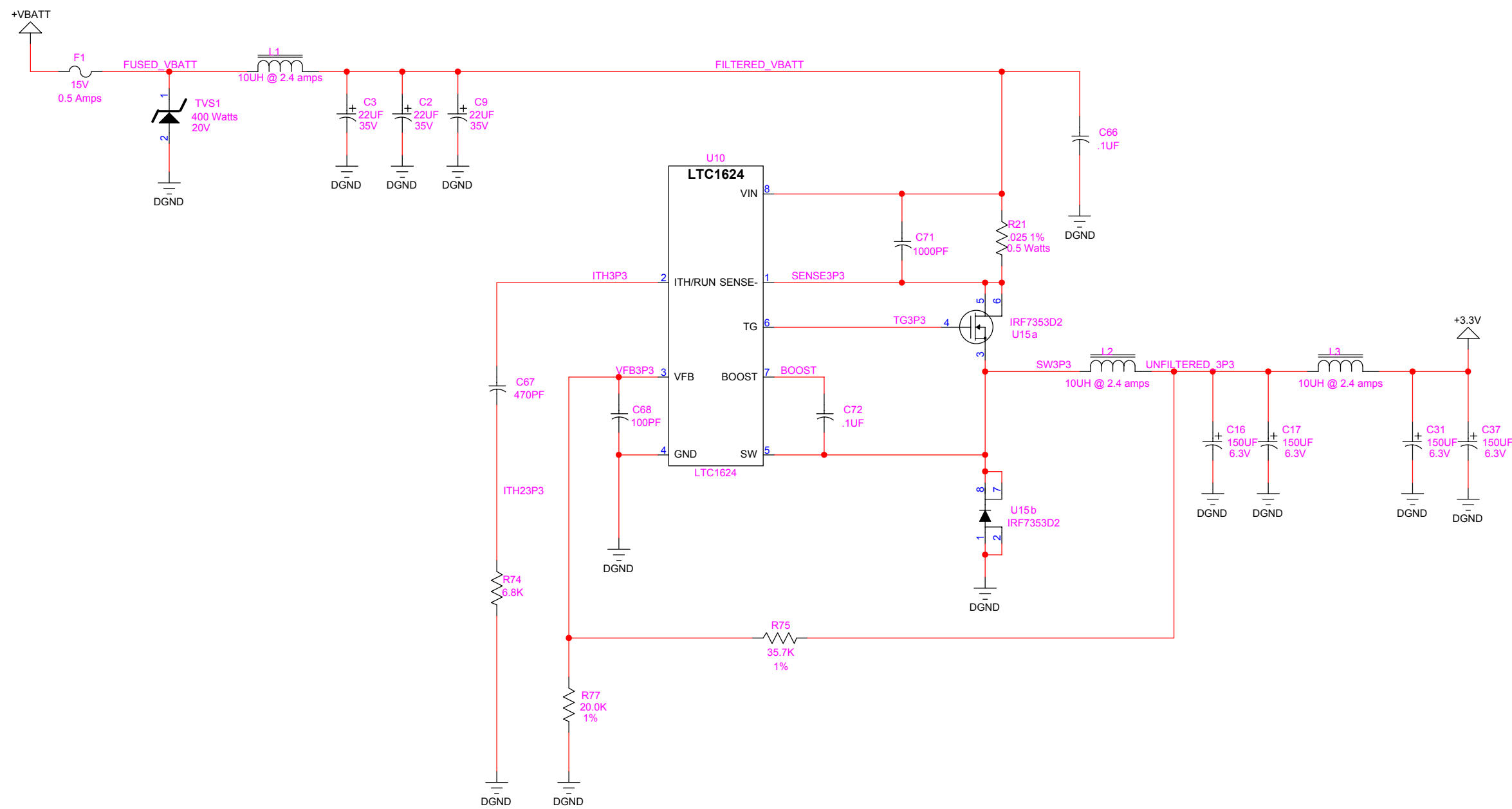
BACKPLANE TERMINATIONS



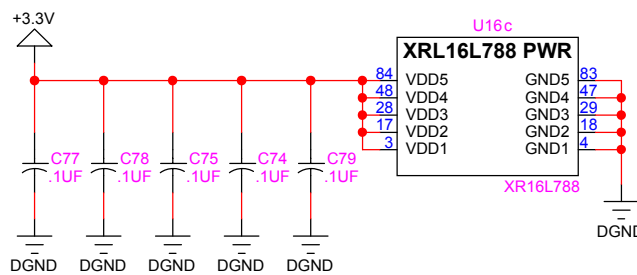
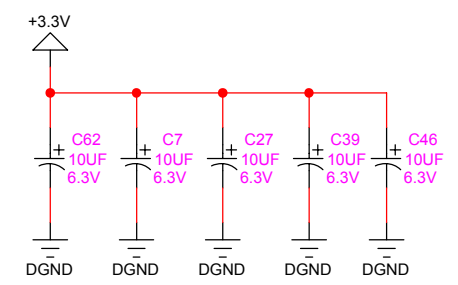
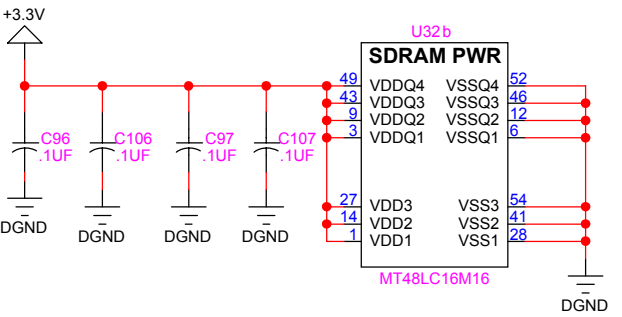
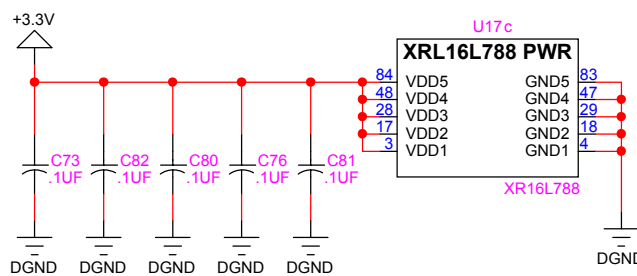
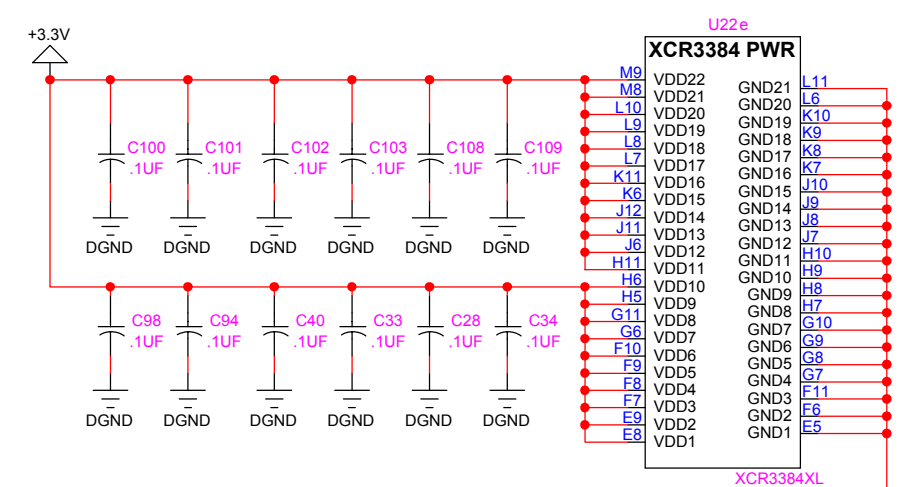
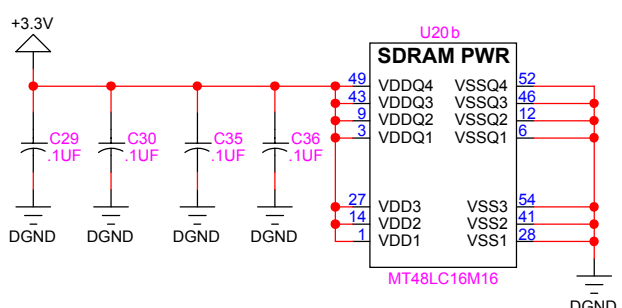
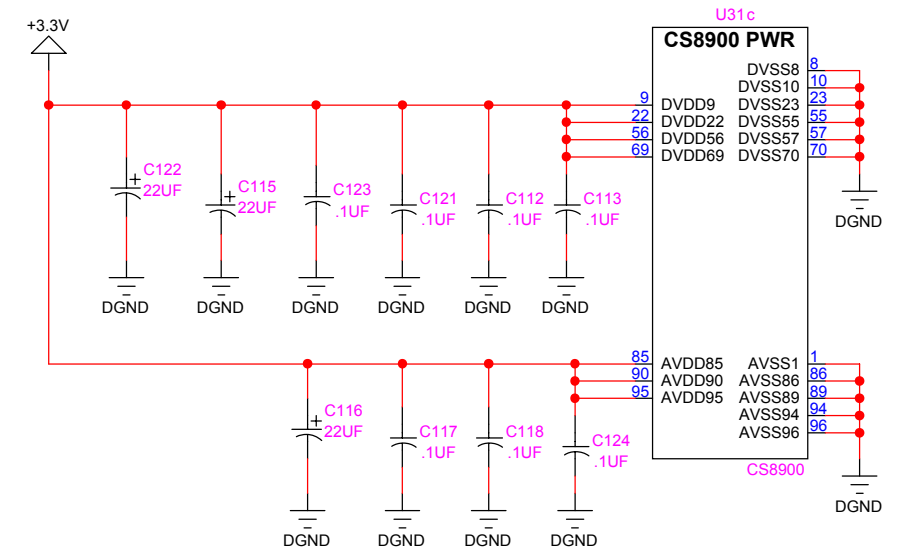
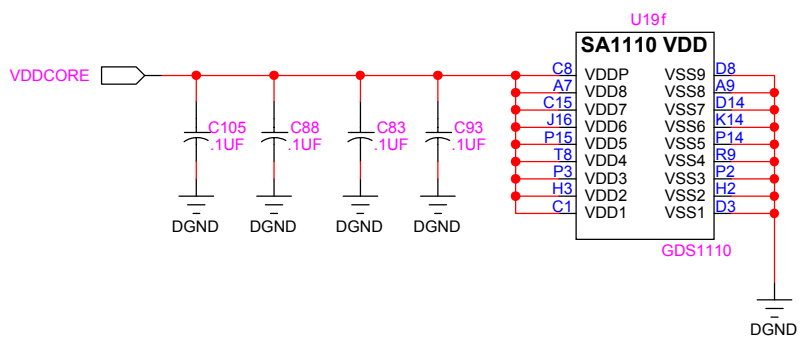
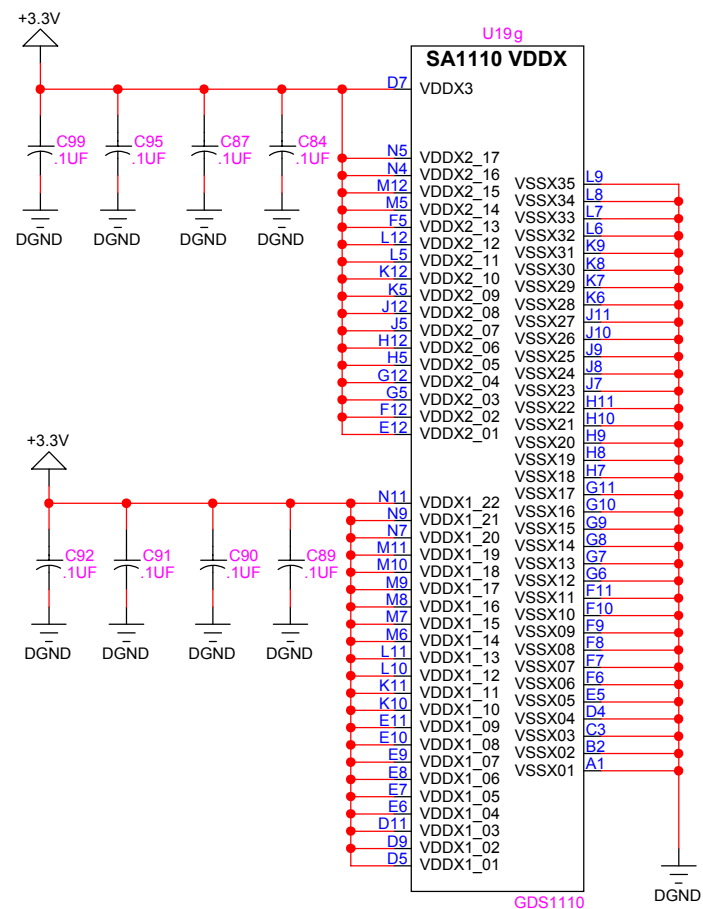
UART AND UPLINK



POWER SUPPLY



POWER DISTRIBUTION



RF MODEM INTERFACE

