SBC8260VE

Socket 370 Half-size All-in-One CPU Card with CRT/LCD and Ethernet

User's Manual

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Integrated circuits on computer boards are sensitive to static electricity. To avoid damaging chips from electrostatic discharge, observe the following precautions:

- Do not remove boards or integrated circuits from their anti-static packaging until you are ready to install them.
- Before handling a board or integrated circuit, touch an unpainted portion of the system unit chassis for a few seconds. This helps to discharge any static electricity on your body.
- Wear a wrist-grounding strap, available from most electronic component stores, when handling boards and components.

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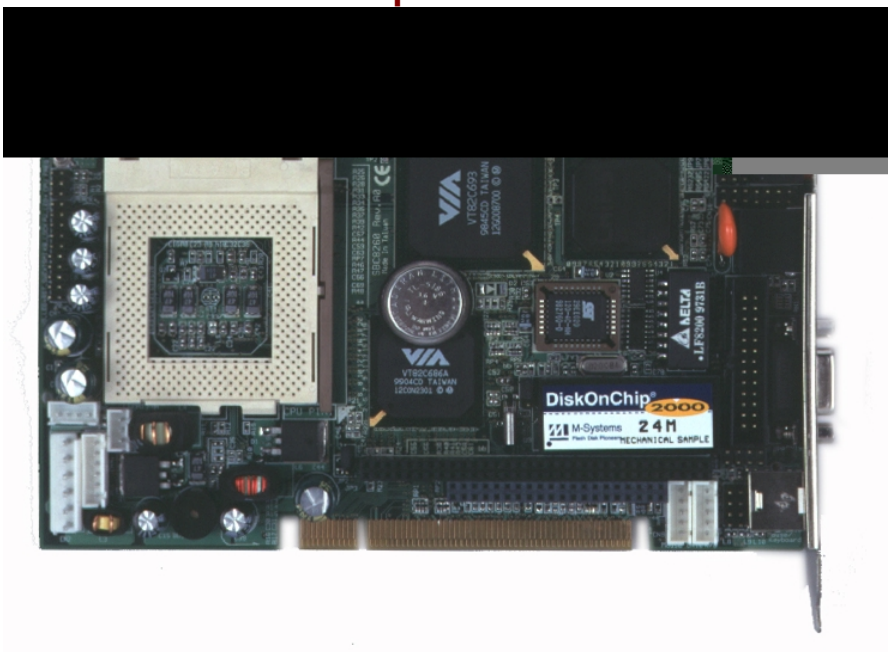
Chapter 1
Introduction

1.1 General Description

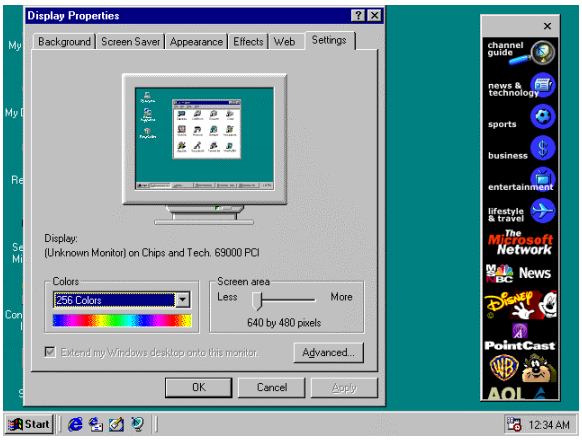


The **SBC8260VE** CPU card is an industrial grade CPU card incorporating the VIA Apollo Pro VT82C693A/Super South VT82C686B chipset and the Chips & Technologies C&T69000 Flat panel VGA controller, both ensuring its compatibility with PCI bus passive backplanes. Its 6-layer structure reduces signal noise and meets all green functions with its built-in power management feature. These advanced concepts along with the PICMG-compliant PCI Local Bus architecture brings outstanding performance to Windows-based applications.

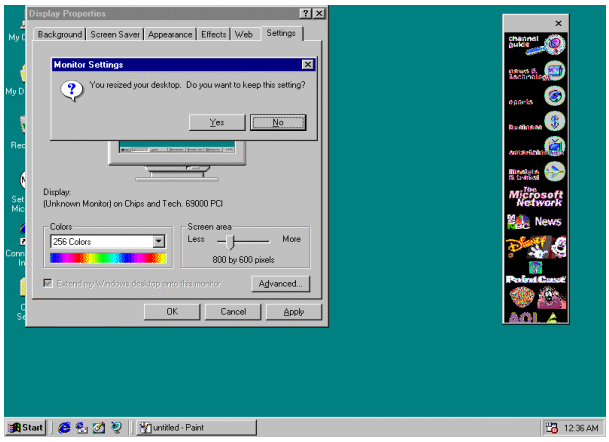
Designed for the professional embedded developers, the Socket 370 all-in-one **SBC8260VE** CPU card is virtually your ultimate one-step solution to various applications.

6.1.2 Changing Display Resolution

To change display resolution, click the right key of mouse at the Windows 98 desktop screen. Select "Properties", "Setting". You can select the resolution you want by changing the "Colors" and the Screen area".



If with a LCD panel display, please make sure the resolution can match the setting. After the setting, select"OK", "OK", and "Yes" to take the change affect.



6.2 Windows NT 3.5x

These drivers are designed to work with Microsoft Windows NT 3.5x

6.2.1 Driver Installation

Step 1 : Install Windows NT as you normally would do for a VGA display. Run Windows NT Setup from the Main Group. Choose the **Display** *option*. In the Display Settings dialog box, click on *Change Display Type*. Click on *Change* from the Adapter Type in the Display Type dialog box. Click on *Other* in the Select Device dialog box.

Step 2: Place the *Display Driver Disc* drive. Press **<ENTER>** and the name of the driver, *Chips and Technologies Video Controller* will appear highlighted in the Models list box. Click on ***INSTALL*** to install the selected driver. Once the installation is complete, the system must be shut down and restarted.

Step 3: Upon restart, at the **Invalid Display Selection** message, click on *OK* and select the desired display settings from the Display Settings dialog box. The system must be shut down and restarted for the new settings to take effect.

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Chapter 7
Award BIOS Utility

Chapter 7 describes the different settings available in the Award BIOS that comes with the **SBC8260VE** CPU card. Also contained here are instructions on how to set up the BIOS configuration.

7.1 BIOS Introduction

The Award BIOS (Basic Input/Output System) installed in your computer system's ROM supports Intel Celeron, Pentium III and VIA Cyrix III processors in a standard IBM-AT compatible I/O system. The BIOS provides critical low-level support for standard devices such as disk drives, serial and parallel ports. It also adds virus and password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

7.2 BIOS Setup

The Award BIOS provides a Setup utility program for specifying the system configurations and settings. The BIOS ROM of the system stores the Setup utility. When you turn ON the computer, the Award BIOS is immediately activated. Pressing the <Del> key immediately allows you to enter the Setup utility. If you are a little bit late pressing the <Del> key, POST (Power On Self Test) will continue with its test routines, thus preventing you from invoking the Setup. If you still wish to enter Setup, restart the system by pressing the "Reset" button or simultaneously pressing the <Ctrl>, <Alt> and <Delete> keys. You can also restart by turning the system OFF and back ON again. The following message will appear on the screen:

Press <DEL> to Enter Setup

In general, you press the arrow keys to highlight items, <Enter> to select, the <PgUp> and <PgDn> keys to change entries, <F1> for help and <Esc> to quit.

When you enter the Setup utility, the Main Menu screen will appear on the screen. The Main Menu allows you to select from various setup functions and exit choices.

ROM PCI/ISA BIOS
CMOS SETUP UTILITY
AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	SYSTEM HARDWARE MONITOR
BIOS FEATURES SETUP	INTEGRATED PERIPHERALS
CHIPSET FEATURES SETUP	SUPERVISOR PASSWORD
POWER MANAGEMENT SETUP	USER PASSWORD
PNP/PCI CONFIGURATION	IDE HDD AUTO DETECTION
LOAD BIOS DEFAULTS	SAVE & EXIT SETUP
LOAD SETUP DEFAULTS	EXIT WITHOUT SAVING
Esc : Quit	↑ ↓ → ← : Select Item
F10 : Save & Exit Setup	(Shift) F2 : Change Color

The section below the setup items of the Main Menu displays the control keys for this menu. Another section located at the bottom of the Main Menu, just below the control keys section, displays information on the currently highlighted item in the list.

NOTE: *If you find that your computer cannot boot after making and saving system changes with Setup, the Award BIOS, via its built-in override feature, resets your system to the CMOS default settings.*

We strongly recommend that you avoid making any changes to the chipset defaults. These defaults have been carefully chosen by both Award and your system manufacturer to provide the absolute maximum performance and reliability.

7.3 Standard CMOS Setup

"Standard CMOS Setup" allows you to record some basic hardware configurations in your computer system and set the system clock and error handling. If the motherboard is already installed in a working system, you will not need to select this option. You will need to run the Standard CMOS option, however, if you change your system hardware configurations, the onboard battery fails, or the configuration stored in the CMOS memory was lost or damaged.

ROM PCI/ISA BIOS
STANDARD CMOS SETUP
AWARD SOFTWARE, INC.

Date (mm:dd:yy) : Wed, Mar 4 1998
Time (hh:mm:ss) : 00 : 00 : 00

			CYLS	HEADS	PRECOMP	LANDZONE	SECTORS	MODE
Drive C :	AUTO	(0Mb)	0	0	0	0	0	AUTO
Drive D :	AUTO	(0Mb)	0	0	0	0	0	AUTO

Drive A :	1.44M,3.5 in	Base Memory :	640K
Drive B :	None	Extended Memory :	15360K
		Other Memory :	384K
Video :	: EGA / VGA		
Halt On :	: All Errors	Total Memory :	16384K

ESC : Quit	↑ ↓ → ← : Select Item	PU / PD / + / - : Modify
F1 : Help	(Shift) F2 : Change Color	

At the bottom of the menu are the control keys for use on this menu. If you need any help in each item field, you can press the <F1> key. It will display the relevant information to help you. The memory display at the lower right-hand side of the menu is read-only. It will adjust automatically according to the memory changed. The following pages describe each item of this menu.

- **Date**
  The date format is:

Day	the day of week, from Sun to Sat, determined by the BIOS, is read only
Month	the month, Jan (1) through Dec (12)
Date	the date, from 1 to 31 (or the maximum allowed in the month), can key in the numerical / function key
Year	the year, from 1994 to 2079

  To set the date, highlight the "Date" field and use the PageUp/ PageDown or +/- keys to set the current time.

- **Time**
  The time format is:

Hour	From 00 to 23
Minute	From 00 to 59
Second	From 00 to 59

  To set the time, highlight the "Time" field and use the <PgUp>/ <PgDn> or +/- keys to set the current time.

- **Drive C/D**
  The onboard PCI IDE connector provides one channel for connecting up to four IDE hard disks or other IDE devices. It can support up to two hard disks.
  To enter the specifications for a hard disk drive, you must select first a "Type". There are 45 predefined types and 4 user definable types are for Enhanced IDE BIOS. Types 1 to 45 are predefined. Type "User" is user-definable. You can select "Auto" under the TYPE and MODE fields. This will enable auto detection of your IDE drives and CD-ROM drive during POST.
  Press <PgUp>/<PgDn> to select a numbered hard disk type or type the number and press the <Enter> key. The hard disk will not work properly if you enter incorrect information for this field. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually. If you select Type User, the utility will ask you to enter the information on the following table.

CYLS	number of cylinders
HEAD	number of read/write heads
PRECOMP	write precompensation
LANDZ	landing zone
SECTOR	number of sectors
SIZE	Automatically adjust according to the configuration
MODE (for IDE HDD only):	Auto Normal (HD < 528MB) Large (for MS-DOS only) LBA (HD > 528MB and supports Logical Block Addressing)

NOTE: *The specifications of your drive must match with the drive table. The hard disk will not work properly if you enter incorrect information in these fields. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually.*

- **Drive A / Drive B**
  These fields identify the types of floppy disk drive A or drive B that has been installed in the computer. The available specifications are:

360K, 5.25 in	5.25 inch PC-type standard drive; 360Kb capacity
1.2M, 5.25 in	5.25 inch AT-type high-density drive; 1.2MB capacity
720K, 3.5 in	3.5 inch double-sided drive; 720Kb capacity
1.44M, 3.5 in	3.5 inch double-sided drive; 1.44MB capacity
2.88M, 3.5 in	3.5 inch double-sided drive; 2.88MB capacity

- **Video**
  This field selects the type of video display card installed in your system. You can choose the following video display cards:

EGA/VGA	Enhanced Graphics Adapter/Video Graphics Array. For EGA, VGA, SEGA, SVGA or PGA monitor adapters. (default)
CGA 40	Color Graphics Adapter, power up in 40 column mode
CGA 80	Color Graphics Adapter, power up in 80 column mode
MONO	For Hercules or MDS adapters, includes high resolution monochrome adapters

- **Halt On**
  This field determines whether the system will halt if an error is detected during power up.

No errors	The system boot will halt on any error detected.
All errors	Whenever the BIOS detects a non-fatal error, the system will stop and you will be prompted.
All, But Keyboard	The system boot will not stop for a keyboard error; it will stop for all other errors.
All, But Diskette	The system boot will not stop for a disk error; it will stop for all other errors.
All, But Disk/Key	The system boot will not stop for a keyboard or disk error; it will stop for all other errors.

7.4 BIOS Features Setup

This section allows you to configure and improve your system and allows you to set up some system features according to your preference.

ROM / PCI ISA BIOS
BIOS FEATURES SETUP
AWARD SOFTWARE, INC.

Virus Warning	: Disabled	Video BIOS Shadow	: Enabled
CPU Internal Cache	: Enabled	C8000-CBFFF Shadow	: Disabled
External Cache	: Enabled	CC000-CFFFF Shadow	: Disabled
CPU L2 Cache ECC Checking	: Enabled	D0000-D3FFF Shadow	: Disabled
		D4000-D7FFF Shadow	: Disabled
Quick Power On Self Test	: Disabled	D8000-DBFFF Shadow	: Disabled
Boot Sequence	: C,A,SCSI	DC000-DFFF Shadow	: Disabled
Swap Floppy Drive	: Disabled		
Boot Up Floppy Seek	: Disabled		
Boot Up NumLock Status	: On		
IDE HDD Block Mode	: Enabled	LCD Type : (Type 6) 640x480 24 TFT	
Gate A20 Option	: Fast	VGA Expansion(Full Screen)	: Disable
Memory Parity/ECC Check	: Disabled	Display Type During POST	: CRT Only
Typematic Rate Setting	: Disabled	Display Type After POST	: CRT Only
Typematic Rate (Chars/Sec)	: 6		
Typematic Delay (Msec)	: 250	ESC : Quit	↑ ↓ → ← : Select Item
Security Option	: Setup	F1 : Help	PU/PD/+/- : Modify
PCI /VGA Palette Snoop	: Disabled	F5 : Old Values	(Shift) F2 : Color
OS Select For DRAM>64MB	: Non-OS2	F6 : Load BIOS Defaults	
		F7 : Load Setup Defaults	

- **Virus Warning**
  This item protects the boot sector and partition table of your hard disk against accidental modifications. If an attempt is made, the BIOS will halt the system and display a warning message. If this occurs, you can either allow the operation to continue or run an anti-virus program to locate and remove the problem.

  NOTE: *Many disk diagnostic programs, which attempt to access the boot sector table, can cause the virus warning. If you will run such a program, disable the Virus Warning feature.*

- **CPU Internal Cache / External Cache**
  Cache memory is additional memory that is much faster than conventional DRAM (system memory). CPUs from 486-type on up contain internal cache memory, and most, but not all, modern PCs have additional (external) cache memory. When the CPU requests data, the system transfers the requested data from the main DRAM into cache memory, for even faster access by the CPU. These items allow you to enable (speed up memory access) or disable the cache function. By default, these are ***Enabled***.

- **CPU L2 Cache ECC Checking**
  When enabled, this allows ECC checking of the CPU's L2 cache. By default, this field is ***Enabled***.

- **Quick Power On Self Test**
  When enabled, this field speeds up the Power On Self Test (POST) after the system is turned ON. If it is set to *Enabled*, BIOS will skip some items.

- **Boot Sequence**
  This field determines the drive that the system searches first for an operating system. The available options are:

  - A, C, SCSI
  - C, A, SCSI
  - C, CDROM, A
  - CDROM, C, A
  - D, A, SCSI
  - SCSI, C, A
  - C only
  - LS/ZIP, C
  - SCSI, A, C

  The default value is ***A, C, SCSI***.

- **Swap Floppy Drive**
  This allows you to determine whether to enable Swap Floppy Drive or not. When enabled, the BIOS swaps floppy drive assignments so that Drive A becomes Drive B, and Drive B becomes Drive A. By default, this field is set to ***Disabled.***

- **Boot Up Floppy Seek**
  When enabled, the BIOS seeks for number of track (40 or 80) of the installed floppy drive. 360K type has 40 tracks while 760K, 1.2M and 1.44M have 80 tracks. By default, this field is set to ***Enabled.***

- **Boot Up NumLock Status**
  This activates the NumLock function after powering up the system. By default, the system boots up with ***NumLock*** ON.

- **IDE HDD Block Mode**
  This field allows your hard disk controller to use the fast block mode to transfer data to and from your hard disk drive.

- **Gate A20 Option**
  This you to select how Gate A20 is worked. Gate A20 is a device used to address memory above 1 MB. The default setting is ***Fast.***

- **Memory Parity Check/ECC Check**
  This option enables or disables parity error checking for system RAM. The settings are *Enabled* (all system RAM parity is checked) and *Disabled*.

- **Typematic Rate Setting**
  When disabled, continually holding down any key on your keyboard will generate only one instance. When enabled, you can set the two typematic controls listed next. By default, this field is set to ***Disabled.***

- **Typematic Rate (Chars/Sec)**
  When the typematic rate is enabled, the system registers repeated keystrokes speeds. You can select speed range from 6 to 30 characters per second. By default, this item is set to ***6.***

- **Typematic Delay (Msec)**
  When typematic rate is enabled, this item allows you to set the time interval between the display of the first and second characters. By default, this item is set to ***250msec***.

- **Security Option**
  This allows you to limit access to the System and Setup. The default value is ***Setup***. When set to *System,* the system prompts for the User Password every boot up. Selecting *Setup* always boots up and prompts for Supervisor Password only when Setup utility is called up.

- **PCI/VGA Palette Snoop**
  Some non-standard VGA display cards may not show colors properly. This field allows you to set whether MPEG ISA/VESA VGA Cards can work with PCI/VGA or not. When enabled, a PCI/VGA can work with a MPEG ISA/VESA VGA card. When disabled, a PCI/VGA cannot work with a MPEG ISA/VESA Card.

- **OS Select for DRAM > 64MB**
  This allows system to access more than 64MB of DRAM memory when used with OS/2 depends on certain BIOS calls to access memory. The default setting is ***Non-OS/2***.

- **Video BIOS Shadow**
  This item allows you to change the Video BIOS location from ROM to RAM. Video Shadow will increase the video speed.

- **C8000 - CBFFF Shadow/DC000 - DFFFF Shadow**
  Shadowing ROM reduces available memory between 640KB and 1024KB. These fields determine whether optional ROM is copied to RAM or not.

7.4.1 Onboard VGA Functions

The following options allow the overriding of the VGA BIOS settings integrated in the core chipset of **SBC8260VE**. Take note that the settings on the following will not take effect when a VGA peripheral card is connected to the system.

- **LCD Type**
  Although the system chipset already supports VGA and LCD alike, BIOS Features Setup still expands this feature further with the option to set any of the 15 LCD types. The available options are:

■ Type 1/13	1024 x 768	DSTN
■ Type 2	1280 x 1024	TFT
■ Type 3	640 x 480	DSTN
■ Type 4	800 x 600	DSTN
■ Type 5	640 x 480 (16bit)	TFT
■ Type 6	640 x 480 (24bit)	TFT
■ Type 7	1024 x 768	TFT
■ Type 8/9/10	800 x 600	TFT
■ Type 11/12	800 x 600	DSTN
■ Type 14	1280 x 1024	DSTN
■ Type 15	1024 x 600	DSTN

- **VGA Expansion (Full Screen)**
  This option allows you to enlarge application screens to full screen scale onto your display. However this option is not inversely compatible, rendering no change when applications screens have resolutions larger than the display resolution. The available options are Enabled and Disabled.

- **Display Type During/After POST**
  This item configures the viewing area for the POST sequence. When configured to the *incorrect display setting* or *Both*, it blankets the POST sequence from being viewed. If you select *Default*, this option is useless and follows the VGA BIOS settings. The available options are CRT Only, LCD Only, Both, and Default.

7.5 Chipset Features Setup

This Setup menu controls the configuration of the motherboard chipset.

ROM PCI/ISA BIOS
CHIPSET FEATURES SETUP
AWARD SOFTWARE, INC.

Bank 0/1 DRAM Timing	: SDRAM 10ns	Current CPU Temp.	
		Current System Temp.	
SDRAM Cycle Length	: 3	Current CPUFAN1 Speed	
DRAM Clock	: 66 MHz		
Memory Hole	: Disabled	Vcore	2.5V
Read Around write	: Disabled	3.3V	5V
Concurrent PCI/Host	: Disabled	12V	
System BIOS Cacheable	: Disabled		
Video RAM Cacheable	: Disabled		
AGP Aperture Size	: 64		
OnChip USB	: Enabled		
USB Keyboard Support	: Disabled		
		ESC : Quit	↑ ↓ → ← : Select Item
		F1 : Help	PU/PD/+/- : Modify
		F5 : Old Values	(Shift)F2 : Color
		F6 : Load BIOS Defaults	
		F7 : Load Setup Defaults	

- **BANK0/1 DRAM Timing**
  This allows you to select the value of the board's paged DRAMs or EDO (extended data output) DRAMs. The available choices are EDO 50ns, EDO 60ns, Slow, Medium, Fast, and Turbo.

- **SDRAM Cycle Length**
  When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing. Do not reset this field from the default value specified by the system designer. The available choices are 2 and 3.

- **DRAM Clock**
  This item allows you to select the DRAM clock value, depending on whether the board has paged DRAMs or EDO (extended data output) DRAMs. The available choices are 66 MHz and Host CLK.

- **Memory Hole**
  To improve performance, certain space in memory is reserved for ISA cards. This memory must be mapped into the memory space below 16MB. The available choices are 15M-16M and Disabled.

- **Read Around write**
  DRAM optimization feature: If a memory read is addressed to a location whose latest write is being held in a buffer before being written to memory, the read is satisfied through the buffer contents, and the read is not sent to the DRAM. The available choices are Enabled and Disabled.

- **Concurrent PCI/Host**
  When disabled, CPU bus will be occupied during the entire PCI operation period. The available choices are Enabled and Disabled.

- **System BIOS Cacheable**
  When enabled, access to the system BIOS ROM addressed at F0000H-FFFFFH is cached, provided that the cache controller is disabled.

- **Video RAM Cacheable**
  Selecting *Enabled* allows caching of the video BIOS ROM at C0000h to C7FFFh, resulting in better video performance. However, if any program writes to this memory area, a memory access error may result.

- **AGP Aperture Size**
  The field sets aperture size of the graphics. The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation. The options available are 4M, 8M, 16M, 32M, 64M, 128M and 256M.

- **OnChip USB**
  This should be enabled if your system has a USB installed on the system board and you wish to use it. Even when so equipped, if you add a higher performance controller, you will need to disable this feature. The available choices are Enabled and Disabled.

- **USB Keyboard Support**
  Select *Enabled* if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard. The options available are Enabled, Disabled.

- **Current System/CPU Temp.**
  These read-only fields reflect the functions of the hardware thermal sensor that monitors the chip blocks and system temperatures to ensure the system is stable.

- **Current CPU Fan1 Speed**
  These optional and read-only fields show the current speeds in RPM (revolution per minute) for the CPU fan and chassis fan as monitored by the hardware monitoring IC.

- **Vcore/2.5V/3.3V/5V/12V**
  These optional and read-only fields show the current voltages in the voltage regulators and power supply as monitored by the hardware monitoring IC.

7.6 Power Management Setup

The Power Management Setup allows you to save energy of your system effectively. It will shut down the hard disk and turn OFF video display after a period of inactivity.

ROM PCI/ISA BIOS
POWER MANAGEMENT SETUP
AWARD SOFTWARE, INC.

ACPI Function	: Disabled	Primary INTR	: ON
Power Management	: User Define	IRQ3 (COM 2)	: Primary
PM Control by APM	: Yes	IRQ4 (COM 1)	: Primary
Video Off After	: Suspend	IRQ5 (LPT 2)	: Primary
Video Off Method	: V/H SYNC +Blank	IRQ6 (Floppy Disk)	: Primary
MODEM Use IRQ	: 3	IRQ7 (LPT 1)	: Primary
Doze Mode	: Instant-Off	IRQ8 (RTC Alarm)	: Disabled
Standby Mode	: Former-Sts	IRQ9 (IRQ Redir)	: Secondary
Suspend Mode	: Disable	IRQ10 (Reserved)	: Secondary
HDD Power Down	: Disable	IRQ11 (Reserved)	: Secondary
Throttle Duty Cycle	: Disable	IRQ12 (PS/2 Mouse)	: Primary
** PM Events **		IRQ13 (Coprocessor)	: Primary
VGA	: OFF	IRQ14 (Hard Disk)	: Primary
LPT & COM	: LPT/COM	IRQ15 (Reserved)	: Disabled
HDD & FDD	: ON		
DMA/master	: OFF	ESC : Quit	↑ ↓ → ← : Select Item
Wake On LAN/Ring	: Disabled	F1 : Help	PU/PD/+/- : Modify
RTC Alarm Resume	: Disabled	F5 : Old Values	(Shift)F2 : Color
		F6 : Load BIOS Defaults	
		F7 : Load Setup Defaults	

- **ACPI Function**
  This item allows you to enable/disable the Advanced Configuration and Power Management (ACPI). The options available are Enabled, Disabled.

- **Power Management**
  This category allows you to select the type (or degree) of power saving and is directly related to the following modes:
  - HDD Power Down
  - Doze Mode
  - Suspend Mode

  There are four selections for Power Management, three of which have fixed mode settings.

Disable (default)	No power management. Disables all four modes
Min. Power Saving	Minimum power management. Doze Mode = 1 hr. Standby Mode = 1 hr., Suspend Mode = 1 hr., and HDD Power Down = 15 min.
Max. Power Saving	Maximum power management -- **ONLY AVAILABLE FOR SL CPU'S**. Doze Mode = 1 min., Standby Mode = 1 min., Suspend Mode = 1 min., and HDD Power Down = 1 min.
User Define	Allows you to set each mode individually. When not disabled, each of the ranges are from 1 min. to 1 hr. except for HDD Power Down which ranges from 1 min. to 15 min. and disable.

NOTE: *In order to enable the CPU overheat protection feature, the Power Management field should not be set to Disabled.*

- **PM Control by APM**
  When enabled, an Advanced Power Management device will be activated to enhance the Max. Power Saving mode and stop the CPU internal clock. If Advance Power Management (APM) is installed on your system, selecting Yes gives better power savings. If the Max. Power Saving is not enabled, this will be preset to *No*.

- **Video Off After**
  When enabled, this feature allows the VGA adapter to operate in a power saving mode.

Always On	Monitor will remain on during power saving modes.
Suspend --> Off	Monitor blanked when the system enters the Suspend mode.
Susp,Stby --> Off	Monitor blanked when the system enters either Suspend or Standby modes.
All Modes --> Off	Monitor blanked when the system enters any power saving mode.

- **Video Off Method**
  This determines the manner in which the monitor is blanked.

V/H SYNC + Blank	This causes the system to turn off the vertical and horizontal synchronization ports and write blanks to the video buffer.
DPMS	Select this option if your monitor supports the Display Power Management Signaling (DPMS) standard of the Video Electronics Standards to select video power management values.
Blank Screen	This option only writes blanks to the video buffer.

- **Modem Use IRQ**
  This field names the interrupt request (IRQ) line assigned to the modem (if any) on your system. Activity on the selected IRQ always awakens the system. The available choices are 3, 4, 5, 7, 9, 10, 11, and NA. By default, the IRQ is set to ***3***.

7.6.1 PM Timers

The following four modes are Green PC power saving functions that are only user configurable when *User Defined* Power Management is selected. See above for available selections.

- **Doze Mode**
  When enabled, and after the set time of system inactivity, the CPU clock runs at a slower speed while all other devices operate at full speed.
- **Standby Mode**
  After the set period of system inactivity expires, the fixed disk drive and the video shut OFF while all other devices operate at full speed.
- **Suspend Mode**
  When enabled, and after the set time of system inactivity, all devices except the CPU will be shut OFF.
- **HDD Power Down**
  When enabled, and after the set time of system inactivity, the hard disk drive is powered down while all other devices remain active.
- **Throttle Duty Cycle**
  When system enters Doze mode, the CPU clock runs only part of the time. You may select the percent of time that the clock runs.

7.6.2 PM Events

PM events are I/O events whose occurrence can prevent the system from entering a power saving mode or can awaken the system from such a mode. In effect, the system remains alert for anything that occurs to a device that is configured as *On*, even when the system is in power down mode.

- **VGA**
  When enabled, any video activity restarts the global timer for Standby mode. The default setting is ***Enabled***.

- **LPT & COM**
  When LPT & COM is *On*, any activity from one of the listed system peripheral devices or IRQs wakes up the system.
- **HDD & FDD**
  When *HDD* & FDD is *On*, any activity from one of the listed system peripheral devices wakes up the system.
- **DMA/master**
  When you *DMA / ISA Master* are *On,* any activity from one of the list system peripheral devices wakes up the system.
- **Wake On LAN/Ring**
  An input signal on the serial Ring Indicator (RI) line (in other words, an incoming call on the modem) or Network awakens the system from a soft off state.
- **RTC Alarm Function**
  When *Enabled,* your can set the date and time at which the RTC (real-time clock) alarm awakens the system from Suspend mode.
- **Primary INTR**
  When set to *On*, any event occurring will awaken a system which has been powered down.

  The following is a list of IRQ's, **I**nterrupt **R**e**Q**uests, which can be exempted much as the COM ports and LPT ports above can. When an I/O device wants to gain the attention of the operating system, it signals this by causing an IRQ to occur. When the operating system is ready to respond to the request, it interrupts itself and performs the service.

  When set *On*, activity will neither prevent the system from going into a power management mode nor awaken it.

  - IRQ3 (COM 2)
  - IRQ4 (COM 1)
  - IRQ5 (LPT 2)
  - IRQ6 (Floppy Disk)
  - IRQ7 (LPT 1)
  - IRQ8 (RTC Alarm)
  - IRQ9 (IRQ2 Redir)
  - IRQ10 (Reserved)
  - IRQ11 (Reserved)
  - IRQ12 (PS / 2 Mouse)
  - IRQ13 (Coprocessor)
  - IRQ14
  - IRQ15

7.7 PNP/PCI Configuration

This option configures the PCI bus system. All PCI bus systems on the system use INT#, thus all installed PCI cards must be set to this value.

ROM PCI/ISA BIOS
PNP/PCI CONFIGURATION
AWARD SOFTWARE, INC.

PNP OS Installed	: No	CPU to PCI Write Buffer	: Enabled
Resources Controlled by	: Auto	PCI Dynamic Bursting	: Enabled
Reset Configuration Data	: Disabled	PCI Master 0 WS Write	: Enabled
		PCI Delay Transaction	: Enabled
		PCI#2 Access #1 Retry	: Disabled
		AGP Master 1 WS Write	: Enabled
		AGP Master 1 WS Read	: Disabled
		PCI IRQ Activated By	: Level
		Assign IRQ For USB	: Enabled
		Assign IRQ For VGA	: Enabled
		OnBoard Ethernet BootROM	: Disabled
		ESC : Quit	↑ ↓ → ← : Select Item
		F1 : Help	PU/PD/+/- : Modify
		F5 : Old Values	(Shift)F2 : Color
		F6 : Load BIOS Defaults	
		F7 : Load Setup Defaults	

- **PNP OS Installed**
  This item allows you to determine install PnP OS or not. The options available are Yes and No.
- **Resource controlled by**
  The Award Plug and Play BIOS has the capacity to automatically configure all of the boot and Plug and Play compatible devices. However, this capability means absolutely nothing unless you are using a Plug and Play operating system such as Windows®95. The options available are Auto and Manual.
- **Reset Configuration Data**
  Normally, you leave this field Disabled. Select Enabled to reset Extended System Configuration Data (ESCD) when you exit Setup or if you have installed a new add-on and the system reconfiguration has caused such a serious conflict that the operating system can not boot. The options available are Enabled and Disabled.

- **CPU to PCI Write Buffer**
  When *Enabled,* writes from the CPU to the PCI bus are buffered, to compensate for the speed differences between the CPU and the PCI bus. When *Disabled*, the writes are not buffered and the CPU must wait until the write is complete before starting another write cycle. The options available are Enabled and Disabled.

- **PCI Dynamic Bursting**
  When *Enabled,* every write transaction goes to the write buffer. Burstable transactions then burst on the PCI bus and non-burstable transactions don't. The options available are Enabled and Disabled**.**

- **PCI Master 0 WS Write**
  When *Enabled,* writes to the PCI bus are executed with zero wait states. The options available are Enabled and Disabled.

- **PCI Delay Transaction**
  The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select *Enabled* to support compliance with PCI specification version 2.1. The options available are Enabled, Disabled

- **PCI#2 Access #1 Retry**
  When PCI#2 (AGP bus) access to PCI#1 (PCI bus) has an error occurred. The options available are Enabled, Disabled

- **AGP Master 1 WS Write**
  When *Enabled,* writes to the AGP (Accelerated Graphics Port) are executed with one wait states. The options available are Enabled, Disabled

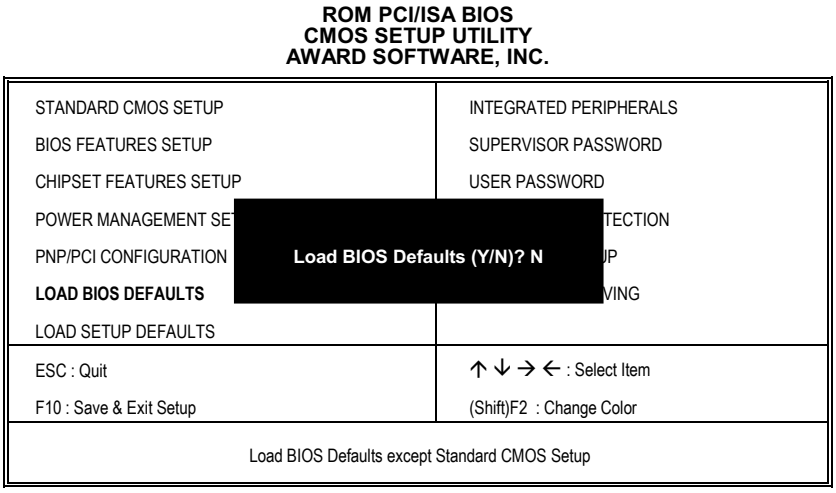
- **AGP Master 1 WS Read**
  When *Enabled,* read to the AGP (Accelerated Graphics Port) are executed with one wait states. The options available are Enabled, Disabled

- **Assign IRQ For USB/VGA**
  Enable/Disable to assign IRQ for USB/VGA. The options available are Enabled, Disabled

- **OnBoard Ethernet BootROM**
  The BIOS of **SBC8260** includes Boot ROM for Novell. This item allows user to enable or disable such function.

7.8 Load BIOS Defaults

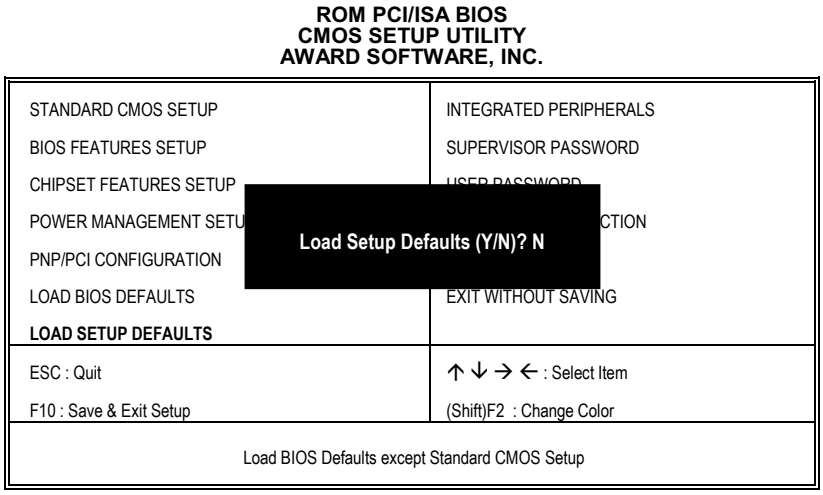
This option allows you to load the troubleshooting default values permanently stored in the BIOS ROM. These default settings are non-optimal and disable all high-performance features.



To load BIOS defaults value to CMOS SRAM, enter "Y". If not, enter "N".

7.9 Load Setup Defaults

This option allows you to load the default values to your system configuration. These default settings are optimal and enable all high performance features.



To load SETUP defaults value to CMOS SRAM, enter "Y". If not, enter "N".

7.10 Integrated Peripherals

This option sets your hard disk configuration, mode and port.

ROM PCI/ISA BIOS
INTEGRATED PERIPHERALS
AWARD SOFTWARE, INC.

OnChip IDE Channel0 : Enabled	
IDE Prefetch Mode : Disabled	
Primary Master PIO : Auto	
Primary Slave PIO : Auto	
Primary Master UDMA : Disable	
Primary Slave UDMA : Disable	
Init Display First : ISA/PCI Slot	
Onboard FDD Controller : Enabled	
Onboard Serial Port 1 : 3F8/IRQ4	
Onboard Serial Port 2 : 2F8/IRQ3	
UART 2 Mode : Standard	
Onboard Parallel Port : 378/IRQ7	ESC : Quit ↑ ↓ → ← : Select Item
Onboard Parallel Mode : Normal	F1 : Help PU/PD/+/- : Modify
	F5 : Old Values (Shift)F2 : Color
	F6 : Load BIOS Defaults
	F7 : Load Setup Defaults

- **OnChip IDE Channel 0**
  The chipset contains a PCI IDE interface with support for two IDE channels. Select *Enabled* to activate the primary IDE interface. Select *Disabled* to deactivate this interface.

- **IDE Prefetch Mode**
  The onboard IDE drive interfaces support IDE prefetching, for faster drive accesses. If you install a primary and/or secondary add-in IDE interface, set this to *Disabled* if the interface does not support prefetching. The options available are Enabled, Disabled.

- **Primary Master/Slave PIO**
  The four IDE PIO (Programmed Input/Output) fields let you set a PIO mode (0-4) for each of the four IDE devices that the onboard IDE interface supports. Modes 0 through 4 provide successively increased performance. In Auto mode, the system automatically determines the best mode for each device. The options available are Auto, Mode 0, Mode 1, Mode 2, Mode 3, and Mode 4.

- **Primary Master/Slave UDMA**
  Ultra DMA/66 implementation is possible only if your IDE hard drive supports it and the operating environment includes a DMA driver (Windows 95 OSR2 or a third-party IDE bus master driver). If your hard drive and your system software both support Ultra DMA 33/66, select Auto to enable BIOS support. The options available are Auto, Mode 0, Mode 1, and Mode 2.

- **Init Display First**
  This item allows you to decide to active whether PCI Slot or AGP first. The options available are PCI Slot, AGP.

- **Onboard FDD Controller**
  Select Enabled if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install and-in FDC or the system has no floppy drive, select Disabled in this field. The options available are Enabled, Disabled.

- **Onboard Serial Port 1/Port 2**
  Select an address and corresponding interrupt for the first and second serial ports. The options available are 3F8/IRQ4, 2E8/IRQ3, 3E8/IRQ4, 2F8/IRQ3, Disabled, Auto.

- **UART 2 Mode**
  This item allows you to determine which Infra Red (IR) function of onboard I/O chip. The options available are IrDA 1.0, Standard, ASKIR, and HPSIR.

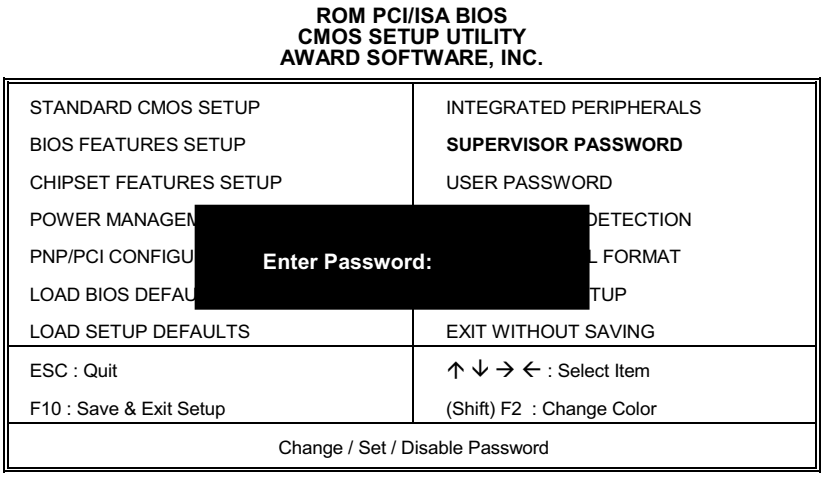
- **Onboard Parallel Port**
  This item allows you to determine access onboard parallel port controller with which I/O address. The options available are 378H/IRQ7, 278H/IRQ5, 3BC/IRQ7, Disabled.

- **Onboard Parallel Mode**
  Select an operating mode for the onboard parallel (printer) port. Select Normal unless your hardware and software require one of the other modes offered in this field. The options available are EPP1.9, ECP, SPP, ECP, EPP1.7, EPP1.7.

7.11 Supervisor / User Password

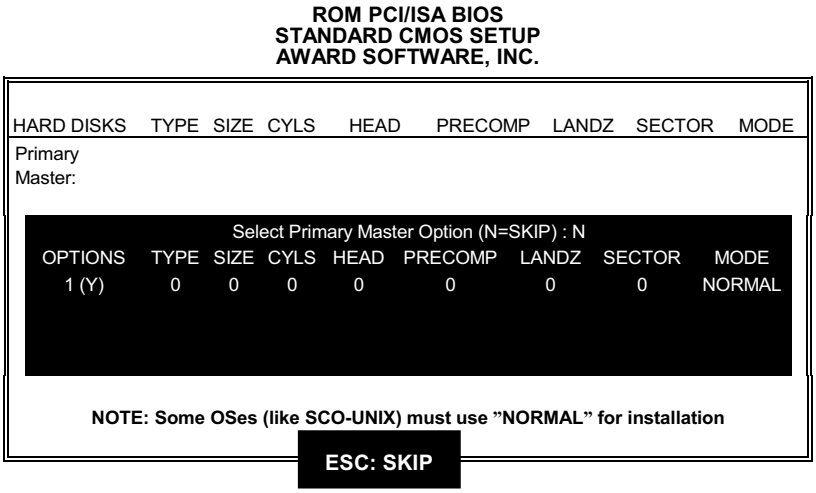
These two options set the system password. Supervisor Password sets a password that will be used to protect the system and Setup utility. User Password sets a password that will be used exclusively on the system. To specify a password, highlight the type you want and press <Enter>. The Enter Password: message prompts on the screen. Type the password, up to eight characters in length, and press <Enter>. The system confirms your password by asking you to type it again. After setting a password, the screen automatically returns to the main screen.

To disable a password, just press the <Enter> key when you are prompted to enter the password. A message will confirm the password to be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.



7.12 IDE HDD Auto Detection

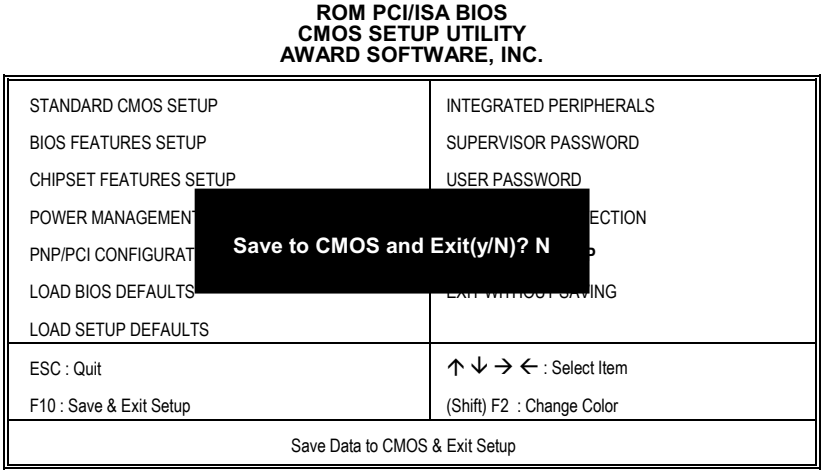
This option detects the parameters of an IDE hard disk drive, and automatically enters them into the Standard CMOS Setup screen.



Up to four IDE drives can be detected, with parameters for each appearing in sequence inside a box. To accept the displayed entries, press the "Y" key; to skip to the next drive, press the "N" key. If you accept the values, the parameters will appear listed beside the drive letter on the screen.

7.13 Save & Exit Setup

This allows you to determine whether or not to accept the modifications. Typing "Y" quits the setup utility and saves all changes into the CMOS memory. Typing "N" brigs you back to Setup utility.



7.14 Exit Without Saving

Select this option to exit the Setup utility without saving the changes you have made in this session. Typing "Y" will quit the Setup utility without saving the modifications. Typing "N" will return you to Setup utility.

ROM PCI/ISA BIOS
CMOS SETUP UTILITY
AWARD SOFTWARE, INC.

STANDARD CMOS SETUP	INTEGRATED PERIPHERALS
BIOS FEATURES SETUP	SUPERVISOR PASSWORD
CHIPSET FEATURES SETUP	USER PASSWORD
POWER MANAGEMENT S	TECTION
PNP/PCI CONFIGURATIO	P
LOAD BIOS DEFAULTS	VING
LOAD SETUP DEFAULTS	
ESC : Quit	↑ ↓ → ← : Select Item
F10 : Save & Exit Setup	(Shift) F2 : Change Color
Abandon all Data & Exit Setup	

Quit Without Saving (Y/N)? N

Appendix A
Watchdog Timer

Using the Watchdog Function

The **SBC8260VE** CPU card uses version 2.0 of the watchdog timer. This onboard WDT generates either a system reset or non-maskable interrupt (NMI), depending on the settings made on jumper **JP3** of **SBC8260VE**. Follow the steps below to enable and program the watchdog function of **SBC8260VE**.

Start

↓

Un-Lock WDT	:	**OUT 120H 0AH ; enter WDT function** **OUT 120H 0BH ; enable WDT function**

↓

Set multiple (1~4)	:	**OUT 120 0NH ; N=1,2,3 or 4**

↓

Set base timer (0~F)	:	**OUT 121 0MH ; M=0,1,2,…F**

↓

WDT counting

↓

re-set timer	:	**OUT 121 0MH ; M=0,1,2,…F**

↓

IF No re-set timer	:	**WDT time-out, generate RESET or NMI**

↓

IF to disable WDT	:	**OUT 120 00H ; Can be disable at any time**

M	N			
	1	2	3	4
0	0.5 sec.	5 secs.	50 secs.	100 secs.
1	1 sec.	10 secs.	100 secs.	200 secs.
2	1.5 secs.	15 secs.	150 secs.	300 secs.
3	2 secs.	20 secs.	200 secs.	400 secs.
4	2.5 secs.	25 secs.	250 secs.	500 secs.
5	3 secs.	30 secs.	300 secs.	600 secs.
6	3.5 secs.	35 secs.	350 secs.	700 secs.
7	4 secs.	40 secs.	400 secs.	800 secs.
8	4.5 secs.	45 secs.	450 secs.	900 secs.
9	5 secs.	50 secs.	500 secs.	1000 secs.
A	5.5 secs.	55 secs.	550 secs.	1100 secs.
B	6 secs.	60 secs.	600 secs.	1200 secs.
C	6.5 secs.	65 secs.	650 secs.	1300 secs.
D	7 secs.	70 secs.	700 secs.	1400 secs.
E	7.5 secs.	75 secs.	750 secs.	1500 secs.
F	8 secs.	80 secs.	800 secs.	1600 secs.

Appendix B
Connector Pin Assignments

40-pin IDE Interface Connector: CN6

Pin	Description	Pin	Description	Pin	Description
1	Reset #	2	GND	3	Data 7
4	Data 8	5	Data 6	6	Data 9
7	Data 5	8	Data 10	9	Data 4
10	Data 11	11	Data 3	12	Data 12
13	Data 2	14	Data 13	15	Data 1
16	Data 14	17	Data 0	18	Data 16
19	GND	20	No connector	21	No connector
22	GND	23	IOW #	24	GND
25	IOR #	26	GND	27	IOCHRDY
28	No connector	29	No connector	30	GND-Default
31	Interrupt	32	No connector	33	SA1
34	No connector	35	SA0	36	SA2
37	HDC CS0 #	38	HDC CSI #	39	HDD Active #
40	GND				

Floppy Disk Connector: CN2

Pin	Description	Pin	Description	Pin	Description
1	GND	2	Reduce write current	3	GND
4	No connector	5	GND	6	No connector
7	GND	8	Index#	9	GND
10	Motor enable A#	11	GND	12	Drive select B#
13	GND	14	Drive select A#	15	GND
16	Motor enable B#	17	GND	18	Direction#
19	GND	20	STEP#	21	GND
22	Write data#	23	GND	24	Write gate#
25	GND	26	Track 0 #	27	GND
28	Write protect#	29	GND	30	Read data#
31	GND	32	Side 1 select#	33	GND
34	Disk change#				

Parallel Port Connector: CN3

Pin	Description	Pin	Description
1	Strobe#	14	Auto Form Feed#
2	Data 0	15	Error#
3	Data 1	16	Initialize#
4	Data 2	17	Printer Select In#
5	Data 3	18	GND
6	Data 4	19	GND
7	Data 5	20	GND
8	Data 6	21	GND
9	Data 7	22	GND
10	Acknowledge#	23	GND
11	Busy	24	GND
12	Paper Empty#	25	GND
13	Printer Select	26	

RS-232 pin assignments for COM1 and COM2

Pin	Description	Pin	Description
1	Data Carrier Detect (DCD)	2	Data Set Ready (DSR)
3	Receive Data (RXD)	4	Request to Send (RTS)
5	Transmit Data (DRT)	6	Clear to Send (CTS)
7	Data Terminal Ready (DTR)	8	Ring Indicator (RI)
9	Ground (GND)		

RS-485 pin assignments for COM2

RS-422				RS-485			
Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	TXD-	6	X	1	RTXD-	6	X
2	TXD+	7	X	2	RTXD+	7	X
3	RXD+	8	X	3	X	8	X
4	RXD-	9	X	4	X	9	X
5	GND	10	X	5	GND	10	X

USB Connector: CN10

Pin	Description	Pin	Description
1	USB Vcc	2	USB Vcc
3	USB P0-	4	USB P1-
5	USB P0+	6	USB P1+
7	GND	8	GND
9	No connector	10	No connector

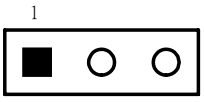
Front Panel Bezel Connector: CN1

Reset Switch: Pins 17 & 18
HDD Active LED: Pins 19 & 20
Speaker: Pins 2 & 8
Internal Buzzer; Pins 4 & 6
SMI Switch: Pins 11 & 12
Power LED: Pins 1 & 5

Pin	Description	Pin	Description
1	Power LED+	2	Ext. Speaker
3	No connector	4	Int. Buzzer
5	Power LED-	6	Int. Buzzer
7	+5V	8	+5V
9	GND	10	No connector
11	Ext. SMI -	12	Ext. SMI +
13	No connector	14	No connector
15	Reserved	16	Reserved
17	Reset Pin2	18	Reset Pin1
19	HDD LED-	20	HDD LED+

NOTE: *You may only install the speaker (pins 2 & 8), or short pins 4 & 6 (internal buzzer) at one given time.*

CPU Fan Connector: CN5



Pin	Description
1	Speed Sensor
2	+12V
3	GND

PCI Slot Connector

The **SBC8260VE** provides a free PCI slot for a PCI device expansion.

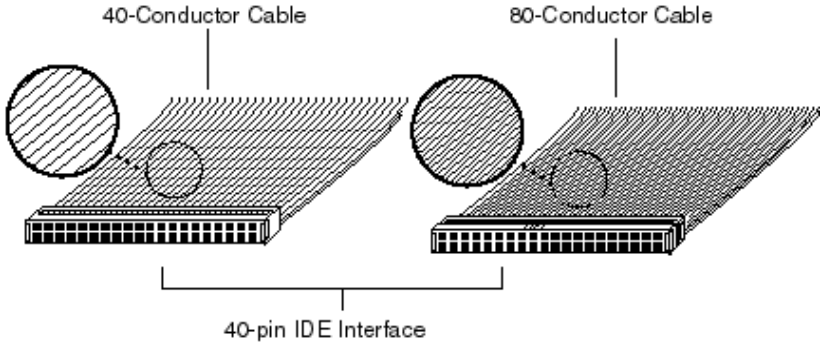
Signal	Pin	Pin	Signal	Signal	Pin	Pin	Signal
-12V	B1	A1	No connector	AD17	B32	A32	AD16
No connector	B2	A2	+12V	CBE2	B33	A33	No connector
GND	B3	A3	No connector	GND	B34	A34	-FRAME
No connector	B4	A4	No connector	-IRDY	B35	A35	GND
+5V	B5	A5	+5V	No connector	B36	A36	-TRDY
+5V	B6	A6	INT_A	-DEVSEL	B37	A37	GND
INT_B	B7	A7	INT_C	GND	B38	A38	-STOP
INT_D	B8	A8	+5V	-PLOCK	B39	A39	No connector
-REQ3	B9	A9	CLK_C	-PERR	B40	A40	SDONE
-REQ1	B10	A10	+5V	No connector	B41	A41	No connector
-GNT3	B11	A11	CLK_D	-SERR	B42	A42	GND
GND	B12	A12	GND	No connector	B43	A43	PAR
GND	B13	A13	GND	CBE1	B44	A44	AD15
CLK_A	B14	A14	-GNT1	AD14	B45	A45	No connector
GND	B15	A15	PCIRST	GND	B46	A46	AD13
CLK_B	B16	A16	+5V	AD12	B47	A47	AD11
GND	B17	A17	-GNT0	AD10	B48	A48	GND
-REQ0	B18	A18	GND	GND	B49	A49	AD9
+5V	B19	A19	-REQ2	KEY	B50	A50	KEY
AD31	B20	A20	AD30	KEY	B51	A51	KEY
AD29	B21	A21	No connector	AD8	B52	A52	CBE0
GND	B22	A22	AD28	AD7	B53	A53	No connector
AD27	B23	A23	AD26	No connector	B54	A54	AD6
AD25	B24	A24	GND	AD5	B55	A55	AD4
No connector	B25	A25	AD24	AD3	B56	A56	GND
CBE3	B26	A26	-GNT2	GND	B57	A57	AD2
AD23	B27	A27	No connector	AD1	B58	A58	AD0
GND	B28	A28	AD22	+5V	B59	A59	+5V
AD21	B29	A29	AD20	No connector	B60	A60	No connector
AD19	B30	A30	GND	+5V	B61	A61	+5V
No connector	B31	A31	AD18	+5V	B62	A62	+5V

Appendix C
Ultra DMA/66/100 Reference

Overview

Ultra ATA/100 is a low cost extension of the Ultra ATA/66 hard drive interface that enhance its burst data rate. Also known as Ultra DMA/100 and Fast ATA-2, Ultra ATA/66/100 allows host computers to send and receive data at 100 MB/s, which is triple the data transfer speeds of 33.3 MB/s of Ultra DMA/33. The result is maximum disk performance under PCI local bus environments.

At its fast burst data rates, Ultra ATA/100 will go farther than Ultra ATA/66 in removing bottlenecks associated with data transfers, especially during sequential operations. Ultra ATA/100 also delivers heightened data integrity to the EIDE interface through use of a 40-pin 80-conductor cable, and CRC (Cyclic Redundancy Check) error detection code. The 80-conductor cable reduces crosstalk and improves signal integrity by providing 40 additional ground lines between the 40-pin IDE signal and ground lines. The connector is plug-compatible with existing 40-pin headers, and the incremental cost for the cable should be minimal. As with Ultra ATA/33, CRC ensures the integrity of transferred data.



Background

Ultra ATA/100 is the latest ATA/IDE hard drive data transfer protocol for moving data between the hard drive buffer and the system memory. The previous interface was Ultra ATA/66, with a maximum burst transfer rate of 66.6 MB/s. Prior to Ultra ATA/66 was Ultra ATA/33 with a maximum burst transfer rate of 33.3 MB/s.

By increasing the burst transfer rates of IDE drives, Ultra ATA/100 brings the effective transfer rate of the system's bus and a drive's internal data rate that much closer into balance. Ultra ATA/100 allows system designers to provide greater system throughput, particularly for long sequential transfers required by audio/visual applications.

Host data transfer rates must exceed media data transfer rates or else performance is reduced because of additional revolutions due to buffer full/empty conditions on reads/writes.

Host Data Rate Requirements

As you can see by the chart, the host data transfer rate doubles about every three years. The chart indicates that Ultra ATA/100 will reach its limits by 2000. The industry has supported host transfer data rates doubling previously with:

- DMA Mode 2 at 16.6 MB/s in 1994
- Ultra ATA/33 at 33.3 MB/s in 1997
- Ultra ATA/66 at 66.6 MB/s in 1999
- Ultra ATA/100 at 100MB/s in 2000

With continued expansions in disk capacity and higher rotational speeds, the hard drive's internal disk rates also continue to increase. The transfer of large files, often written sequentially on the hard drive, is particularly affected by the transfer rate. During sequential reads, the hard drive, because of its fast internal data rate, may fill its buffer faster than the host can empty it when using the Ultra ATA/66 or the older multi-word DMA interfaces. Performance bottlenecks usually result in this connection between the host and the hard drive. Improving the interface to keep up with internal data rate improvements is exactly what Ultra ATA/100 can achieve.

As previously mentioned, fast host data transfer rates help maintain sequential media transfers, but they also accelerate cache hits. The following table is based on all commands are either a cache hit (data comes from the buffer and has <1 ms latency), or a cache miss (data comes from the media and has >10 ms latency.)

Ultra ATA/66 Performance Benefit		
	Cache Miss	Cache Hit
Sequential	Yes	Yes
Random	No	Yes

Backward Compatibility

The Ultra ATA/100 protocol and commands are designed to be compatible with existing ATA devices and systems. Drives implementing Ultra ATA/100 are fully backward compatible with older ATA modes, including Ultra ATA/33/66. It will handle all the data transfer modes. The slower modes will be supported with a different clock signal and clock divider. The slower modes previously worked with a standard 40-pin interface cable, but Ultra ATA/66/100 requires a 40-pin 80-conductor cable for modes 3 and 4.

	Data Transfer Rate (max.)	Cable	Conductors	CRC
DMA Mode1	11.1 MB/s	40-pin IDE	40-pin	No
Multi-word DMA Mode 1	13.3 MB/s	40-pin IDE	40-pin	No
Multi-word DMA Mode 2	16.6 MB/s	40-pin IDE	40-pin	No
Ultra ATA Mode 2	33.3 MB/s	40-pin IDE	40-pin	Yes
Ultra ATA Mode 4	66.6 MB/s	40-pin IDE	80-pin	Yes
Ultra ATA Mode 5	99.9 MB/s	40-pin IDE	80-pin	Yes

Hard drives that support Ultra ATA/66/100 also support Ultra ATA/33 and multi-word DMA, and can be used with existing multi-word DMA host chipsets. Installed PCs without Ultra ATA/100 capability can use new hard drives in legacy ATA modes at transfer rates up to 33.3 MB/s. However, by upgrading with an Ultra ATA/100 PCI adapter card and 80-conductor cable, they can also take advantage of the interface's newer speed and data integrity features. Bus timings must

be scaled to transfer up to twice as fast. PC vendors who would like to incorporate the advantages of Ultra ATA/100 in new systems can do so by using new chipsets and motherboards from Intel and other leading vendors that license the technology. Although a new cable is required for Ultra ATA/66/100, the chipset pin count remains the same at 40.

System Requirements for Ultra ATA/66/100

To use the Ultra ATA/66/100 technology, a system must have:

- Ultra ATA/66/100 compatible logic either on the system motherboard, or on an Ultra DMA PCI adapter card
- Ultra DMA compatible BIOS
- DMA-aware device driver for the operating system
- Ultra ATA/66/100-compatible IDE device such as a hard drive or CD-ROM drive
- 40-pin 80-conductor cable

Frequently Asked Questions

1. *Can I run UDMA/66/100 on a 40 pin 40 conductor cable?*
   No, the UDMA/66/100 technology is defined such that the PC and the HDD can both detect the presence of the 80-conductor cable. UDMA/66/100 will not be enabled unless the 80-conductor cable is present.

2. *I have an UDMA/33 system today. How can I support UDMA/66/100?*
   An existing system can be upgraded by purchasing a PCI-EIDE controller that supports UDMA/66/100.

3. *Do Microsoft Operating Systems support UDMA?*
   Windows releases indicate that they all support DMA transfers. Windows does not know the difference between Ultra ATA/33 or Ultra ATA/66/100. Your HDD, your controller and the BIOS determine UDMA data transfer rate. This applies for all the following Windows operating systems:

   -Windows 98

   -Windows NT Service Pack 3

   -Windows 95 OEM Service Release 2

4. I don't have a system that supports Ultra ATA/66/100, can I run the Ultra ATA/66/100 HDD in it?

Yes, the HDD will not run in UDMA/66/100 mode but instead is a slower compatible mode such as Ultra ATA/33, DMA Mode 2 (16.6 MB/s) or PIO Mode 4 (16.6MB/s)

5. Can I mix Ultra ATA/33 and Ultra ATA/66/100 drives on the same cable?

Yes, a legacy ATA (IDE) specification drive can coexist with an Ultra ATA/66/100 drive. However, for the Ultra ATA/66/100 device to attain Ultra DMA 4 mode, an Ultra ATA/66/100 capable cable is required.

6. What are some common troubleshooting steps?

- Make sure the cable is Ultra ATA/66/100 capable. An Ultra ATA/66/100-capable cable is a 40-pin, 80-conductor cable with a black connector on one end, a blue connector on the other end and a gray connector in the middle. In addition, line 34 on the cable should be notched or cut (this may be difficult to see with the human eye).
- Make sure the system board is capable of Ultra ATA/66/100. An Ultra ATA/66/100 capable system board has a detect circuit with a capacitor for detecting line 34 missing on the cable. If there is no capacitor, the system can wrongly detect the presence of an Ultra ATA/66/100 cable and therefore try to configure the device for a higher transfer rate.
- Some system boards may not successfully handle Ultra ATA/66/100 on both ATA (IDE) channels. If you have difficulty, consider troubleshooting with the device in the Primary Master position.
- Contact the system board manufacturer for the latest BIOS upgrade and any Ultra ATA/66/100 special device drivers or patches.
- Make sure the operating system is DMA capable and that the DMA mode is activated. (For Windows 95/98, check Device Manager | Drive Settings tab for a check box.)
- Make sure the drive is Ultra ATA/66/100 capable and has been configured to run at Ultra ATA/66/100 transfer rates. (Seagate drives require an Ultra ATA/66/100 activation utility.)

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