

V-06 OBC Opened Node Timing Test

ENGINEER: _____ Fritz Sonnichsen

DATE: _____ 11/08/05 _____

COMMENTS:

TEST INCOMPLETE. Performed at MBARI

REQUIREMENTS COVERED:

7- Delivery of 1 ms time information

The timing jitter was found to be within 3 s as shown in the photos attached.

18- Jitter

This test is not conclusive:

a) Noise on the lines prevented assurance that science could obtain usable signal. However the timing signal penetrated the noise as shown in the photos

b) This test was not conducted with heavy power users on the adjacent power lines shared in the science cables.

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The ability to port a timing signal with a jitter of less than 1 microsecond over the OBC must be proven.

The required equipment for this test is:

- 1) oscilloscope
- 2) OBC Shore Station
- 3) OBC Sub Sea Assembly
- 4) Laptop Configured with Timing Test Software and an asynch terminal

VORSICHT:

WHENEVER CONNECTING OE TRANSPONDERS TO THE SUB-SEA UNIT, ALWAYS POWER DOWN THE UNIT TO AVOID DAMAGE DUE TO TRANSIENTS

MARS
Org: WHOI
Engr: Al Bradley
Fritz Sonnichsen
Date: 05/18/05

V-06 OBC Opened Node Timing Test

Procedure

1) For initial checkup, after connecting the East A&B and the West A&B shore fibers to their corresponding transponders in the sub-sea unit issue help commands to each device to check it's operation. Needless to say you will need an RS232 connection to each port (East A,B and West A,B) on the shore stations.

Check Shore East or West A side

###CKAH

Check Shore East or West B side

###CKBH

Sub-sea East or West A side

###S01AH

Sub-sea Esat or West B side (clock)

###C01BH

Sub-sea East or West A side (clock)

###C01AH

Sub-sea Esat or West B side

###S01BH

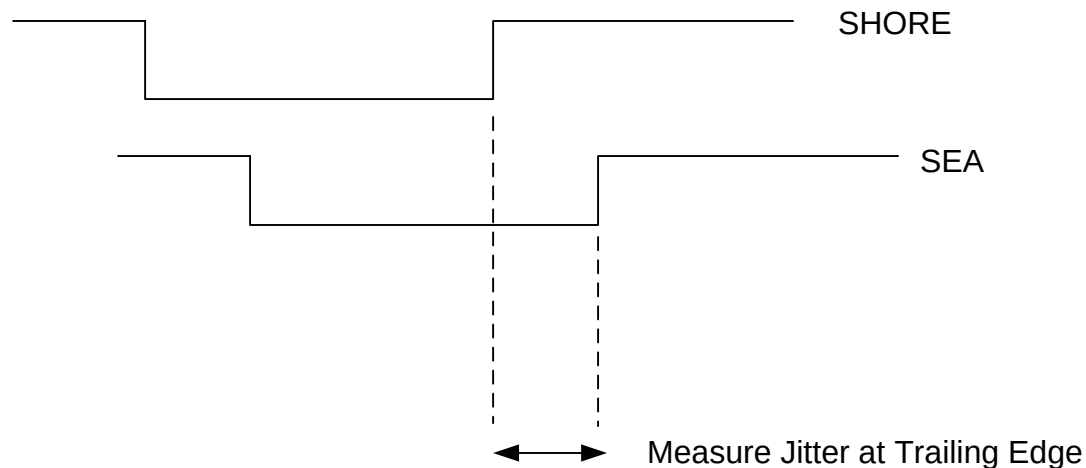
In the event or problems a simple optical test can be performed:

- 1) Insert an optical power meter (1550nm) at the fiber end which connects to a sub-sea transponder
- 2) From an ASCII terminal connected to the shore station send continuous character strings
- 3) Expect about -35 to -45 dBm

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Procedure

- 1) Connect the OBC sub-sea and shore units as shown in the setup sheets,
 - 2) Using the set1a,b set2 and set3 pages, test the signal jitter with an oscilloscope connecting oscilloscope probes as shown
 - 3) The square wave signals should appear once per second with a jitter of less than 3 microseconds measure from the trailing edge as shown below
 - 4) Capture and record the results.
- 2) Connect an asynch terminal to either RS232 DCE port of the west shore node on the A side. Use no-flow and 8N1
 - 3) Make sure the GPS is sync'd with the satellite. Using the async terminal issue:
#CKA
?G
The GPS string should contain an A at the location shown. A V means that it is not synch'd with the satellite:
GPRMC,203329,A,4131.5204,N,07039.9853,W,0.000,0.0,200505,16.1,W*45
 - 4) Set the clocks using the commands shown on the next page



V-06 OBC Opened Node Timing Test-Setting the Time Offset

MEASURING and SETTING CLOCK DELAY

Expect an offset of $(1\text{sec} \times 2 \times 10^8 \text{ ms}^{-1}) (\text{fiber-length m})$

STEP1: SYNC GPS TO THE SHORE CLOCK

#CKA address the shore clock

!GS have it send a #[gps-time]@ on the second and then unaddress

#CKA!S1 turn GPS sync on

STEP2: SYNC SEA TO SHORE CLOCK

#C01A address the sea clock

!T set in time mode

#CKA address the shore clock

?T have shore clock send the time

#C01A re-address the sea clock

?T display the sea clock time

STEP 3: LOG THE CURRENT SEA AND SHORE CLOCK TIMES

#CKA address the shore clock

!EC01A<cr> sends a #C01A!E@ to the SEA clock and then unaddresses. This will cause the SEA clock to Echo an @ back

#CKA re-address the shore clock

?L2 display last two entries in the log

The log simply writes a timestamp anytime it sees an @. In this case the @ sent by the shore (CKA) writes the first entry and the one sent back from C01A writes the second entry

STEP 4: CALCULATE THE RELATIVE TIME OFFSET FROM SEA TO SHORE

the log will contain 2 lines with the times reported by the sea and shore clocks:

0000 19:49:19.590766

0000 19:49:19.576920

diff = (difference of times above) = 13846

round-trip = diff - 0.013333 = 513 (where 0.013333 is the pre programed delay for ring-around)

light-delay = round-trip / 2 = 256

STEP5: SET SEA CLOCK DELAY

#C01A address the SEA clock

?O display offset

!O0256 set light-delay from above

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STEP6: Repeat the commands for the WEST B side using address:

CKB for the shore

C01B for the subsea

SUMMARY OF TIMING COMMANDS ISSUED TO CLOCKS

SHORE - #CKA #CKB

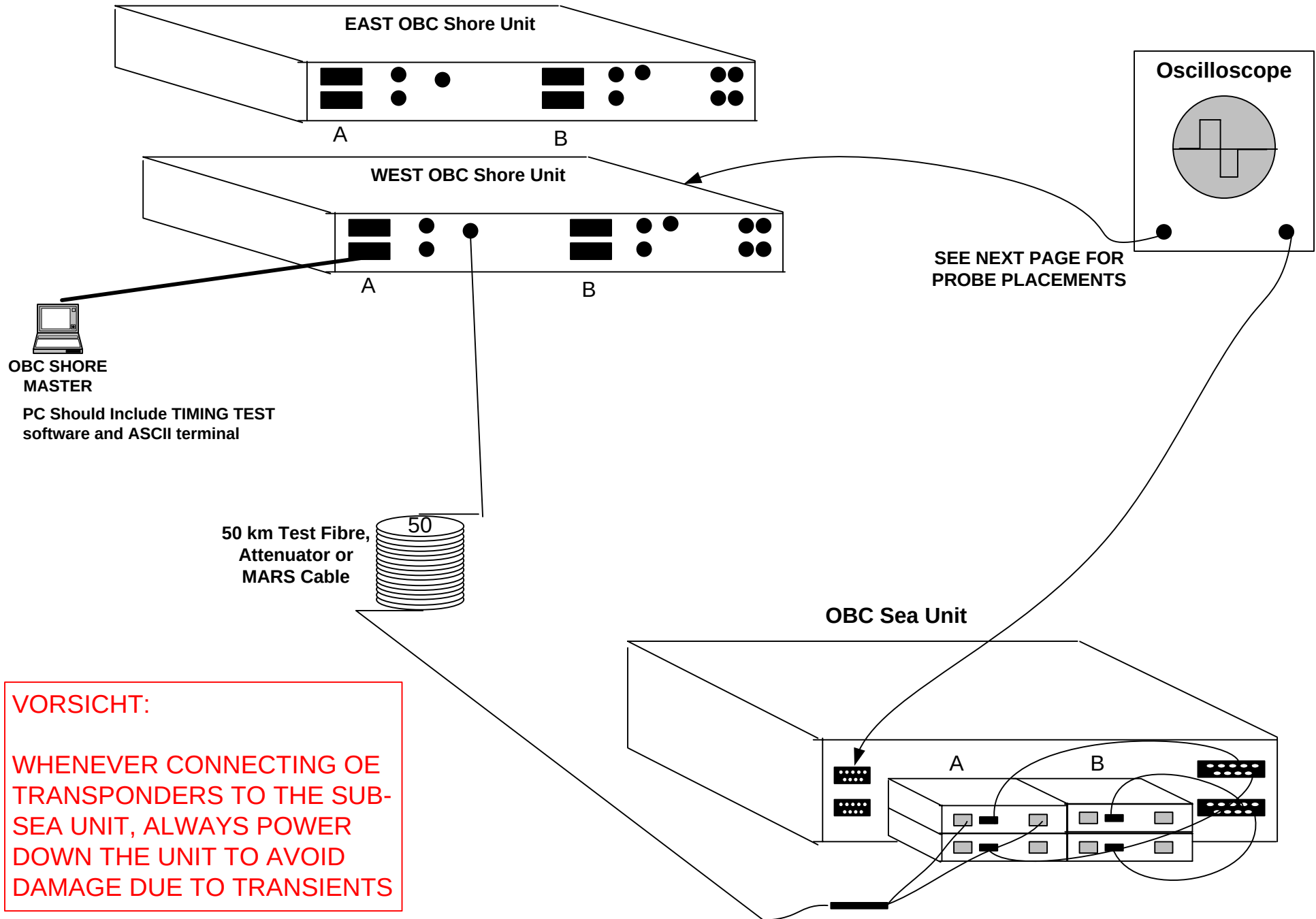
?T show real time clock value (not GPS)
?G determine if GPS is running V=no A=yes
?S show sync flag status
!S1 turn sync on
!T set clock
!S turn on sync flag after boot
?Ln display n log entries

SEA - #C01A #C01B

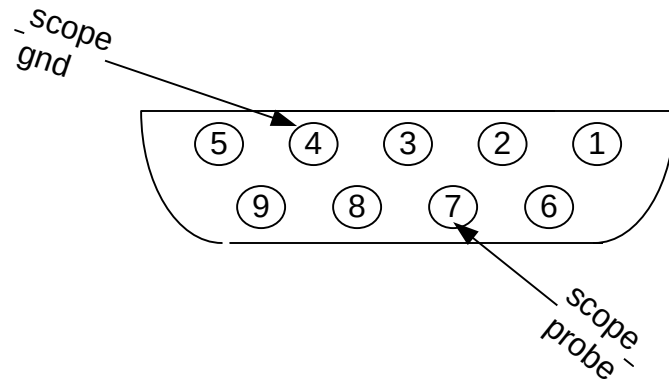
A locking status P gies (tiller – 6811 output)
!Q reset integrator
!T#CKA?T set the SEA clock to the value transmitted by the shore clock
!Tabcdefghij@ set the clock to time abcedfghij

V-06 OBC Opened Node Timing Test – SHORE to SUBSEA OBC

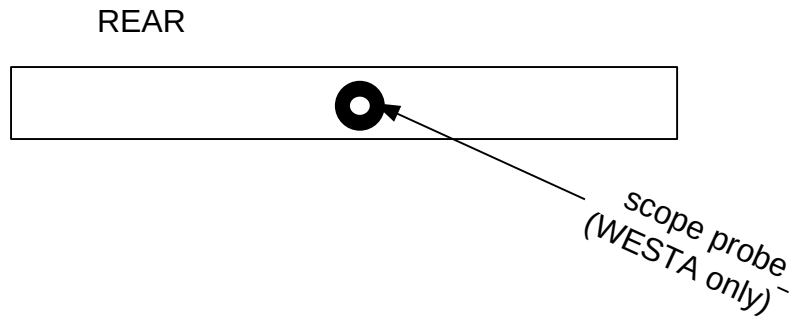
SEE NEXT PAGE FOR OSCILLOSCOPE TEST POINTS



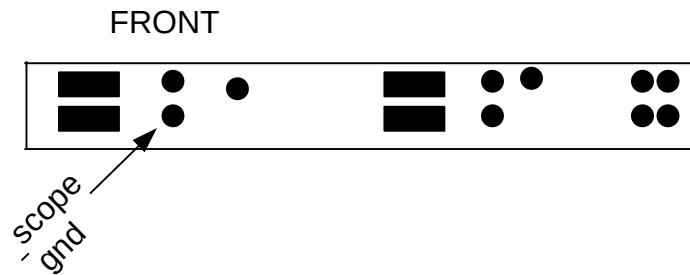
V-06 OBC Opened Node Timing Test - Setup



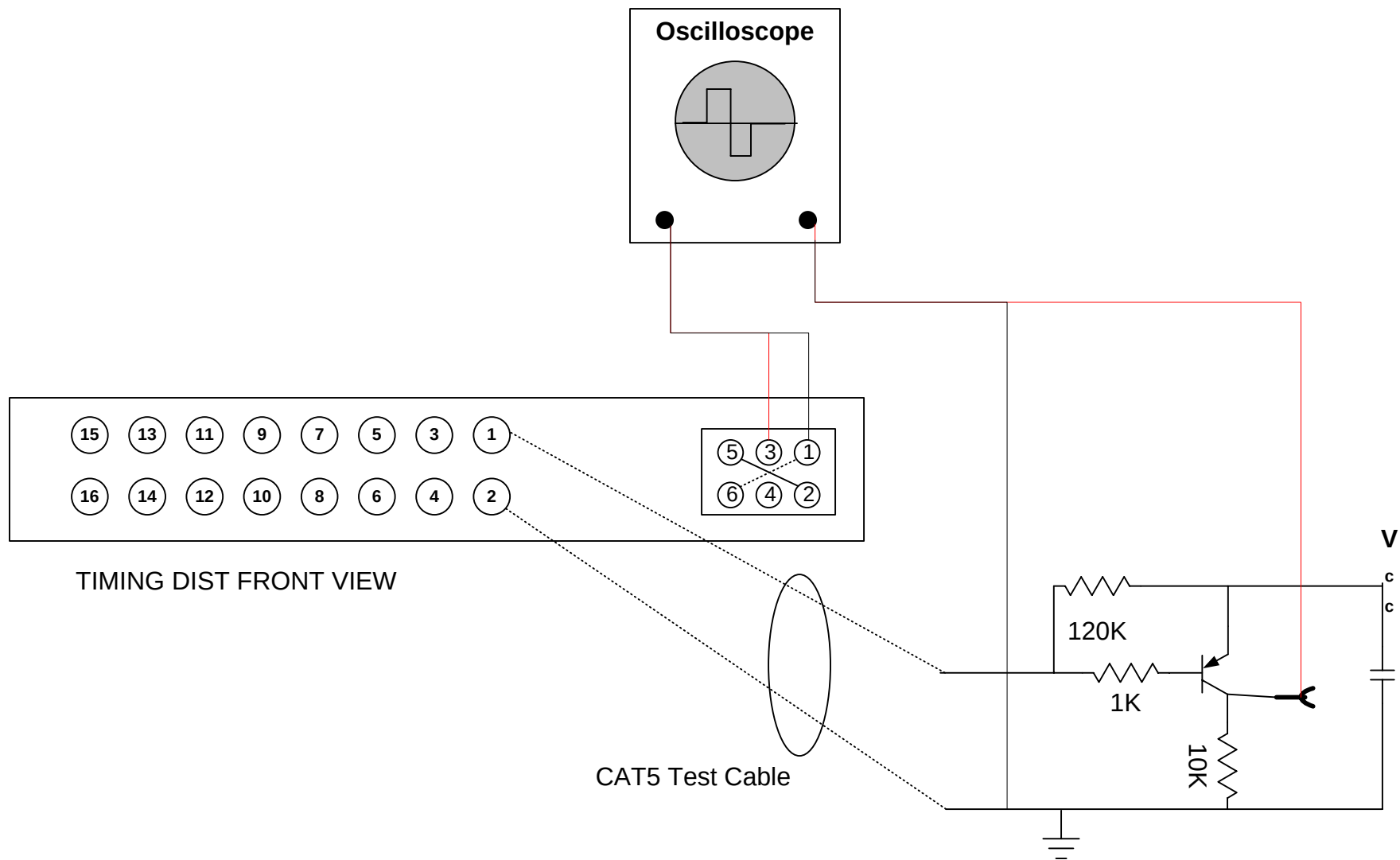
Probe points: J1-Time connector on OBC sub-sea unit



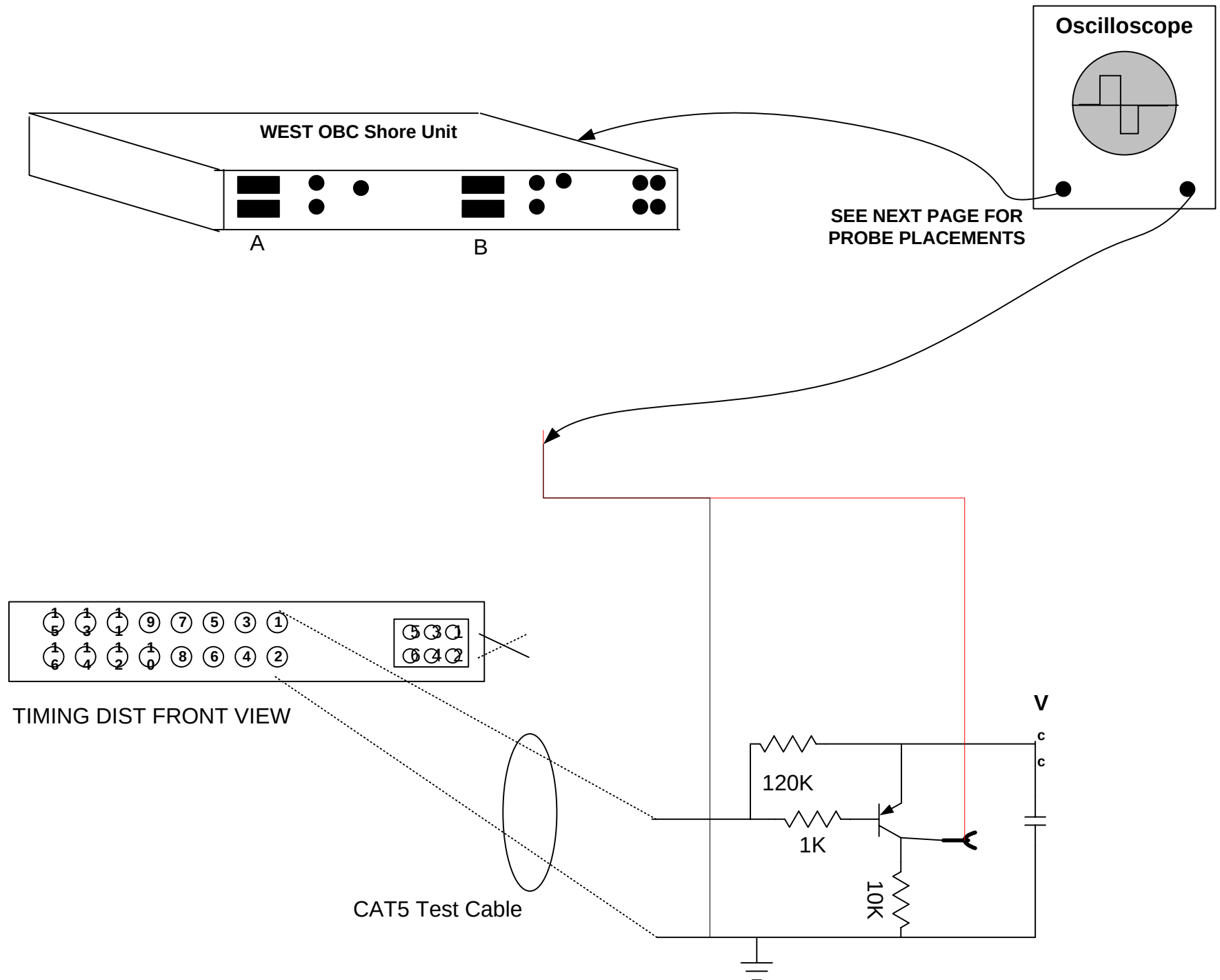
Probe points: With shore station open



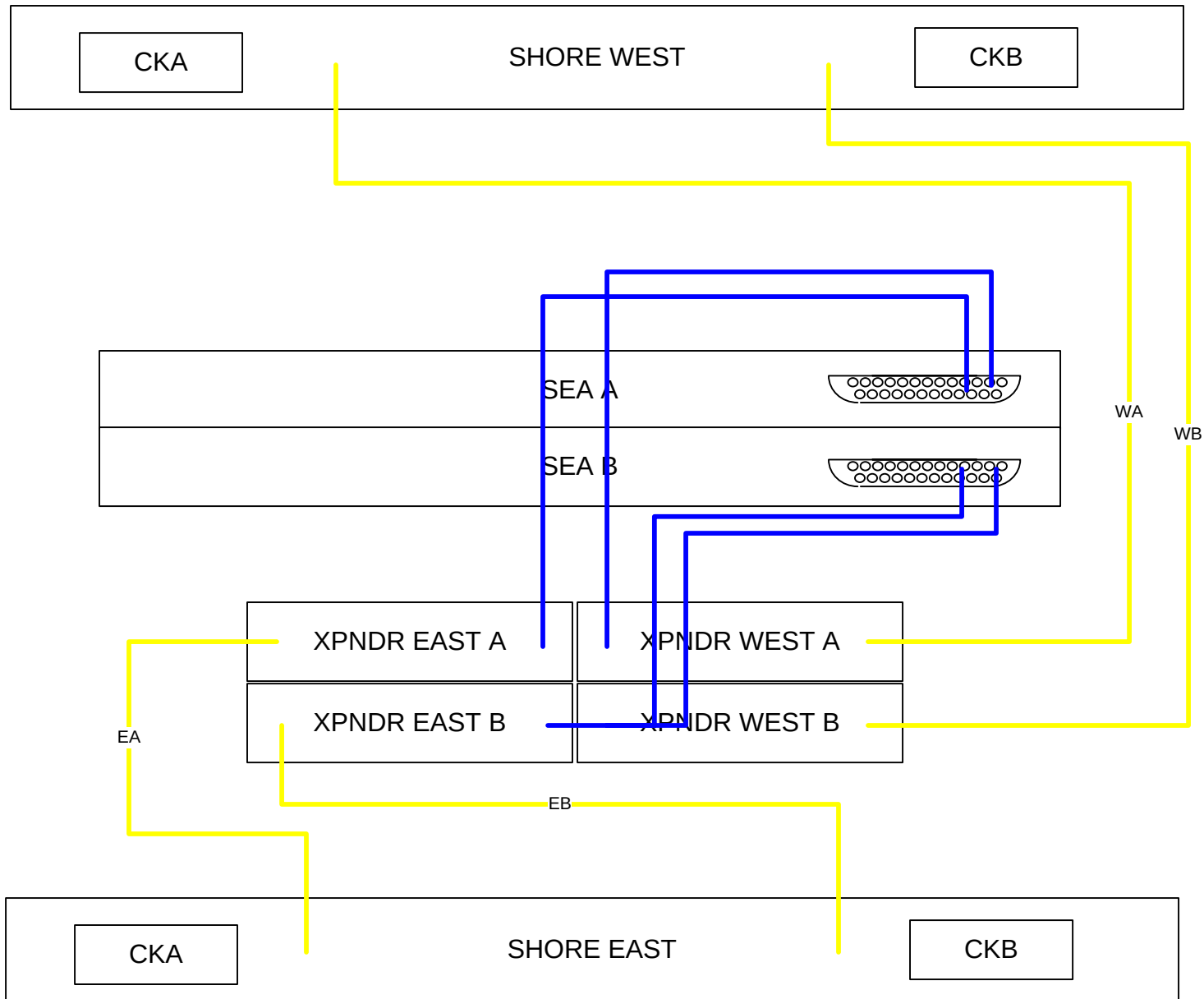
V-06 OBC Opened Node Timing Test-TIMING DIST to SCIENCE RECIEVER



V-06 OBC Opened Node Timing Test – SHORE to Science Receiver

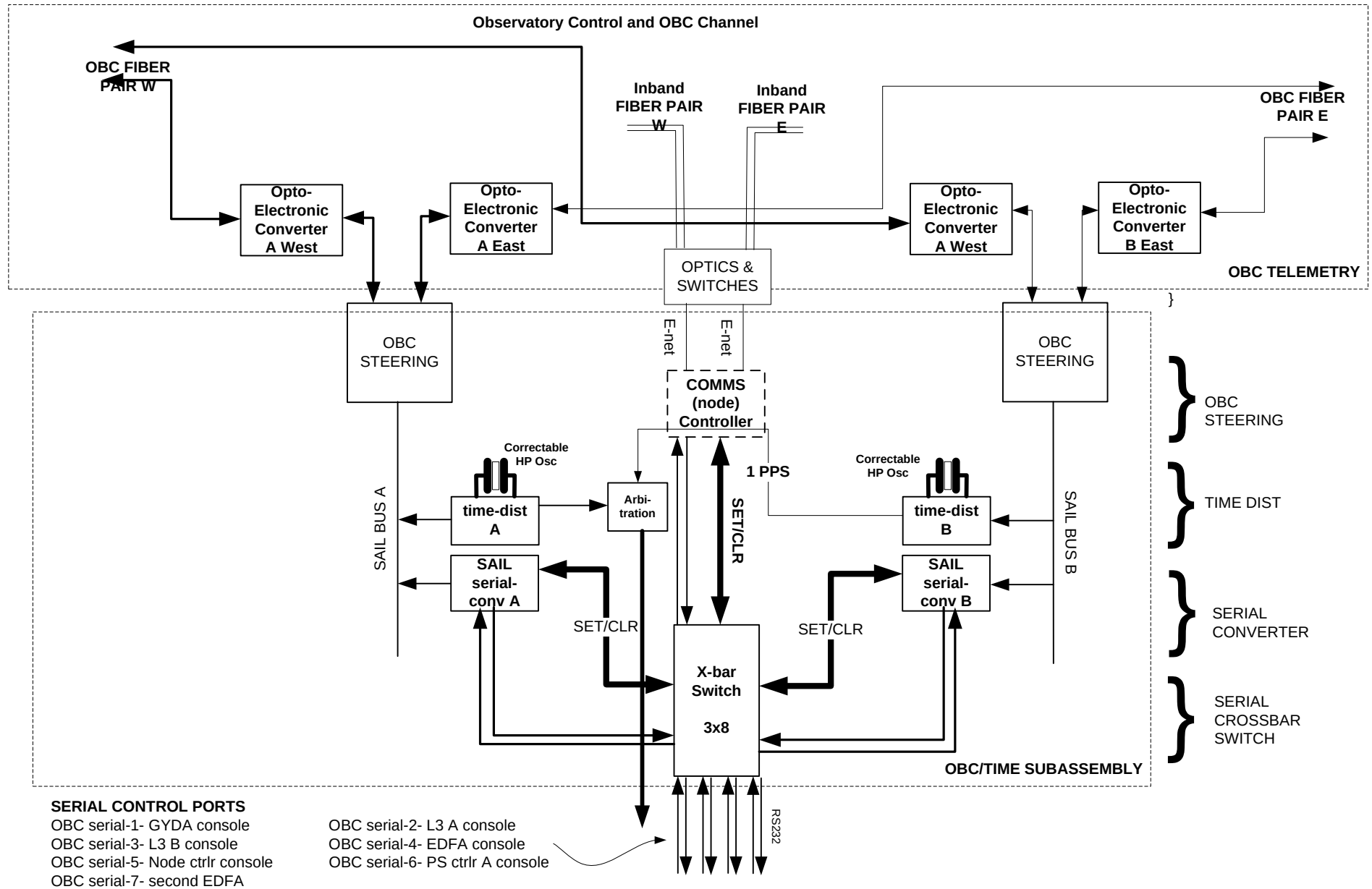


V-06 OBC Opened Node Timing Test – Architecture Summary

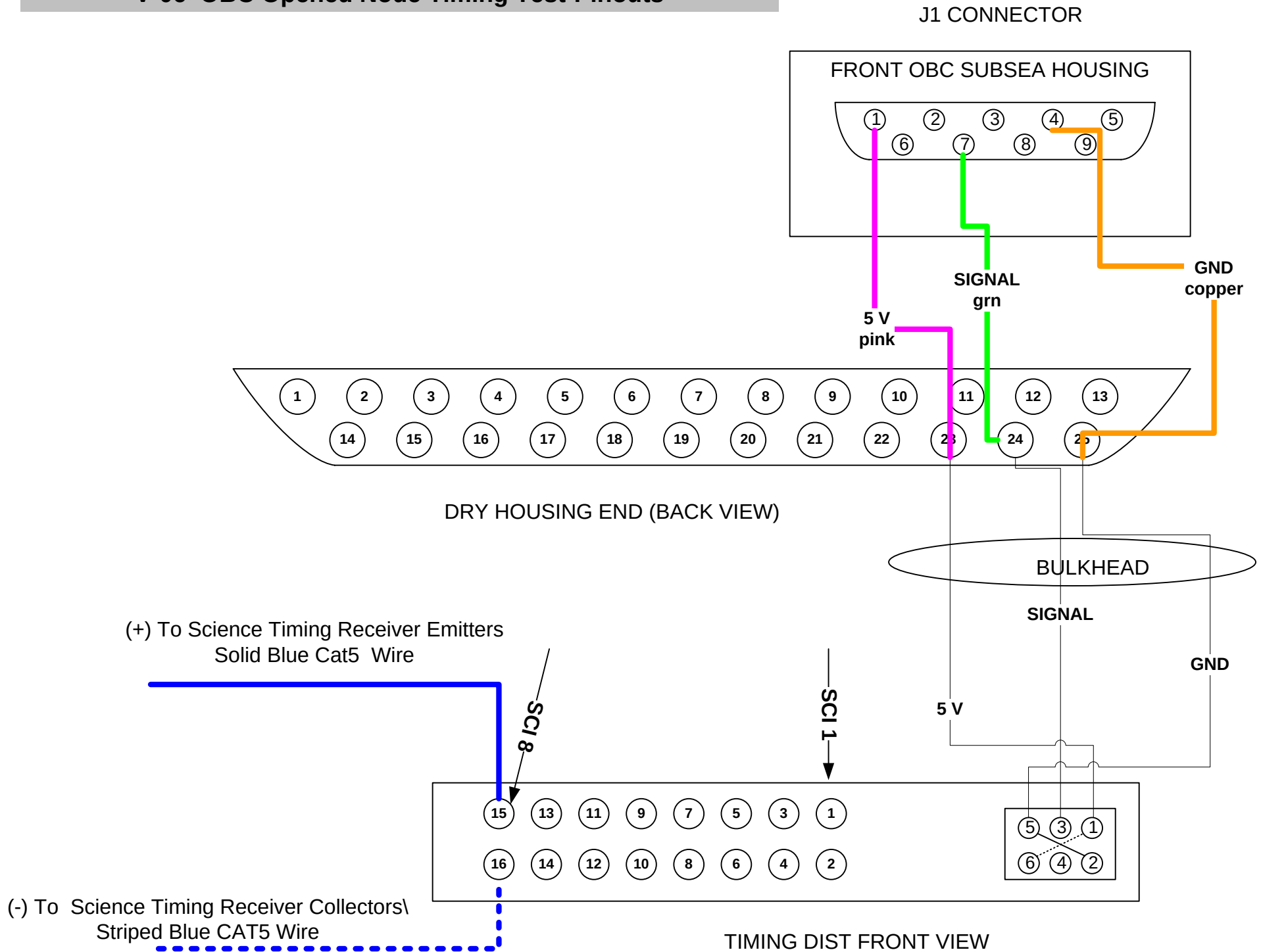


V-06 OBC Opened Node Timing Test-Architecture

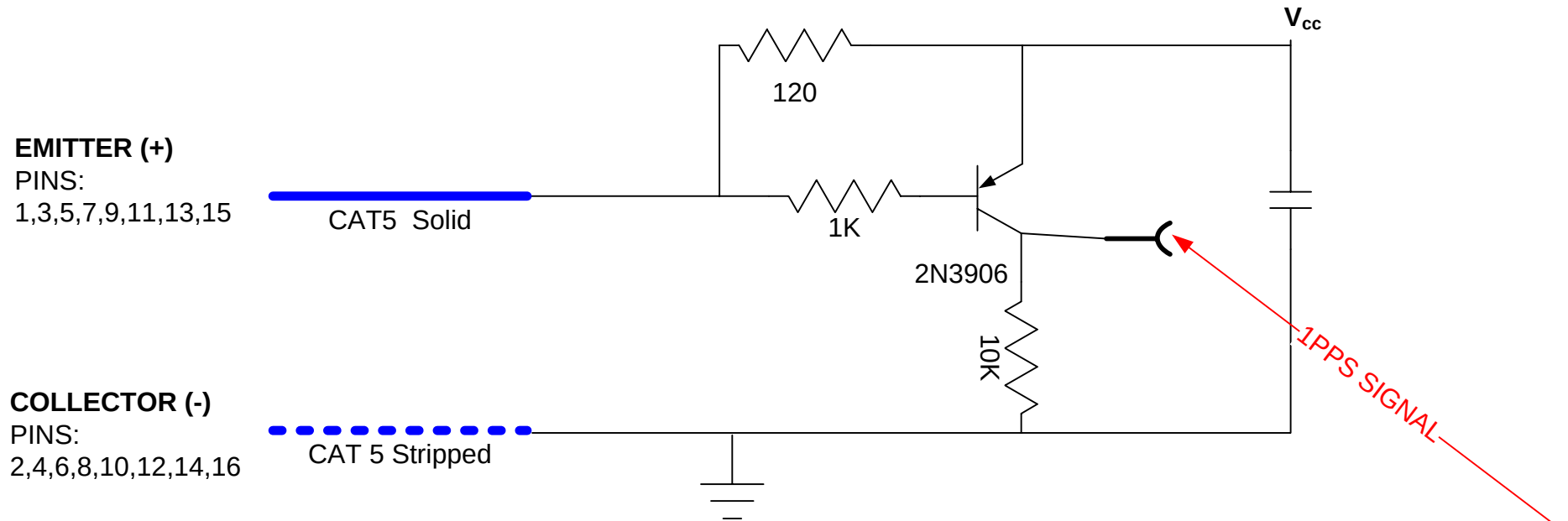
OBC Architecture - Block Diagram for One Node



V-06 OBC Opened Node Timing Test-Pinouts



V-06 OBC Opened Node Timing Test-Science Timing Pulse Receiver

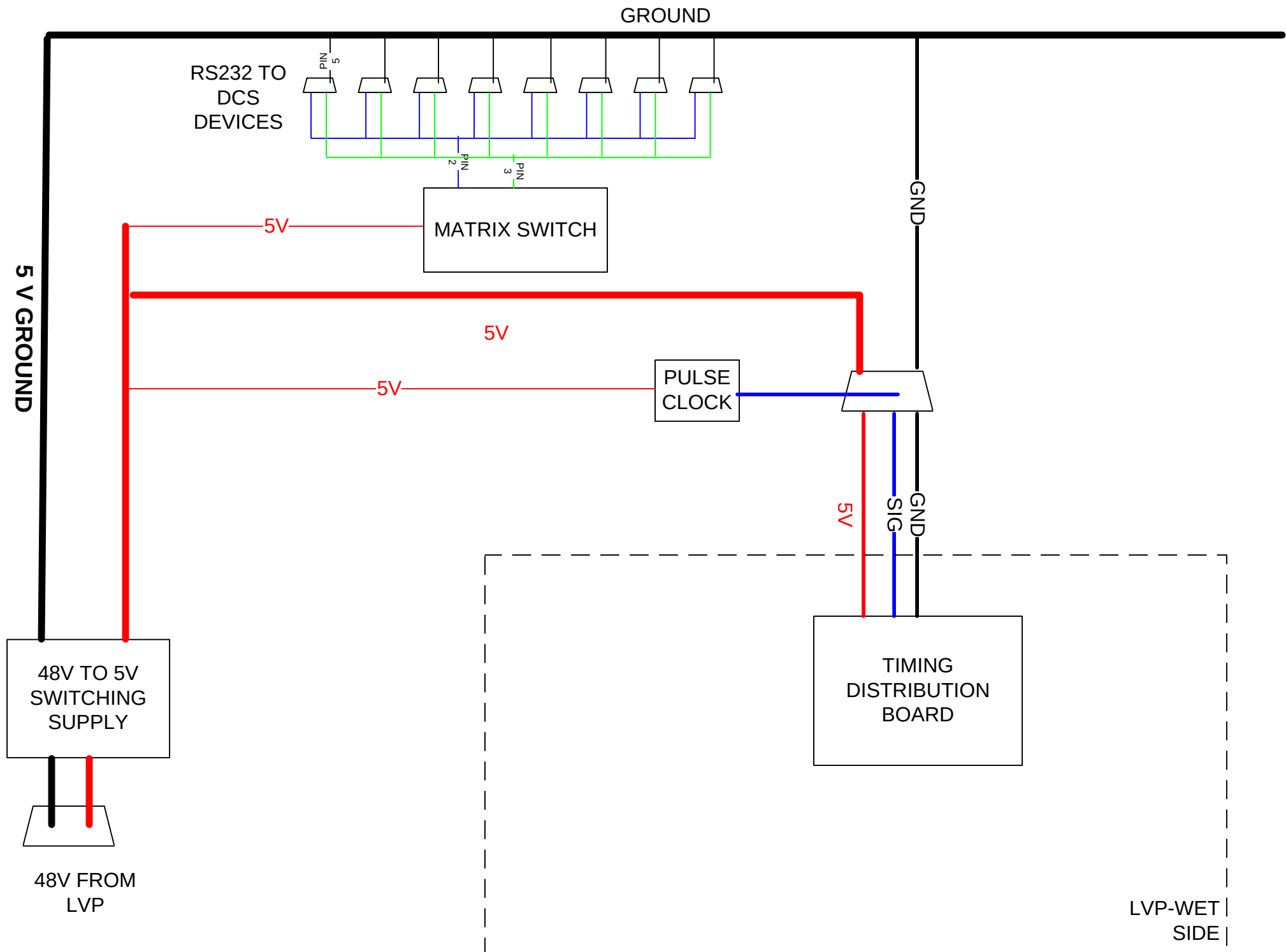


V-06 OBC Opened Node Timing Test-Science Wet Connector Pinout

ETHERNET RJ45 T568B at SCIENCE INSTRUMENT

WET CONNECTOR

01	Orange White	Tx+	←	→	01	Orange White	Tx+
02	Orange	Tx-	←	→	02	Orange	Tx-
03	Green White	Rx+	←	→	03	Green White	Rx+
04	Green	Rx-	←	→	04	Blue	Rx-
05	Blue	TD+	←	→	05	Blue White	TD+
06	Blue White	TD-	←	→	06	Green	TD-
07	Brown White	n/a	←	→	07	Brown White	n/a
08	Brown	n/a	←	→	08	Brown	n/a
09	white	400V-					
10	white	400V+					
11	white	48V+					
12	white	48V-					



V-06 OBC Opened Node Timing Test

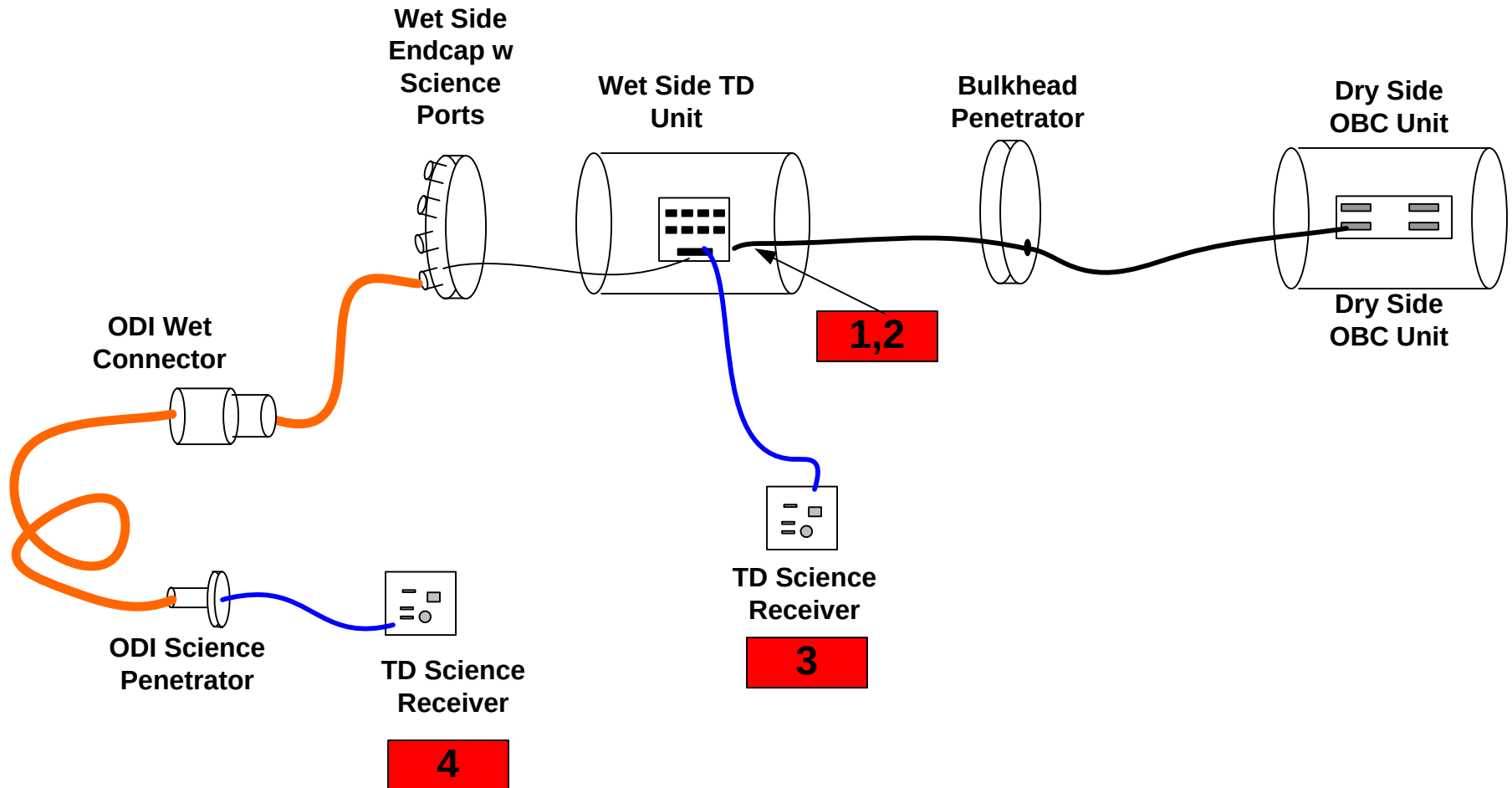
SNAP OF SHORE CLOCK HELP SCREEN

: Help C01A Fri, Apr 1, 2005, 10:25
!T, ?T To set, disp RTC. (dddd hh mm ss then @)
!E@ echos @ 10ms+DELAY later
!UNLOCK allow memory writes and ramtest
?L see LOCK state
!Dhhhh, ?D set/see reply DELAY
!Odddd, ?O set see RTC offset, microseconds
!SR Set Output Relay
?V see 4 voltages
!A Avoid deadman timeout
!Q Quick Lock
A see All lock variables
P see TPDIFF
X see eXternal pulse (DTIC3-DTOC2)
I see TPINT
!Jhhhh jump internal phase by hhhh
!Ihhhhhhhhhhhhhhhh Jam phase Integrator
+Monitor w ?C, ?R
:

SNAP OF SEA CLOCK HELP SCREEN

: Help CKA 2.2 Thu, Apr 14, 2005, 11:34
!T, ?T To set, see RTC. (dddd hh mm ss then @)
!UNLOCK allow memory writes and ramtest
!Dhhhh, ?D set/see DELAY
?Ln see n (=1:7) last LOG entries
!GS sends (pound)G@ on sec, unaddresses
!Ecccc[cr] sends (pound)cccc!E@, unaddresses
?S see, !Sb b=1/0, GPS sync on/off
?G see GPS string
+Monitor w ?C, ?R
:

V-06 OBC Opened Node Timing Test

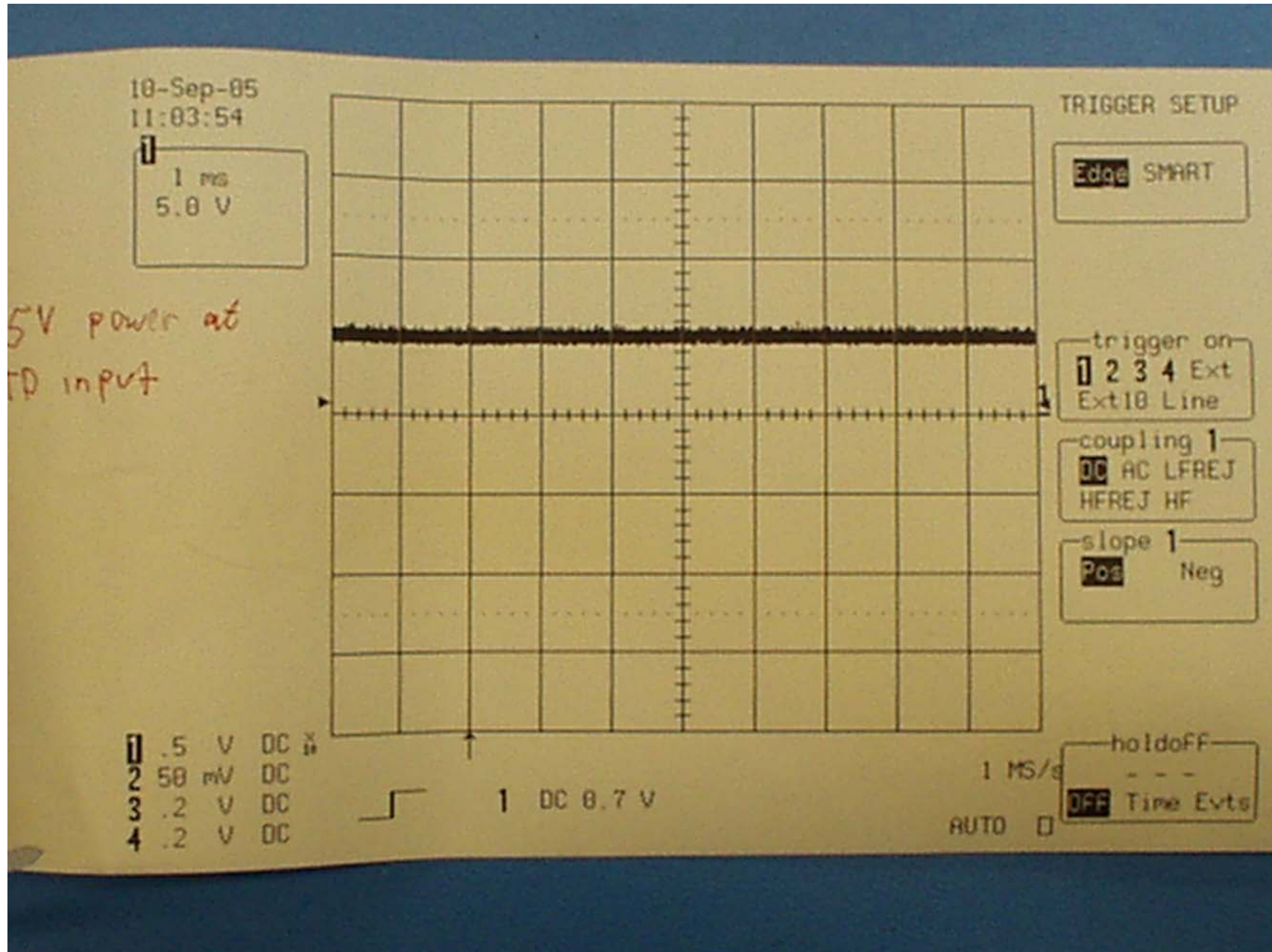


Test Points Shown in RED:

- 1) Power Source at Input to TD Board
- 2) Timing Pulse at Input to TD Board
- 3) Timing Pulse Directly From Output of TD Board
- 4) Timing Pulse at Science Penetrator

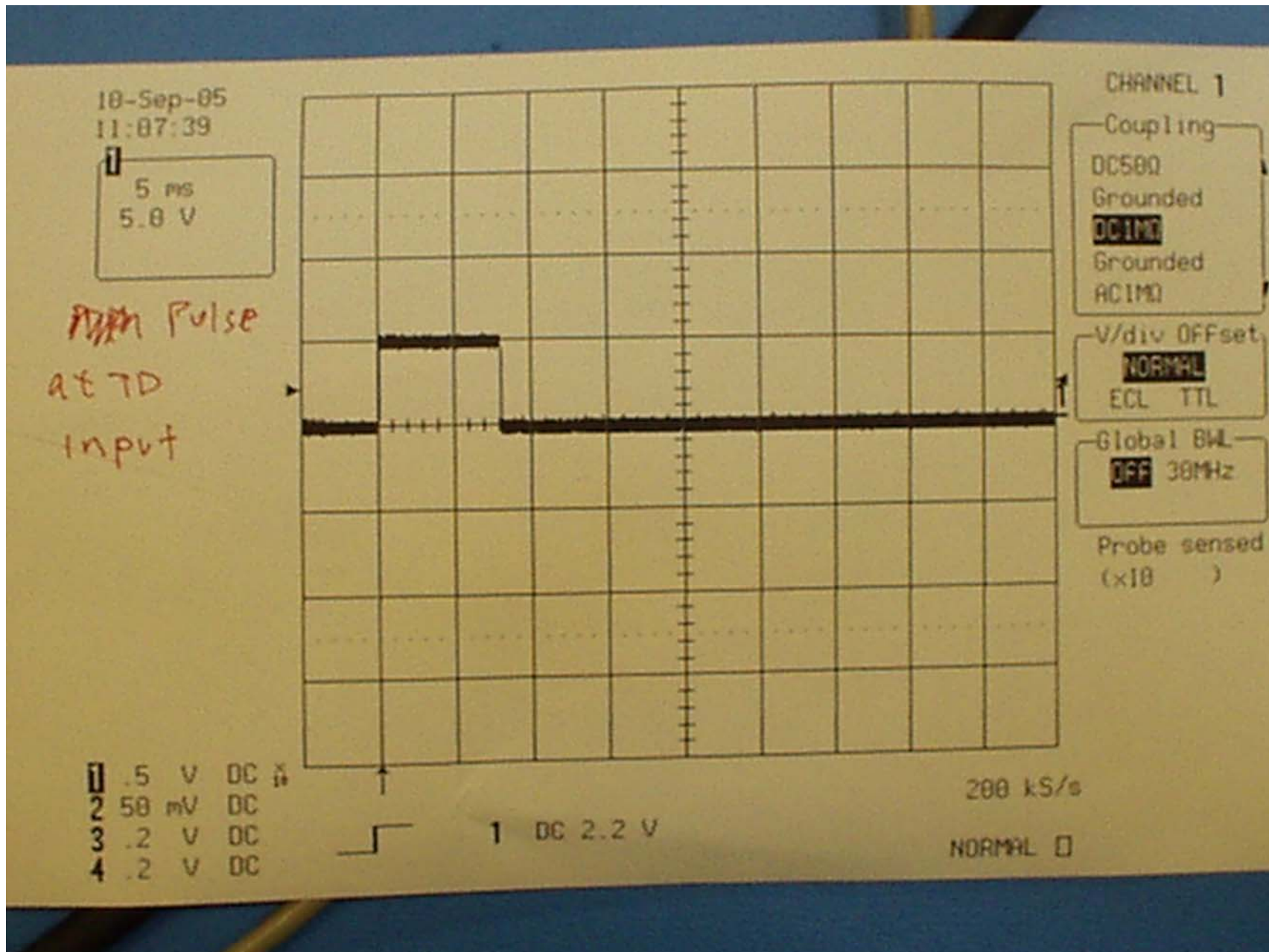
POWER SOURCE INPUT TO TD BOARD AT THE WET SIDE

1



TD SIGNAL FROM DRY SIDE MEASURED AT INPUT TO TD BOARD
ON THE WET SIDE (Measured with TD Science Receiver Circuit)

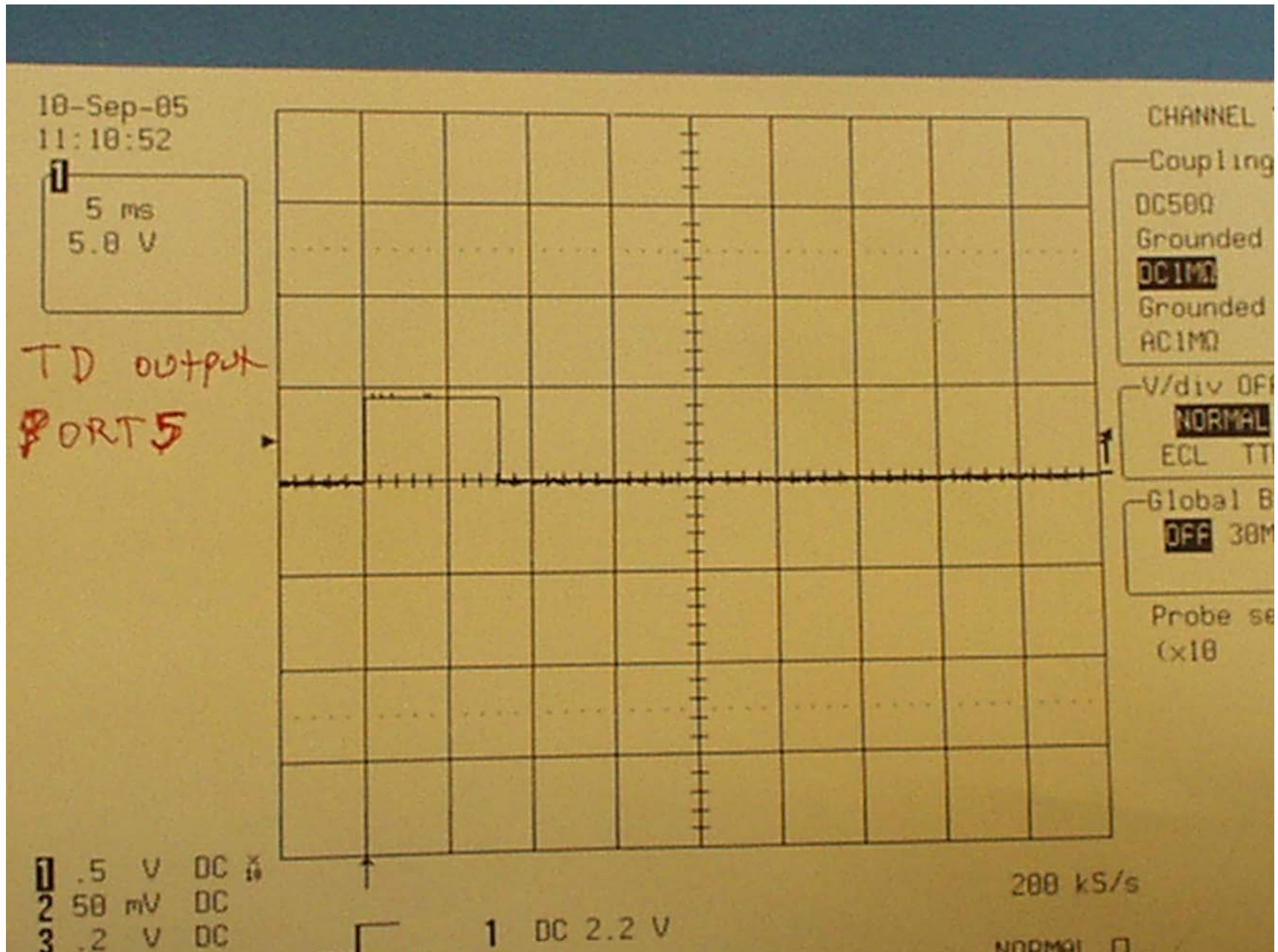
2



V-06 OBC Opened Node Timing Test

TD OUT SIGNAL PORT 5 FROM WET SIDE (Measured with TD
Science Receiver Circuit)

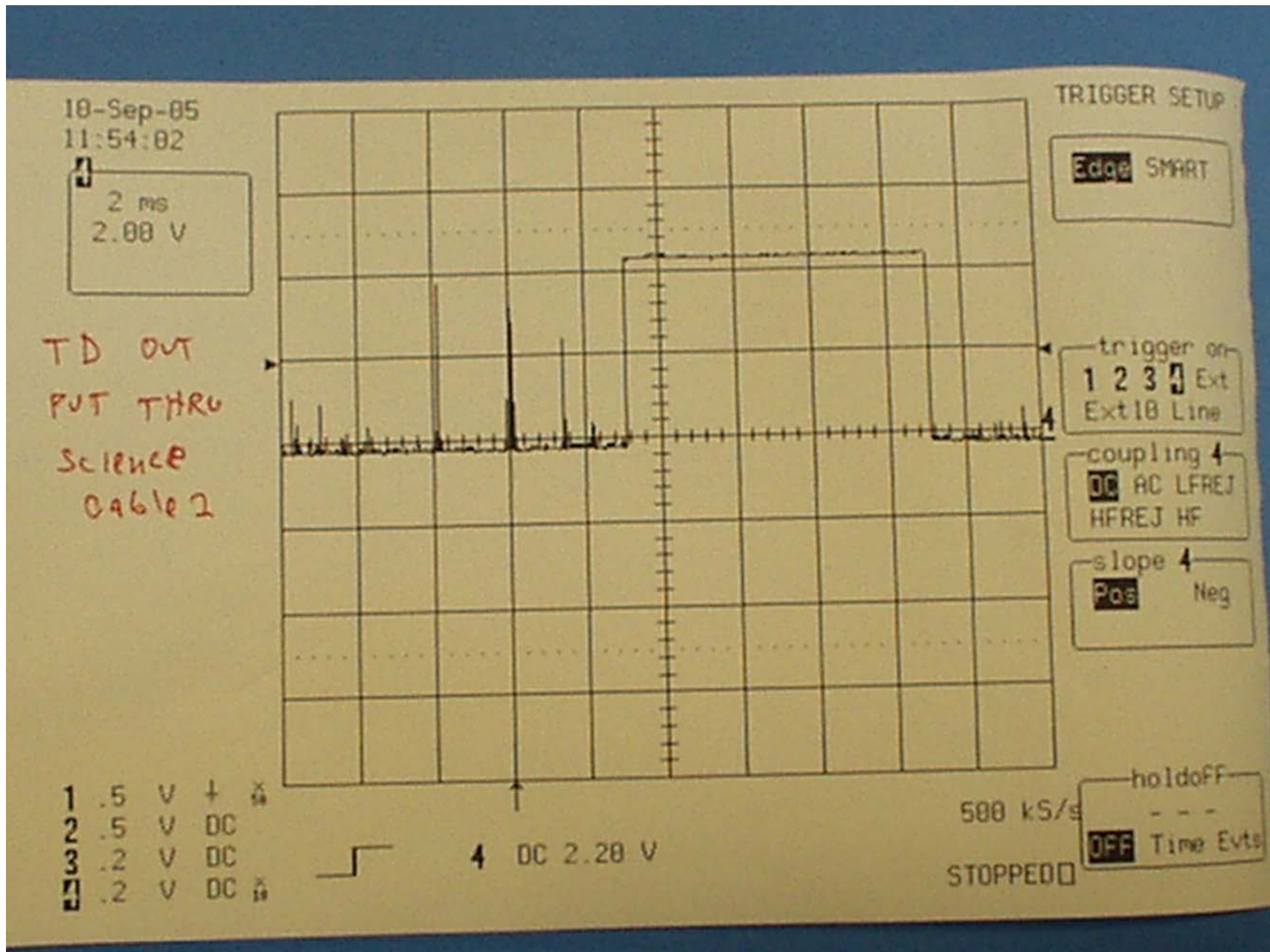
3



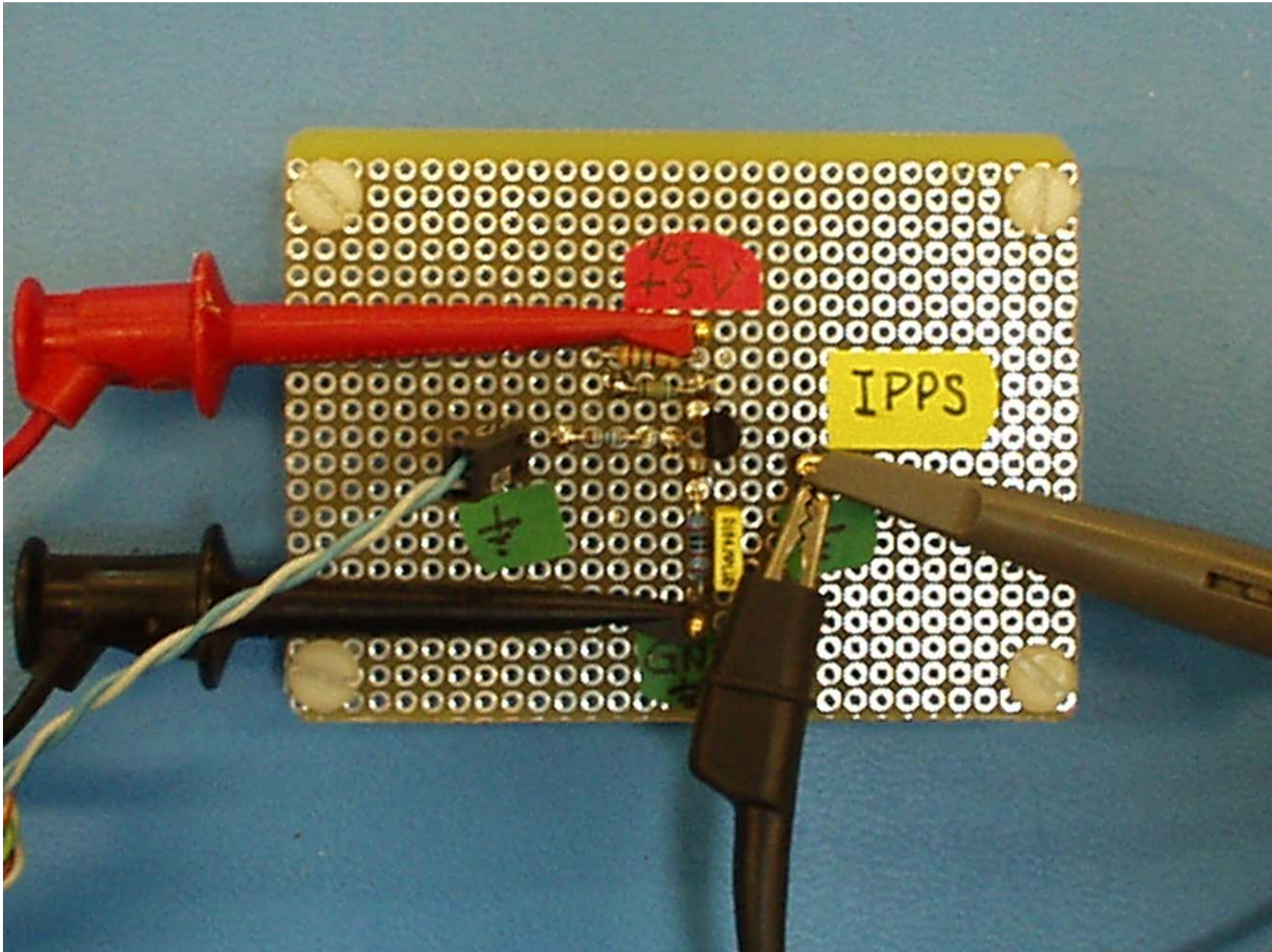
V-06 OBC Opened Node Timing Test

TD Signal Measured at Science Penetrator after Passing Through
ODI Cable (No Ethernet Active) NOTE NOISE

4

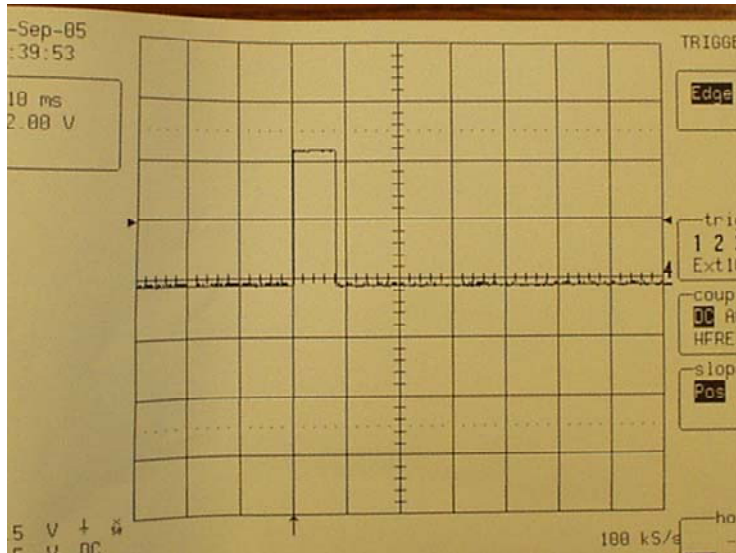


SCIENCE RECEIVER BOARD (LITTLEFIELD)

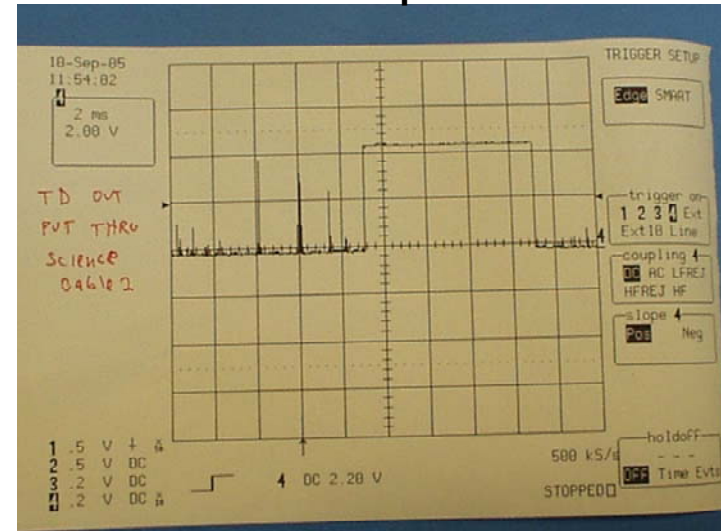


V-06 OBC Opened Node Timing Test

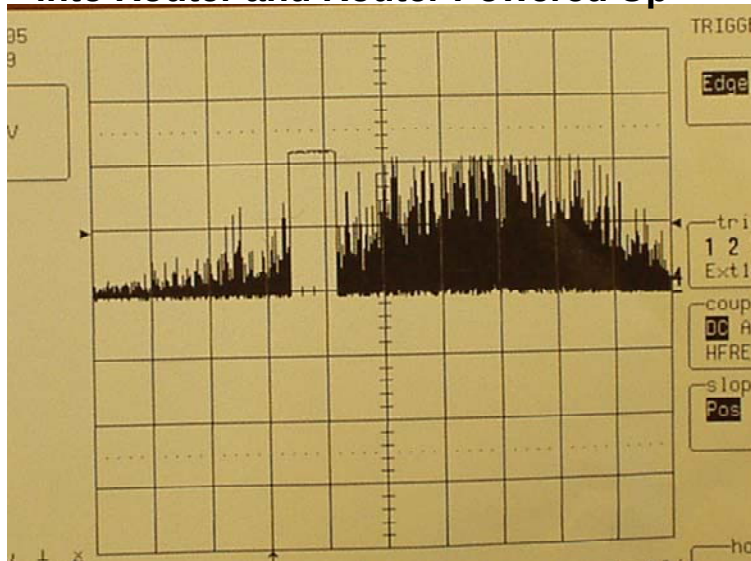
Timing Signal with Y Cables Unplugged



Timing Signal with Y Cables Inserted into router but router powered down



Timing Signal with Y Cables Inserted into Router and Router Powered Up



**V-06 OBC Opened Node Timing Test – Timing Dist (top) to Science
Receiver (bottom)**