

D

C

B

A

D

C

B

A

6

5

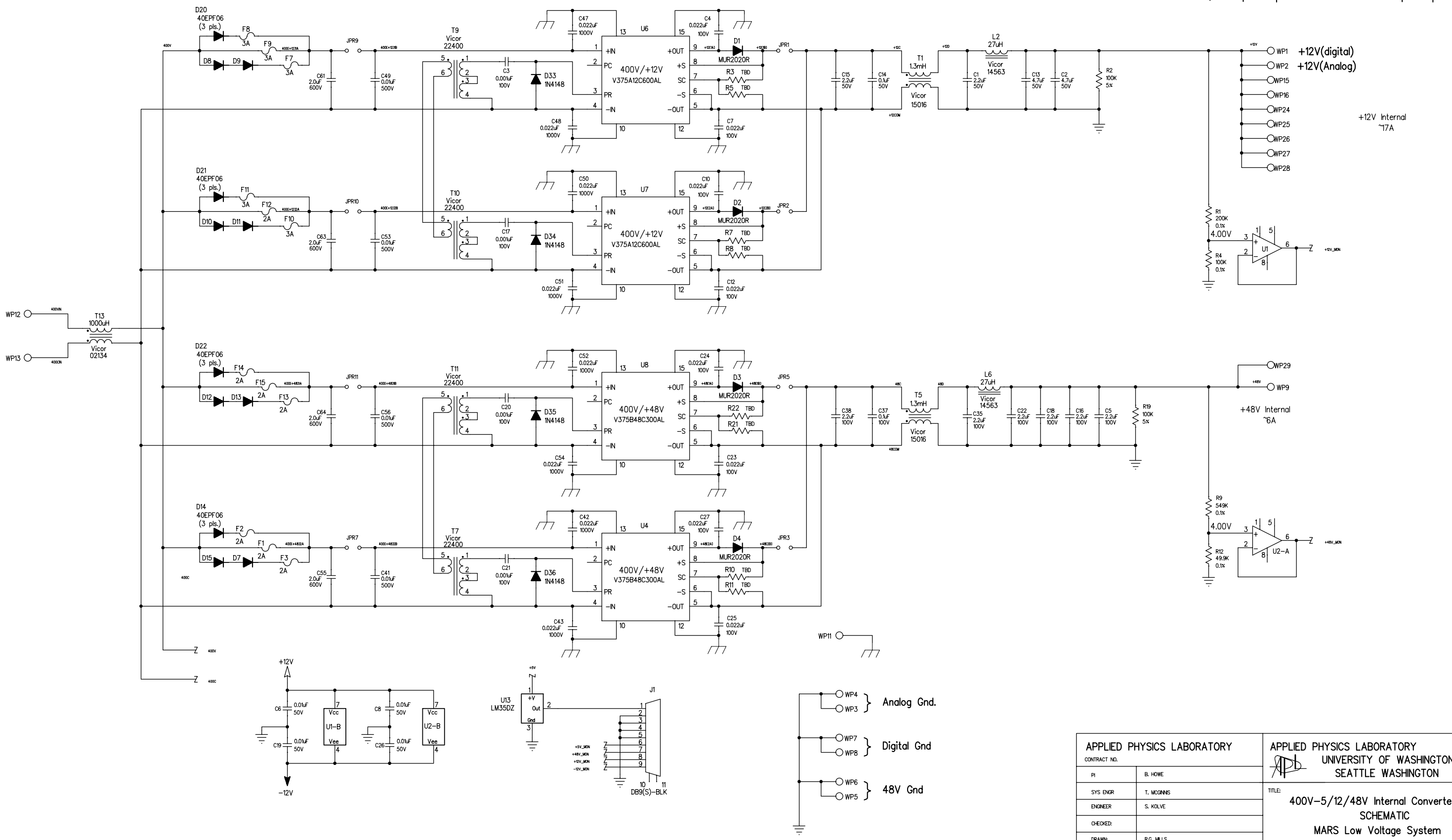
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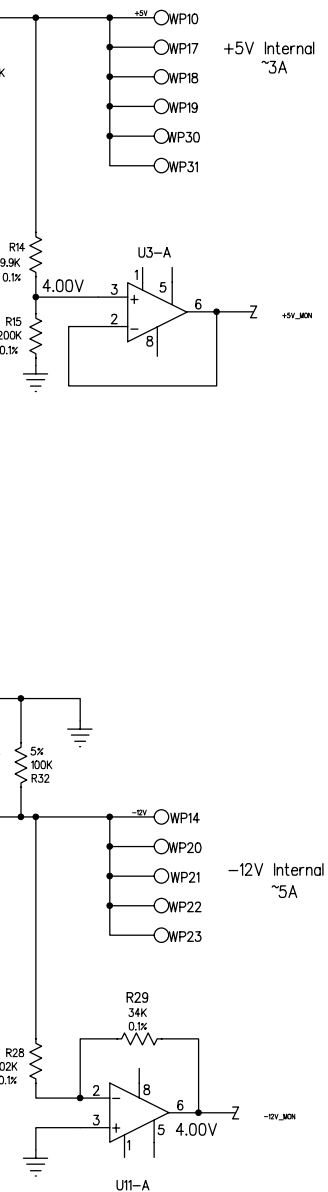
1

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED



APPLIED PHYSICS LABORATORY		APPLIED PHYSICS LABORATORY	
CONTRACT NO.		UNIVERSITY OF WASHINGTON	
P1	B. HOWE	SEATTLE WASHINGTON	
SYS ENGR	T. MCINNIS	TITLE:	
ENGINEER	S. KOLVE	400V-5/12/48V Internal Converter	
CHECKED:		SCHEMATIC	
DRAWN:	R.G. MILLS	MARS Low Voltage System	
APPLICATION DATA		REV:	CODE:
NEXT ASSY.	USED ON	A	98514
		DATE:	DRAWING NO:
		19 January 2005	53856
		Sheet 1 of 2	

The diagram illustrates a complex wiring scheme for a 52-pin connector. It features four 5V power sources, each labeled '+5V Z', connected to a series of resistors (RN1, RN2, RN3, RN4) and a 52-pin connector. The resistors are labeled '1K'. The connector pins are numbered 1 through 52. The wiring shows a complex interconnection between the power sources, resistors, and the connector pins.

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