

D

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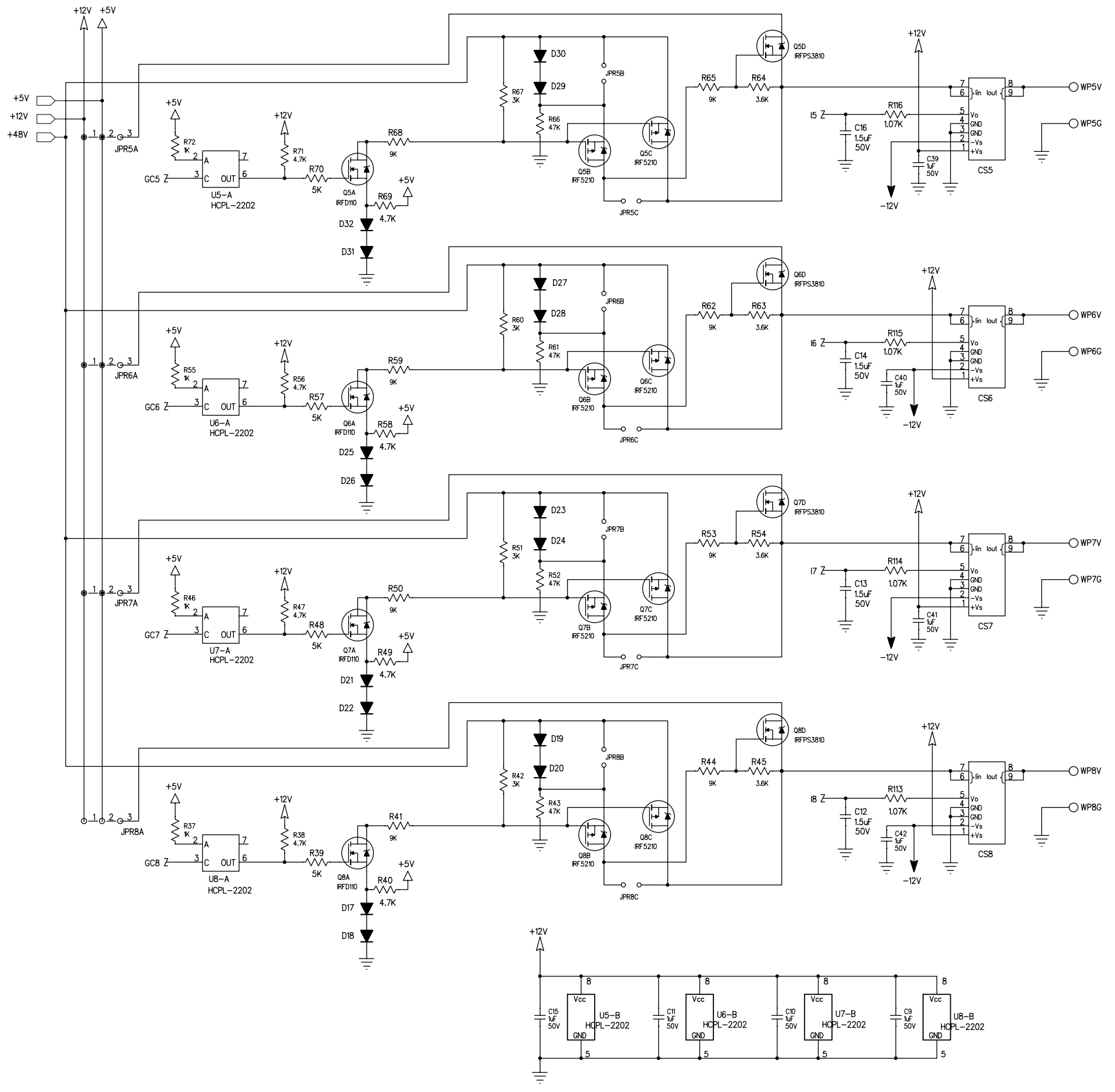
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APPLIED PHYSICS LABORATORY		APPLIED PHYSICS LABORATORY		
CONTRACT NO.		UNIVERSITY OF WASHINGTON		
PI	B. HOWE	SEATTLE WASHINGTON		
SYS ENGR	T. MCGINNIS	TITLE: Internal Load Control Schematic MARS Low Voltage System		
ENGINEER	S. KOLVE			
CHECKED:				
DRAWN:	R.G. MILLS			
APPLICATION DATA		REV:	CODE:	DRAWING NO:
NEXT ASSY.	USED ON	A	98514	53858
		DATE:	9 November 2004	Sheet 2 of 5

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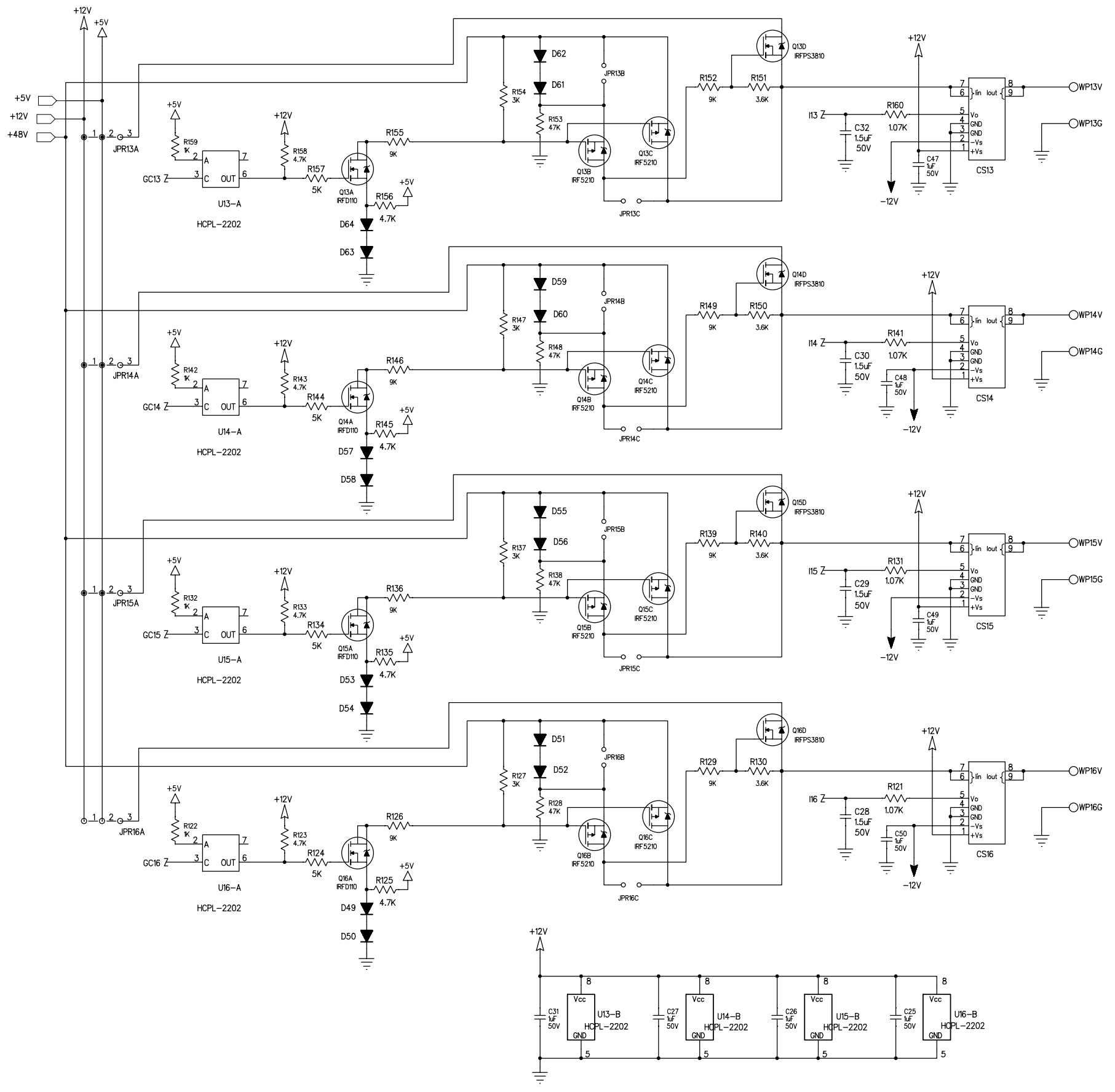
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APPLIED PHYSICS LABORATORY		APPLIED PHYSICS LABORATORY		
CONTRACT NO.		UNIVERSITY OF WASHINGTON		
PI	B. HOWE	SEATTLE WASHINGTON		
SYS ENGR	T. MCGINNIS	TITLE: Internal Load Control Schematic MARS Low Voltage System		
ENGINEER	S. KOLVE			
CHECKED:				
DRAWN:	R.G. MILLS			
APPLICATION DATA		REV:	CODE:	DRAWING NO:
NEXT ASSY.	USED ON	B	98514	53858
		DATE:	9 November 2004	Sheet 4 of 5

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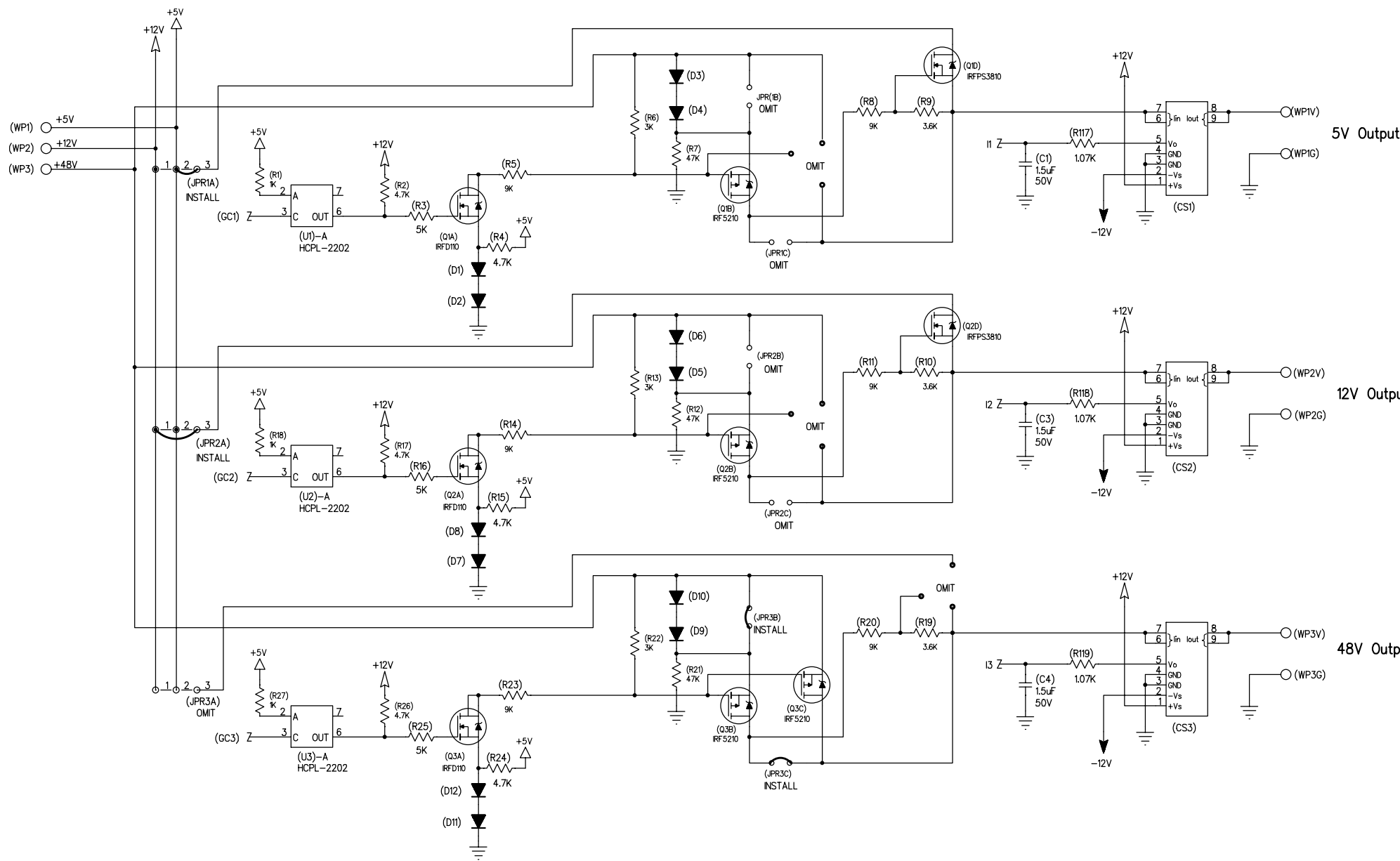
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THIS SHEET IS FOR ILLUSTRATION OF OUTPUT
VOLTAGE CONFIGURATIONS ONLY. DO NOT
PROCESS TO PCB NETLIST.

APPLIED PHYSICS LABORATORY		APPLIED PHYSICS LABORATORY	
CONTRACT NO.		UNIVERSITY OF WASHINGTON	
PI	B. Howe	TITLE: Internal Load Control Schematic MARS Low Voltage System	
SYS ENGR	T. McGinnis		
ENGINEER	S. Kolve		
CHECKED:			
DRAWN:	R. G. Mills	DRAWING NO: 53858	
APPLICATION DATA			
NEXT ASSY.	USED ON	REV: B	CODE: 98514
		DATE: 9 November 2004	Sheet 5 of 5