

!PADS-POWERPCB-V2.0-MILS! DESIGN DATABASE ASCII FILE 1.0

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REMARK ~~BIOSA_CPU_2P1~~ ~~Monday, November 14, 2005,~~ BIOSA_CPU_3P2 -- Wednesday, August 01, 2007, Revision: 2.13.2

PART ITEMS

C1 CAPC1005N
C2 CAPC1608N
C3 CAPC1005N
C4 CAPC1005N
C5 CAPC1005N
C6 CAPC1005N
C7 CAPC1005N
C8 CAPC1005N
C9 CAPC1005N
C10 CAPC1005N
C11 CAPC1005N
C12 CAPC1005N
C13 CAPC1608N
C14 CAPC1608N
C15 CAPC1608N
C16 CAPC1608N
C17 CAPC1005N
C18 CAPC1608N
C19 CAPC3216N
C20 CAPC3216N
C21 CAPC3216N
C22 CAPC3216N
C23 CAPC3216N
C24 CAPC1005N
C25 CAPC3216N
C26 CAPC1005N
C27 CAPC1005N
C28 CAPC1005N
C29 CAPC1005N
C30 CAPC1005N
C31 CAPC1005N
C32 CAPC1005N
C33 CAPC1005N
C34 CAPC1608N
C35 CAPC1608N
C36 CAPC1608N
~~C37 CAPC3225N~~
C38 CAPC1608N
~~C39 CAPC3225N~~
C40 CAPC2012N
C41 CAPC1005N
C42 CAPC1005N
C43 CAPC1005N
C44 CAPC1005N
C45 CAPC1005N
C46 CAPC1005N
C47 CAPC1005N
C48 CAPC1005N
C49 CAPC1005N
C50 CAPC1005N
C51 CAPC1005N
C52 CAPC1005N
C53 CAPC1005N
C54 CAPC1005N
C55 CAPC1005N
C56 CAPC1005N
C57 CAPC1005N
C58 CAPC1005N
C59 CAPC2012N
C60 CAPC1005N
C61 CAPC1005N

C62 CAPC1005N
C63 CAPC1005N
C64 CAPC1005N
C65 CAPC1005N
C66 CAPC1005N
C67 CAPC1005N
C68 CAPC1005N
C69 CAPC1005N
C70 CAPC1005N
C71 CAPC1005N
C72 CAPC1005N
C73 CAPC1005N
C74 CAPC1005N
C75 CAPC1005N
C76 CAPC1005N
C77 CAPC1005N
C78 CAPC1005N
C79 CAPC1608N
C80 CAPC2012N
C81 CAPC3216N
C82 CAPC3225N
C83 CAPC1005N
C84 CAPC2012N
C85 CAPC1005N
C86 CAPC1005N
C87 CAPC2012N
C88 CAPC1005N
C89 CAPC1005N
C90 CAPC2012N
C91 CAPC1005N
C92 CAPC1005N
C93 CAPC2012N
C94 CAPC1005N
C95 CAPC1005N
C96 CAPC2012N
C97 CAPC1005N
C98 CAPC1005N
C99 CAPC2012N
C100 CAPC1005N
C101 CAPC1005N
C102 CAPC2012N
C103 CAPC1005N
C104 CAPC1005N
C105 CAPC2012N
C106 CAPC1005N
C107 CAPC1005N
C108 CAPC1608N
C109 CAPC1005N
C110 CAPC1005N
C111 CAPC1608N
C112 CAPC1005N
C113 CAPC1005N
C114 CAPC1005N
C115 CAPC1005N
C116 CAPC1005N
C117 CAPC1005N
C118 CAPC1005N
C119 CAPC1005N
C120 CAPC1005N
C121 CAPC1005N
C122 CAPC1005N
C123 CAPC1005N
C124 CAPC1005N
C125 CAPC1005N
C126 CAPC1005N
C127 CAPC1005N
C128 CAPC1005N

C129 CAPC1005N
C130 CAPC1005N
C131 CAPC1005N
C132 CAPC1005N
C133 CAPC1005N
C134 CAPC1005N
C135 CAPC1005N
C136 CAPC1005N
C137 CAPC1005N
C138 CAPC1005N
C139 CAPC1005N
C140 CAPC1005N
C141 CAPC1005N
C142 CAPC1005N
C143 CAPC1005N
~~C144 CAPC1005N~~
~~C145 CAPC1005N~~
~~C146 CAPC1608N~~
~~C147 CAPC2012N~~
C148 CAPC1005N
~~C149 CAPC1005N~~
~~C150 CAPC1005N~~
C151 CAPC2012N
C152 CAPC1005N
C153 CAPC1005N
C154 CAPC1005N
C155 CAPC1005N
C156 CAPC3216N
C157 CAPC3225N
C158 CAPC1005N
D1 LED_0603
D2 LED_0603
D3 LED_0603
D4 SOT-563F
D5 SOT-563F
D6 LED_0603
D7 LED_0603
D8 LED_0603
D9 LED_0603
D10 LED_0603
D11 LED_0603
D12 LED_0603
D13 LED_0603
D14 LED_0603
D15 LED_0603
FB1 INDC1005N
FB2 INDC1005N
FB3 INDC1005N
FB4 INDC1005N
FB5 INDC1005N
FB6 INDC1005N
J1 FTSH-110-DV
J2 FTMH-DV-ESA-100
J3 ~~CON-SDCMF109~~**CON_MICRO_SD**
J4 FTMH-DV-ESA-80
J5 FTMH-DV-ESA-100
J6 FTMH-DV-ESA-40
J7 FTSH-1~~13~~**305**-DV
JP1 HDR4
JP2 HDR3
JP3 HDR2
L1 INDC2520N
L2 INDC2520N
OSC1 XTAL_ASV
~~OSC2 XTAL_ABM10~~

Q1 SOT95P280-3N
Q2 SOT95P280-3N
Q3 SOT95P280-3N
R1 RESC1005N
R2 RESC1005N
R3 RESC1005N
R4 RESC1005N
R5 RESC1005N
R6 RESC1005N
R7 RESC1005N
R8 RESC1005N
R9 RESC1005N
R10 RESC1005N
R11 RESC1005N
R12 RESC1005N
R13 RESC1005N
R14 RESC1005N
R15 RESC1005N
R16 RESC1005N
R17 RESC1005N
R18 RESC1005N
R19 RESC1005N
R20 RESC1005N
R21 RESC1005N
R22 RESC1005N
R23 RESC1005N
R24 RESC1608N
R25 RESC1608N
R26 RESC1608N
R27 RESC1005N
R28 RESC1005N
R29 RESC1005N
R30 RESC1005N
R31 RESC1005N
R32 RESC1005N
R33 RESC1005N
R34 RESC1005N
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R64 RESC1005N
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R67 RESC1005N
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R70 RESC1005N
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R91 RESC1005N
R92 RESC1005N
R93 RESC1005N
R94 RESC1005N
R95 RESC1005N
R96 RESC1608N
R97 RESC1005N
R98 RESC1005N
R99 RESC1608N
R100 RESC1608N
R101 RESC1005N
R102 RESC1005N
R103 RESC1608N
R104 RESC1005N
R105 RESC1005N
R106 RESC1005N
R107 RESC1005N
R108 RESC1608N
R109 RESC1608N
R110 RESC1005N
R111 RESC1005N
R112 RESC1608N
R113 RESC1005N
R114 RESC1005N
R115 RESC1005N
R116 RESC1608N
R117 RESC1608N
R118 RESC1005N
R119 RESC1005N
R120 RESC1005N
R121 RESC1005N
R122 RESC1005N
R123 RESC1608N
R124 RESC1608N
R125 RESC1005N
R126 RESC1005N
R127 RESC1005N
R200 RESC1005N
SW1 SW_MS-TS4722
T1 XFMR_PE-H1164

U1 BGA100P17X17-289
U2 SOT95P280-5N
U3 SOT95P280-5N
U4 TSOP65P640-8N
U5 SOT95P280-5N
U6 BGA80P9X15-90
U7 BGA80P8X6-48
U8 BGA100P12X12-144
U9 SOP65X780-16N
~~U10 QFP7X7-48N~~
U11 SOT65P210-6N
U12 SOT95P280-5N
U13 ~~TSOP65P490-8N~~SOT95P280-5N
NET
SIGNAL AC_TERM_RX_1
C33.1 R111.1 R114.1
SIGNAL AC_TERM_RX_2
C31.1 R106.1 R107.1
SIGNAL AC_TERM_RX_3
C29.1 R98.1 R102.1
SIGNAL AC_TERM_RX_4
C27.1 R94.1 R95.1
SIGNAL AC_TERM_TX_1
C32.1 R110.1 R113.1
SIGNAL AC_TERM_TX_2
C30.1 R104.1 R105.1
SIGNAL AC_TERM_TX_3
C28.1 R97.1 R101.1
SIGNAL AC_TERM_TX_4
C26.1 R92.1 R93.1
SIGNAL B0_SIZE
D15.1 R122.1 U1.E15
SIGNAL B1_SIZE
D14.1 R118.2 U1.E14
SIGNAL BOB_SMITH_1
C23.1 R87.1 R88.2 R91.1
SIGNAL BOB_SMITH_2
C22.1 R81.1 R82.1 R83.2
SIGNAL BOB_SMITH_3
C21.1 R75.1 R76.1 R77.2
SIGNAL BOB_SMITH_4
C20.1 R66.1 R67.2 R71.1
SIGNAL BS_1_TERM_1
R84.1 R85.1 R87.2
SIGNAL BS_1_TERM_2
R89.1 R90.1 R91.2
SIGNAL BS_2_TERM_1
R78.1 R79.1 R81.2
SIGNAL BS_2_TERM_2
R80.1 R82.2 R86.1
SIGNAL BS_3_TERM_1
R70.1 R72.1 R75.2
SIGNAL BS_3_TERM_2
R73.1 R74.1 R76.2
SIGNAL BS_4_TERM_1
R64.1 R65.1 R66.2
SIGNAL BS_4_TERM_2
R68.1 R69.1 R71.2
SIGNAL BS_TERM_1
R88.1 T1.23
SIGNAL BS_TERM_2
R83.1 T1.28
SIGNAL BS_TERM_3
R77.1 T1.33
SIGNAL BS_TERM_4
R67.1 T1.38
SIGNAL CONSOLE_RX_5

U1.N16 U9.9
SIGNAL CONSOLE_RX_RS232 5
J6.37 JP1.3 U9.8
SIGNAL CONSOLE_TX 5
U1.N14 U9.11
SIGNAL CONSOLE_TX_RS232 5
J6.38 JP1.1 U9.13
SIGNAL CPU_ADDR_00 5
J2.3 U1.U4 U6.G8 U7.E1 U8.~~A1~~C6
SIGNAL CPU_ADDR_01
J2.5 U1.T4 U6.G9 U7.D1 U8.~~A2-U10.38~~C7
SIGNAL CPU_ADDR_02
J2.9 U1.U3 U6.F7 U7.C1 U8.A3-~~U10.37~~
SIGNAL CPU_ADDR_03
J2.11 U1.T3 U6.F3 U7.A1 U8.A4-~~U10.36~~
SIGNAL CPU_ADDR_04
J2.15 U1.U2 U6.G1 U7.B1 U8.A5
SIGNAL CPU_ADDR_05
J2.17 U1.U1 U6.G2 U7.D2 U8.A7
SIGNAL CPU_ADDR_06
J2.21 U1.T1 U6.G3 U7.C2 U8.A9
SIGNAL CPU_ADDR_07
J2.23 U1.T2 U6.H1 U7.A2 U8.A10
SIGNAL CPU_ADDR_08
J2.27 U1.R1 U6.H2 U7.B5 U8.A11
SIGNAL CPU_ADDR_09
J2.29 U1.R2 U6.J3 U7.A5 U8.~~A12~~E8
SIGNAL CPU_ADDR_10
J2.33 U1.R3 U6.G7 U7.C5 U8.B1
SIGNAL CPU_ADDR_11
J2.35 U1.P1 U6.H9 U7.D5 U8.B3
SIGNAL CPU_ADDR_12
J2.39 U1.P2 U7.B6 U8.B4
SIGNAL CPU_ADDR_13
J2.41 U1.N1 U7.A6 U8.B5
SIGNAL CPU_ADDR_14
J2.45 U1.N2 U7.C6 U8.B6
SIGNAL CPU_ADDR_15
J2.47 U1.N3 U7.D6 U8.B7
SIGNAL CPU_ADDR_16
J2.53 U1.N4 U7.E6
SIGNAL CPU_ADDR_17
J2.55 U1.M1 U7.B2
SIGNAL CPU_ADDR_18
J2.59 U1.M2 U7.C3
SIGNAL CPU_ADDR_19
J2.61 U1.M3 U7.D4
SIGNAL CPU_ADDR_20_BA0
J2.65 U1.P3 U6.J7 U7.D3
SIGNAL CPU_ADDR_21_BA1
J2.67 U1.P4 U6.H8 U7.C4
SIGNAL CPU_CLOCK
R9.2 U1.E1
SIGNAL CPU_CLOCK_UT
R9.1 U4.8
SIGNAL CPU_DATA_00
J2.4 U1.U15 U6.R8 U7.E2 U8.D11-~~U10.10~~
SIGNAL CPU_DATA_01
J2.6 U1.T15 U6.N7 U7.H2 U8.D10-~~U10.9~~
SIGNAL CPU_DATA_02
J2.10 U1.U14 U6.R9 U7.E3 U8.D9-~~U10.8~~
SIGNAL CPU_DATA_03
J2.12 U1.T14 U6.N8 U7.H3 U8.D8-~~U10.7~~
SIGNAL CPU_DATA_04
J2.16 U1.R14 U6.P9 U7.H4 U8.D7-~~U10.5~~
SIGNAL CPU_DATA_05

J2.18 U1.P14 U6.M8 U7.E4 U8.D6~~-U10.4~~
SIGNAL CPU_DATA_06 5
J2.22 U1.U13 U6.M7 U7.H5 U8.D5~~-U10.3~~
SIGNAL CPU_DATA_07
J2.24 U1.T13 U6.L8 U7.E5 U8.D4~~-U10.2~~
SIGNAL CPU_DATA_08
J2.28 U1.R13 U6.L2 U7.F2 U8.D3~~-U10.1~~
SIGNAL CPU_DATA_09
J2.30 U1.P13 U6.M3 U7.G2 U8.D2~~-U10.48~~
SIGNAL CPU_DATA_10 5
J2.34 U1.U12 U6.M2 U7.F3 U8.D1~~-U10.47~~
SIGNAL CPU_DATA_11
J2.36 U1.T12 U6.P1 U7.G3 U8.C12~~-U10.46~~
SIGNAL CPU_DATA_12
J2.40 U1.R12 U6.N2 U7.F4 U8.C11~~-U10.45~~
SIGNAL CPU_DATA_13 5
J2.42 U1.P12 U6.R1 U7.G5 U8.C10~~-U10.44~~
SIGNAL CPU_DATA_14
J2.46 U1.U11 U6.N3 U7.F5 U8.C9~~-U10.41~~
SIGNAL CPU_DATA_15
J2.48 U1.T11 U6.R2 U7.G6 U8.C8~~-U10.40~~
SIGNAL CPU_DATA_16
J2.54 U1.R11 U6.E8
SIGNAL CPU_DATA_17
J2.56 U1.P11 U6.D7
SIGNAL CPU_DATA_18
J2.60 U1.U10 U6.D8
SIGNAL CPU_DATA_19
J2.62 U1.T10 U6.B9
SIGNAL CPU_DATA_20
J2.66 U1.R10 U6.C8
SIGNAL CPU_DATA_21
J2.68 U1.P10 U6.A9
SIGNAL CPU_DATA_22
J2.72 U1.U9 U6.C7
SIGNAL CPU_DATA_23
J2.74 U1.T9 U6.A8
SIGNAL CPU_DATA_24
J2.78 U1.R9 U6.A2
SIGNAL CPU_DATA_25
J2.80 U1.P9 U6.C3
SIGNAL CPU_DATA_26
J2.84 U1.U8 U6.A1
SIGNAL CPU_DATA_27
J2.86 U1.T8 U6.C2
SIGNAL CPU_DATA_28
J2.90 U1.R8 U6.B1
SIGNAL CPU_DATA_29
J2.92 U1.P8 U6.D2
SIGNAL CPU_DATA_30
J2.96 U1.R7 U6.D3
SIGNAL CPU_DATA_31
J2.98 U1.P7 U6.E2
***SIGNAL* CPU_FPGA_TMS**
J7.5 R33.2 U8.L9 U11.4
***SIGNAL* CPU_FPGA_TMS_UT**
R33.1 U1.K17
SIGNAL CPU_GPIO_00 5
J4.4 U8.K1
SIGNAL CPU_GPIO_01 5
J4.6 U8.J12
SIGNAL CPU_GPIO_02 5
J4.10 U8.J11
SIGNAL CPU_GPIO_03 5
J4.12 U8.J9
SIGNAL CPU_GPIO_04 5

J4.16 U8.J6
SIGNAL CPU_GPIO_05 5
J4.18 U8.J5
SIGNAL CPU_GPIO_06 5
J4.22 U8.J4
SIGNAL CPU_GPIO_07 5
J4.24 U8.J2
SIGNAL CPU_GPIO_08 5
J4.28 U8.J1
SIGNAL CPU_GPIO_09 5
J4.30 U8.H11
SIGNAL CPU_GPIO_10 5
J4.34 U8.H9
SIGNAL CPU_GPIO_11 5
J4.36 U8.H8
SIGNAL CPU_GPIO_12 5
J4.40 U8.H7
SIGNAL CPU_GPIO_13 5
J4.42 U8.H6
SIGNAL CPU_GPIO_14 5
J4.46 U8.H4
SIGNAL CPU_GPIO_15 5
J4.48 U8.H3
SIGNAL CPU_GPIO_16 5
J4.52 U8.H2
SIGNAL CPU_GPIO_17 5
J4.54 U8.G12
SIGNAL CPU_GPIO_18 5
J4.58 U8.G11
SIGNAL CPU_GPIO_19 5
J4.60 U8.G10
SIGNAL CPU_GPIO_20 5
J4.64 U8.G9
SIGNAL CPU_GPIO_21 5
J4.66 U8.G8
SIGNAL CPU_GPIO_22 5
J4.70 U8.F8
SIGNAL CPU_GPIO_23 5
J4.72 U8.F4
SIGNAL CPU_GPIO_24 5
J4.3 U8.M8
SIGNAL CPU_GPIO_25 5
J4.5 U8.M7
SIGNAL CPU_GPIO_26 5
J4.9 U8.M6
SIGNAL CPU_GPIO_27 5
J4.11 U8.M5
SIGNAL CPU_GPIO_28 5
J4.15 U8.M4
SIGNAL CPU_GPIO_29 5
J4.17 U8.M3
SIGNAL CPU_GPIO_30 5
J4.21 U8.M2
SIGNAL CPU_GPIO_31 5
J4.23 U8.~~M1~~L8
SIGNAL CPU_GPIO_32 5
J4.27 U8.L~~11~~7
SIGNAL CPU_GPIO_33 5
J4.29 U8.L~~8~~6
SIGNAL CPU_GPIO_34 5
J4.33 U8.~~L7~~E12
SIGNAL CPU_GPIO_35 5
J4.35 U8.L~~6~~4
SIGNAL CPU_GPIO_36 5
J4.39 U8.L~~4~~3
SIGNAL CPU_GPIO_37 5

J4.41 U8.~~L3~~K12
~~SIGNAL~~ CPU_GPIO_38 5
J4.45 U8.~~L2~~K11
~~SIGNAL~~ CPU_GPIO_39 5
J4.47 U8.K~~12~~9
~~SIGNAL~~ CPU_GPIO_40 5
J4.51 U8.K~~11~~8
~~SIGNAL~~ CPU_GPIO_41 5
J4.53 U8.K~~9~~6
~~SIGNAL~~ CPU_GPIO_42 ~~5~~
J4.57 ~~U8.K8~~U1.J16
~~*SIGNAL* CPU_GPIO_43 5~~
~~J4.59 U8.K6~~
~~*SIGNAL* CPU_GPIO_44 5~~
~~J4.63 U8.K5~~
~~*SIGNAL* CPU_GPIO_45 5~~
~~J4.65 U8.K4~~
~~*SIGNAL* CPU_GPIO_46 5~~
~~J4.69 U8.K3~~
~~*SIGNAL* CPU_GPIO_47 5~~
~~J4.71 U8.K2~~
~~SIGNAL~~ CPU_OE_N 5
J2.83 R16.1 U1.T17 U7.G1 U8.B9-~~U10.32~~
~~SIGNAL~~ CPU_TCK
J1.9 R4.2 U1.G14
~~SIGNAL~~ CPU_TDI
J1.5 R2.2 U1.F14
~~SIGNAL~~ CPU_TDO
J1.13 R7.2
~~SIGNAL~~ CPU_TDO_UT
R7.1 U1.F15
~~SIGNAL~~ CPU_TMS
J1.7 R3.2 U1.G15
~~SIGNAL~~ CPU_TRST_N
U1.F16 U2.4
~~SIGNAL~~ CPU_WE_0_N 5
J2.71 U1.M17 U7.A4 U8.~~B12 U10.34~~C2
~~SIGNAL~~ CPU_WE_1_N
J2.73 U1.N17 U8.B10-~~U10.33~~
~~SIGNAL~~ CPU_WE_2_N
J2.77 U1.P17
~~SIGNAL~~ CPU_WE_3_N
J2.79 U1.R17
~~SIGNAL~~ DGND 5
C1.2 C2.2 C3.2 C4.2 C5.2 C6.2 C7.2 C8.2
C9.2 C10.2 C11.2 C12.2 C14.2 C16.2 C17.2 C18.2
C19.2 C20.2 C21.2 C22.2 C23.2 C24.2 C25.2 C26.2
C27.2 C28.2 C29.2 C30.2 C31.2 C32.2 C33.2 C34.2
C35.2 C36.2 C37.2 ~~C38.2 C39.2~~C40.2 C41.2 C42.2 C43.2 C44.2
~~C43.2 C44.2~~C45.2 C46.2 C47.2 C48.2 C49.2 C50.2 C51.2 C52.2
~~C51.2 C52.2~~C53.2 C54.2 C55.2 C56.2 C57.2 C58.2 C59.2 C60.2
~~C59.2 C60.2~~C61.2 C62.2 C63.2 C64.2 C65.2 C66.2 C67.2 C68.2
~~C67.2 C68.2~~C69.2 C70.2 C71.2 C72.2 C73.2 C74.2 C75.2 C76.2
~~C75.2 C76.2~~C77.2 C78.2 C79.2 C80.2 C81.2 C82.2 C83.2 C84.2
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~~C99.2 C100.2~~C101.2 C102.2 C103.2 C104.2 C105.2 C106.2 C107.2 C108.2
~~C107.2 C108.2~~C109.2 C110.2 C111.2 C112.2 C113.2 C114.2 C115.2 C116.2
~~C115.2 C116.2~~C117.2 C118.2 C119.2 C120.2 C121.2 C122.2 C123.2 C124.2
~~C123.2 C124.2~~C125.2 C126.2 C127.2 C128.2 C129.2 C130.2 C131.2 C132.2
~~C131.2 C132.2~~C133.2 C134.2 C135.2 C136.2 C137.2 C138.2 C139.2 C140.2
~~C139.2 C140.2~~C141.2 C142.2 C143.2 ~~C144.2 C145.2 C146~~C148.2 C151.2 C152.2 C153.2 C154.2
~~C147.2 C148.2 C149.2 C150.2 C151.2~~C155.2 C156.2 C157.2 C158.2 D4.4 D5.4 J1.4 J1.6
J1.~~6~~-J1.8 J1.10 J1.11 J1.12 J1.14 J1.16 J1.18 J1.20

~~J1.20~~ J2.7 J2.8 J2.13 J2.14 J2.19 J2.20 J2.25 **J2.26**
J2.26 ~~J2.31~~ J2.32 J2.37 J2.38 J2.43 J2.44 J2.49 **J2.50**
J2.50 ~~J2.51~~ J2.52 J2.57 J2.58 J2.63 J2.64 J2.69 **J2.70**
J2.70 ~~J2.75~~ J2.76 J2.81 J2.82 J2.87 J2.88 J2.93 **J2.94**
~~J2.94 J3.3 J3.6 J3.12 J3.13 J3.14 J3.15 J4.7~~
~~J4.8 J4.13 J4.14 J4.19 J4.20 J4.25 J4.26 J4.31~~
~~J4.32 J4.37 J4.38 J4.43 J4.44 J4.49 J4.50 J4.55~~
~~J4.56 J4.61 J4.62 J4.67 J4.68 J4.73 J4.74 J5.5~~
~~J5.6 J5.9 J5.10 J5.13 J5.14 J5.17 J5.18 J5.21~~
~~J5.22 J5.25 J5.26 J5.29 J5.30 J5.33 J5.34 J5.37~~
~~J5.38 J5.41 J5.42 J5.45 J5.46 J5.49 J5.50 J5.53~~
~~J5.54 J5.55 J5.56 J5.59 J5.60 J5.63 J5.64 J5.67~~
~~J5.68 J5.71 J5.72 J5.75 J5.76 J5.79 J5.80 J5.83~~
~~J5.84 J5.87 J5.88 J5.91 J5.92 J5.95 J5.96 J6.40~~
~~J7.7 J7.9 J7.10 J7.11 J7.17 J7.19 JP1.2 JP1.4~~
~~JP2.1 JP2.3 OSC1.2 OSC2.2 OSC2.4 Q1.2 Q2.2 Q3.2~~
~~R8.2 R19.2 R20.2 R21.2 R27.2 R28.2 R29.2 R39.2~~
~~R40.2 R60.1 R61.1 R115.1 R116.2 R122.2 R126.1 R200.2~~
~~SW1.2 U1.A1 U1.E3 U1.G7 U1.G8 U1.G9 U1.G10 U1.G11~~
~~U1.H7 U1.H8 U1.H9 U1.H10 U1.H11 U1.J7 U1.J8 U1.J9~~
~~U1.J10 U1.J11 U1.K7 U1.K8 U1.K9 U1.K10 U1.K11 U1.L7~~
~~U1.L8 U1.L9 U1.L10 U1.L11 U2.3 U3.3 U4.4 U5.2~~
~~U6.A3 U6.B3 U6.B8 U6.C1 U6.D1 U6.E9 U6.F1 U6.L3~~
~~U6.L9 U6.M1 U6.N1 U6.P3 U6.P8 U6.R3 U7.H1 U7.H6~~
~~U8.A6 U8.B2 U8.B11 U8.E12 U8.F2 U8.F5 U8.F6 U8.F7~~
~~U8.F10 U8.G2 U8.G5 U8.G6 U8.G7 U8.K7 U8.K10 U8.L1~~
~~U9.1 U9.14 U10.6 U10.18 U10.31 U10.42 U11.2 U12.2~~
~~U13.5 U13.6 U13.7 U13.8~~
J3.6 J3.10 J3.11 J3.12 J4.7 J4.8 J4.13 J4.14
J4.19 J4.20 J4.25 J4.26 J4.31 J4.32 J4.37 J4.38
J4.43 J4.44 J4.49 J4.50 J4.55 J4.56 J4.61 J4.62
J4.67 J4.68 J4.73 J4.74 J5.5 J5.6 J5.9 J5.10
J5.13 J5.14 J5.17 J5.18 J5.21 J5.22 J5.25 J5.26
J5.29 J5.30 J5.33 J5.34 J5.37 J5.38 J5.41 J5.42
J5.45 J5.46 J5.49 J5.50 J5.53 J5.54 J5.55 J5.56
J5.59 J5.60 J5.63 J5.64 J5.67 J5.68 J5.71 J5.72
J5.75 J5.76 J5.79 J5.80 J5.83 J5.84 J5.87 J5.88
J5.91 J5.92 J5.95 J5.96 J6.40 J7.2 J7.10 JP1.2
JP1.4 JP2.1 JP2.3 OSC1.2 Q1.2 Q2.2 Q3.2 R8.2
R11.2 R13.2 R19.2 R20.2 R21.2 R27.2 R28.2 R29.2
R60.1 R61.1 R115.1 R116.2 R122.2 R126.1 SW1.2 U1.A1
U1.E3 U1.G7 U1.G8 U1.G9 U1.G10 U1.G11 U1.H7 U1.H8
U1.H9 U1.H10 U1.H11 U1.J7 U1.J8 U1.J9 U1.J10 U1.J11
U1.K7 U1.K8 U1.K9 U1.K10 U1.K11 U1.L7 U1.L8 U1.L9
U1.L10 U1.L11 U2.3 U3.3 U4.4 U5.2 U6.A3 U6.B3
U6.B8 U6.C1 U6.D1 U6.E9 U6.F1 U6.L3 U6.L9 U6.M1
U6.N1 U6.P3 U6.P8 U6.R3 U7.H1 U7.H6 U8.A1 U8.A6
U8.A12 U8.B2 U8.B11 U8.F2 U8.F5 U8.F6 U8.F7 U8.F10
U8.G2 U8.G5 U8.G6 U8.G7 U8.K7 U8.K10 U8.L1 U8.M1
U8.M12 U9.1 U9.14 U11.2 U12.2 U13.2
SIGNAL ENVP_C1N 5
C13.2 U9.4
SIGNAL ENVP_C1P 5
C13.1 U9.2
SIGNAL ENVP_C2N 5
C15.2 U9.6
SIGNAL ENVP_C2P 5
C15.1 U9.5
SIGNAL ENVP_VNEG 5
C16.1 U9.7
SIGNAL ENVP_VPLUS 5
C14.1 U9.3

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*SIGNAL* EXT_FPGA_N
J2.85 U1.U16
*SIGNAL* EXT_FPGA_RUPT 5
J2.89 R2001.1 U1.G16
*SIGNAL* EXT_FPGA_STALL_N 5
J2.91 R187.1 U8.C1
*SIGNAL* FLASH_N 5
U1.P15 U7.F1
*SIGNAL* FPGA_1_N
U1.T16 U8.D12
*SIGNAL* FPGA_2_N
U1.R16 U8.E2
*SIGNAL* FPGA_AVDD 200
C111.1 C112.1 C113.1 FB6.2 U8.E8-U8.G3
*SIGNAL* FPGA_CLOCK
R10.2 U8.F1
*SIGNAL* FPGA_CLOCK_UT
R10.1 U4.7
*SIGNAL* FPGA_N
U1.T16 U8.B8
*SIGNAL* FPGA_RCK
J7.20 U8.L10
*SIGNAL* FPGA_RUPT 5
R2019.1 U1.G17 U8.C5
*SIGNAL* FPGA_TCK
J7.12 U8.J8 U11.6
*SIGNAL* FPGA_TDI
J7.14 U8.M9 U11.3
*SIGNAL* FPGA_TDO
J7.16 R127.2
*SIGNAL* FPGA_TDO_UT
R127.1 U8.J10
*SIGNAL* FPGA_TMS
J7.18 U8.L9 U11.4
*SIGNAL* FPGA_TRST
J7.22 U8.L12 U11.1
*SIGNAL* FPGA_VPN
C38.1 C39.1 J7.8 U8.M12
*SIGNAL* FPGA_VPP
C36.1 C37.1 J7.6 U8.M11
*SIGNAL* GREEN_1_CL 5
D12.2 R112.1
*SIGNAL* GREEN_1_ON
D12.1 U1.B16
*SIGNAL* GREEN_2_CL 5
D10.2 R108.1
*SIGNAL* GREEN_2_ON
D10.1 U1.C16
*SIGNAL* GREEN_3_CL 5
D8.2 R99.1
*SIGNAL* GREEN_3_ON
D8.1 U1.D16
*SIGNAL* GREEN_4_CL 5
D6.2 R96.1
*SIGNAL* GREEN_4_ON
D6.1 U1.E16
*SIGNAL* GREEN_CL 5
D3.2 R26.2
*SIGNAL* GREEN_LED 5
D3.1 Q3.3
*SIGNAL* GREEN_LED_ON 5
Q3.1 R29.1 U1.L16
*SIGNAL* I2C_CLOCK
J4.77 U8.E5

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*SIGNAL* I2C_DATA
J4.75 U8.E3
*SIGNAL* ICE_SRST_N
J1.15 R5.2 U3.2
*SIGNAL* ICE_TRST_N
J1.3 R1.2 U2.2
*SIGNAL* ISET
R116.1 U1.F1
*SIGNAL* LAN_1_RXM
J6.6 T1.25
*SIGNAL* LAN_1_RXP
J6.5 T1.24
*SIGNAL* LAN_1_TXM
J6.2 T1.22
*SIGNAL* LAN_1_TXP
J6.1 T1.21
*SIGNAL* LAN_2_RXM
J6.14 T1.26
*SIGNAL* LAN_2_RXP
J6.13 T1.27
*SIGNAL* LAN_2_TXM
J6.10 T1.29
*SIGNAL* LAN_2_TXP
J6.9 T1.30
*SIGNAL* LAN_3_RXM
J6.22 T1.35
*SIGNAL* LAN_3_RXP
J6.21 T1.34
*SIGNAL* LAN_3_TXM
J6.18 T1.32
*SIGNAL* LAN_3_TXP
J6.17 T1.31
*SIGNAL* LAN_4_RXM
J6.30 T1.36
*SIGNAL* LAN_4_RXP
J6.29 T1.37
*SIGNAL* LAN_4_TXM
J6.26 T1.39
*SIGNAL* LAN_4_TXP
J6.25 T1.40
*SIGNAL* LAN_FXSD 5
R115.2 U1.F3
*SIGNAL* MB_FPGA_TMS 5
J4.63 R32.2
*SIGNAL* MB_FPGA_TMS_UT
R32.1 U1.K16
*SIGNAL* MHZ_25
OSC1.3 U4.1
*SIGNAL* MISC_1
U1.H15 U8.F3
*SIGNAL* MISC_2
U1.H14 U8.B8
*SIGNAL* MPC1_ACT_N
R59.1 U1.D2
*SIGNAL* OSC_VDD
C9.1 C10.1 FB1.2 OSC1.1 OSC1.4
*SIGNAL* PCI_AD_00
J5.3 U1.A16
*SIGNAL* PCI_AD_01 5
J5.4 U1.A15
*SIGNAL* PCI_AD_02
J5.7 U1.C15
*SIGNAL* PCI_AD_03 5
J5.8 U1.B15
*SIGNAL* PCI_AD_04
J5.11 U1.D15

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SIGNAL PCI_AD_05 5
J5.12 U1.A14
SIGNAL PCI_AD_06
J5.15 U1.C14
SIGNAL PCI_AD_07 5
J5.16 U1.B14
SIGNAL PCI_AD_08
J5.19 U1.A13
SIGNAL PCI_AD_09 5
J5.20 U1.C13
SIGNAL PCI_AD_10
J5.23 U1.B13
SIGNAL PCI_AD_11 5
J5.24 U1.D13
SIGNAL PCI_AD_12
J5.27 U1.A12
SIGNAL PCI_AD_13 5
J5.28 U1.C12
SIGNAL PCI_AD_14
J5.31 U1.B12
SIGNAL PCI_AD_15 5
J5.32 U1.D12
SIGNAL PCI_AD_16
J5.35 U1.C9
SIGNAL PCI_AD_17 5
J5.36 U1.A8
SIGNAL PCI_AD_18
J5.39 U1.D9
SIGNAL PCI_AD_19 5
J5.40 U1.B8
SIGNAL PCI_AD_20
J5.43 U1.D8
SIGNAL PCI_AD_21 5
J5.44 U1.A7
SIGNAL PCI_AD_22
J5.47 U1.C7
SIGNAL PCI_AD_23 5
J5.48 U1.B7
SIGNAL PCI_AD_24
J5.51 U1.C6
SIGNAL PCI_AD_25 5
J5.52 U1.B6
SIGNAL PCI_AD_26 5
J5.57 U1.D6
SIGNAL PCI_AD_27 5
J5.58 U1.A5
SIGNAL PCI_AD_28 5
J5.61 U1.C5
SIGNAL PCI_AD_29 5
J5.62 U1.B5
SIGNAL PCI_AD_30 5
J5.65 U1.D5
SIGNAL PCI_AD_31 5
J5.66 U1.A4
SIGNAL PCI_CBEN_0
J5.97 U1.D14
SIGNAL PCI_CBEN_1
J5.93 U1.A11
SIGNAL PCI_CBEN_2
J5.89 U1.B9
SIGNAL PCI_CBEN_3
J5.85 U1.A6
SIGNAL PCI_CLOCK_OUT 5
J5.86 R62.2
SIGNAL PCI_CLOCK_RUN_N
R58.1 U1.B10
SIGNAL PCI_CLOCK_UT_0

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R63.1 U1.A2
* SIGNAL* PCI_CLOCK_UT_1
R62.1 U1.B1
* SIGNAL* PCI_DEVSEL_N
J5.74 R54.1 U1.C11
* SIGNAL* PCI_FRAME_N 5
J5.78 R53.1 U1.D10
* SIGNAL* PCI_GRANT_N 5
J5.90 R49.1 U1.C4
* SIGNAL* PCI_IDSEL 5
J5.98 R61.2 U1.D7
* SIGNAL* PCI_INTERRUPT 5
R198.1 U1.H17
* SIGNAL* PCI_IRDY_N 5
J5.82 R51.1 U1.A9
* SIGNAL* PCI_M66EN
R60.2 U1.E4
* SIGNAL* PCI_PARITY
J5.70 U1.C8
* SIGNAL* PCI_PBMS
R48.1 U1.D3
* SIGNAL* PCI_PERR_N
J5.73 R57.1 U1.B11
* SIGNAL* PCI_REF_CLOCK
R63.2 U1.D4
* SIGNAL* PCI_REQUEST_N 5
J5.94 R50.1 U1.B4
* SIGNAL* PCI_SERR_N
J5.77 R56.1 U1.A10
* SIGNAL* PCI_STOP_N
J5.81 R55.1 U1.D11
* SIGNAL* PCI_TRDY_N
J5.69 R52.1 U1.C10
* SIGNAL* PECL_CLOCK_N 200
D5.3 J4.76 R47.2 U8.E11
* SIGNAL* PECL_CLOCK_P 200
D4.3 J4.78 R47.1 U8.F11
* SIGNAL* PLUS1P5V 200
C108.1 C109.1 C110.1 C136.1 C137.1 C138.1 C139.1 C140.1
C141.1 C142.1 C143.1 C157.1 C158.1 FB6.1 L2.1 U8.A8
U8.C4 U8.E1 U8.E10 U8.H1 U8.H5 U8.H12 U8.J7 U13.5
* SIGNAL* PLUS1P8V 200
C82.1 C83.1 C84.1 C85.1 C86.1 C96.1 C97.1 C98.1
FB2.1 FB4.1 L1.1 U12.5
* SIGNAL* PLUS1P8V_ANALOG 200
C40.1 C41.1 C42.1 C43.1 C44.1 C45.1 C46.1 C47.1
C48.1 C49.1 C87.1 C88.1 C89.1 FB2.2 U1.E5 U1.E6
U1.F5 U1.F6 U1.G5 U1.G6 U1.H5 U1.H6 U1.J5 U1.J6
* SIGNAL* PLUS1P8V_DIGITAL 200
C60.1 C61.1 C62.1 C63.1 C64.1 C65.1 C66.1 C67.1
C68.1 C69.1 C99.1 C100.1 C101.1 C151.1 FB4.2 U1.E7
U1.E8 U1.E9 U1.E10 U1.F7 U1.F8 U1.F9 U1.F10 U1.H12
U1.H13 U1.J12 U1.J13 U1.K12 U1.K13 U1.M7 U1.M8 U1.M9
U1.N7 U1.N8 U1.N9
* SIGNAL* PLUS2P5V 200
C35.1 C108.1 C109.1 C110.1 C136.1 C137.1 C138.1 C139.1
C140.1 C141.1 C142.1 C143.1 C147.1 FB6.1 J7.24 J7.26
U8.A8 U8.C4 U8.E1 U8.E10 U8.H1 U8.H5 U8.H12 U8.J7
U13.4
* SIGNAL* PLUS3P3V 5
C1.1 C2.1 C4.1 C5.1 C6.1 C7.1 C8.1 C11.1
C12.1 C17.1 C18.1 C19.1 C34.1 C81.1 C90.1 C91C35.1 C81.1 C90.1
C91.1 C92.1 C102.1 C103.1 C104.1 C114.1 C115.1 C116.1 C117.1
C117.1 C118.1 C119.1 C120.1 C121.1 C122.1 C123.1 C124.1 C125.1
C125.1 C126.1 C127.1 C128.1 C129.1 C130.1 C131.1 C132.1 C133.1
C133.1 C134.1 C135.1 C144.1 C145.1 C146.1 C148.1 D4.6 D5.6C148.1 C152.1 C153.1 C154.1

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C155.1

C156.1 D4.6 D5.6 FB1.1 FB3.1 FB5.1 J1.1 J1.2 ~~J2.1 J2.2 J2.99~~

J2.1 J2.2 J2.99 J2.100 J3.4 J4.1 J4.2 J4.79 ~~J4.80 J5.1 J5.2~~

~~J5. J4.80 J5.1 J5.2 J5.99~~ J5.100 J7. ~~2 J7.4 JP2.2 JP3.1 R1.1 R26~~ JP2.2 JP3.1

R1.1 R2.1 R3.1 R4.1 R5.1 R12.2 R14.2 R15. ~~2 R16.2 R17.2~~

~~R18.2 R30.1 R16.2 R17.2 R18.2 R36.2~~ R37.2 R38.2 ~~R41.2 R42.2 R43.2 R44~~R39.2 R40.2

~~R45.2 R46.2~~R48.2 R49.2 R50.2 R51.2 R52.2 R53.2 R54.2 R55.2

~~R54.2 R55.2~~R56.2 R57.2 R58.2 R59.2 R118.1 U2R200.1 U2.5 U3.5

~~U3.5~~U4.2 U4.6 U5.5 U6.A7 U6.B2 U6.B7 U6.C9 U6.D9

U6. ~~D9 U6.~~E1 U6.F9 U6.L1 U6.L7 U6.M9 U6.N9 U6.P2 U6.P7

U6. ~~P7 U6.~~R7 U7.G4 U8. ~~A2 U8.B12 U8.~~E4 U8.E6 U8.E7 U8.E9 ~~U8.H10~~

~~U8.J3 U8.L5 U8.M10 U9.12 U9.15 U9.16 U10.19 U10.43~~

~~U11.5 U12.1 U12.4 U13.1 U13.2~~

U8.H10 U8.J3 U8.L2 U8.L5 U8.L10 U8.L11 U8.M10 U9.12

U9.15 U9.16 U11.5 U12.1 U12.4 U13.1 U13.4

SIGNAL PLUS3P3V_ANALOG 10

C24.1 C25.1 C50.1 C51.1 C52.1 C53.1 C54.1 C55.1

C56.1 C57.1 C58.1 C59.1 C105.1 C106.1 C107.1 FB5.2

R119.1 R121.1 T1.3 T1.8 T1.13 T1.18 U1.K5 U1.K6

U1.L5 U1.L6 U1.M5 U1.M6 U1.N5 U1.N6

SIGNAL PLUS3P3V_DIGITAL 200

C70.1 C71.1 C72.1 C73.1 C74.1 C75.1 C76.1 C77.1

C78.1 C79.1 C80.1 C93.1 C94.1 C95.1 FB3.2 U1.E11

U1.E12 U1.E13 U1.F11 U1.F12 U1.F13 U1.G12 U1.G13 U1.L12

U1.L13 U1.M10 U1.M11 U1.M12 U1.M13 U1.N10 U1.N11 U1.N12

U1.N13

SIGNAL PLUS3P3V_LED 5

JP3.2 R24.1 R25.1 R26.1 R96.2 R99.2 R100.2 R103.2

R108.2 R109.2 R112.2 R117.2 R123.2 R124.2

SIGNAL POWER_ON_RESET_N 5

U2.1 U3.1 U5.1

SIGNAL RED_CL 5

D1.2 R24.2

SIGNAL RED_LED 5

D1.1 Q1.3

SIGNAL RED_LED_ON 5

Q1.1 R27.1 U1.K14

SIGNAL RESET_IN_N 5

J2.95 SW1.1 U5.4

SIGNAL RESET_N 5

U1.A3 U1.A17 U3.4

SIGNAL RXM_1

R111.2 T1.16 U1.H4

SIGNAL RXM_2

R106.2 T1.15 U1.J4

SIGNAL RXM_3

R98.2 T1.6 U1.K4

SIGNAL RXM_4

R94.2 T1.5 U1.L4

SIGNAL RXP_1

R114.2 T1.17 U1.H3

SIGNAL RXP_2

R107.2 T1.14 U1.J3

SIGNAL RXP_3

R102.2 T1.7 U1.K3

SIGNAL RXP_4

R95.2 T1.4 U1.L3

SIGNAL SDI_CLOCK

R23.2 U1.T7

SIGNAL SDO_CLOCK_UT

R22.1 R23.1 U1.U7

SIGNAL SDRAM_CAS_N

U1.T5 U6.K7

SIGNAL SDRAM_CLOCK

R22.2 U6.J1

SIGNAL SDRAM_CS_N

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U1.P5 U6.J8
* SIGNAL* SDRAM_PU
R14.1 U6.J2
* SIGNAL* SDRAM_RAS_N
U1.R5 U6.J9
* SIGNAL* SDRAM_SDQM_0
U1.U6 U6.K9
* SIGNAL* SDRAM_SDQM_1
U1.T6 U6.K1
* SIGNAL* SDRAM_SDQM_2
U1.R6 U6.F8
* SIGNAL* SDRAM_SDQM_3
U1.P6 U6.F2
* SIGNAL* SDRAM_WE_N
U1.U5 U6.K8
* SIGNAL* SD_CARD_CS_NSD_CLK_CLK_SCLK
U1.R16 U10.35
* SIGNAL* SD_CARD_INTR_N5
R17.1 U1.H16 U10.11
* SIGNAL* SD_CD
J3.11 R45.1 U10.26
* SIGNAL* SD_CD_DATA_3
J3.1 R35.2 R43.1
* SIGNAL* SD_CD_DATA_3_UT
R35.1 U10.16
* SIGNAL* SD_CLOCK
J3.5 R33.2 R41.1
* SIGNAL* SD_CLOCK_UT
R33.1 U10.20
* SIGNAL* SD_CMD
J3.2 R34.2 R42.1
* SIGNAL* SD_CMD_UT
R34.1 U10.17
* SIGNAL* SD_DATA_0
J3.7 R32.2 R38.1
* SIGNAL* SD_DATA_0_UT
R32.1 U10.21
* SIGNAL* SD_DATA_1
J3.8 R31.2 R37.1
* SIGNAL* SD_DATA_1_UT
R31.1 U10.22
* SIGNAL* SD_DATA_2
J3.9 R36.2 R44.1
* SIGNAL* SD_DATA_2_UT
R36.1 U10.15
* SIGNAL* SD_HCKI
R39.1 U10.28
* SIGNAL* SD_WP
J3.10 R46.1 U10.27
* SIGNAL* SD_XADK
R30.2 U10.30
* SIGNAL* SD_XTI
C149.1 OSC2.1 U10.13
* SIGNAL* SD_XTO
C150.1 OSC2.3 U10.14
* SIGNAL* SD_XTYP2
R40.1 U10.39
J3.5 U8.F9
* SIGNAL* SD_CMD_CMD_DI
J3.3 R36.1 U8.G1
* SIGNAL* SD_DAT0_DATA_DO
J3.7 R40.1 U8.K4
* SIGNAL* SD_DAT1_IRQ_IRQ 5

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J3.8 R39.1 U1.H16 U8.K3
SIGNAL SD_DAT2_RW_NC
J3.1 R38.1 U8.K2
SIGNAL SD_DAT3_NC_CS
J3.2 R37.1 U8.G4
SIGNAL SD_DETECT_N
J3.9 R200.2 U8.K5
SIGNAL STALL_N
R15.1 U1.P16 U8.C3
SIGNAL SW1P5 5
L2.2 U13.3
SIGNAL SW1P8 5
L1.2 U12.3
SIGNAL SYSTEM_RESET_N 5
C3.1 J2.97 R6.2 U8.F12 ~~U10.12~~
SIGNAL SYSTEM_RESET_N_UT
R6.1 U1.U17
SIGNAL TCK 5
J4.69 J7.1 R11.1 R34.2 U8.J8 U11.6
SIGNAL TCK_UT
R34.1 U1.J14
SIGNAL TDI 5
J4.65 J7.9 R31.2 U8.M9 U11.3
SIGNAL TDI_UT
R31.1 U1.K15
SIGNAL TDO 5
J4.59 J7.3 R127.2 U1.J17
SIGNAL TDO_UT
R127.1 U8.J10
SIGNAL TERM_1
J6.32 R90.2
SIGNAL TERM_2
J6.31 R89.2
SIGNAL TERM_3
J6.28 R85.2
SIGNAL TERM_4
J6.27 R84.2
SIGNAL TERM_5
J6.24 R86.2
SIGNAL TERM_6
J6.23 R80.2
SIGNAL TERM_7
J6.20 R79.2
SIGNAL TERM_8
J6.19 R78.2
SIGNAL TERM_9
J6.16 R74.2
SIGNAL TERM_10
J6.15 R73.2
SIGNAL TERM_11
J6.12 R72.2
SIGNAL TERM_12
J6.11 R70.2
SIGNAL TERM_13
J6.8 R69.2
SIGNAL TERM_14
J6.7 R68.2
SIGNAL TERM_15
J6.4 R65.2
SIGNAL TERM_16
J6.3 R64.2
SIGNAL TESTEN
R8.1 U1.F17
SIGNAL TRST 5
J4.71 J7.8 R13.1 R35.2 U8.L12 U11.1

***SIGNAL* TRST_UT**

R35.1 U1.J15

SIGNAL TXM_1
R110.2 T1.19 U1.H2
SIGNAL TXM_2
R104.2 T1.12 U1.J2
SIGNAL TXM_3
R97.2 T1.9 U1.K2
SIGNAL TXM_4
R92.2 T1.2 U1.L2
SIGNAL TXP_1
R113.2 T1.20 U1.H1
SIGNAL TXP_2
R105.2 T1.11 U1.J1
SIGNAL TXP_3
R101.2 T1.10 U1.K1
SIGNAL TXP_4
R93.2 T1.1 U1.L1
SIGNAL UART_PU 5
R12.1 U1.L14 U1.L15 U1.M14 U1.M16
SIGNAL UART_RTS_N 5
R210.1 U1.M15

***SIGNAL* VPUMP**

C36.1 C37.1 J7.7 U8.M11

SIGNAL WAN_FXSD 5
R125.1 R126.2 U1.F2
SIGNAL WAN_GREEN_CL 5
D14.2 R123.1
SIGNAL WAN_LOS 5
J6.39 R125.2
SIGNAL WAN_RXM
J6.34 R120.2 U1.G4
SIGNAL WAN_RXP
J6.33 R120.1 U1.G3
SIGNAL WAN_TXM
J6.36 R119.2 U1.G2
SIGNAL WAN_TXP
J6.35 R121.2 U1.G1
SIGNAL WAN_YELLOW_CL 5
D15.2 R124.1
SIGNAL YELLOW_1_CL 5
D13.2 R117.1
SIGNAL YELLOW_1_ON
D13.1 U1.B17
SIGNAL YELLOW_2_CL 5
D11.2 R109.1
SIGNAL YELLOW_2_ON
D11.1 U1.C17
SIGNAL YELLOW_3_CL 5
D9.2 R103.1
SIGNAL YELLOW_3_ON
D9.1 U1.D17
SIGNAL YELLOW_4_CL 5
D7.2 R100.1
SIGNAL YELLOW_4_ON
D7.1 U1.E17
SIGNAL YELLOW_CL 5
D2.2 R25.2
SIGNAL YELLOW_LED 5
D2.1 Q2.3
SIGNAL YELLOW_LED_ON 5
Q2.1 R28.1 U1.L17
End

