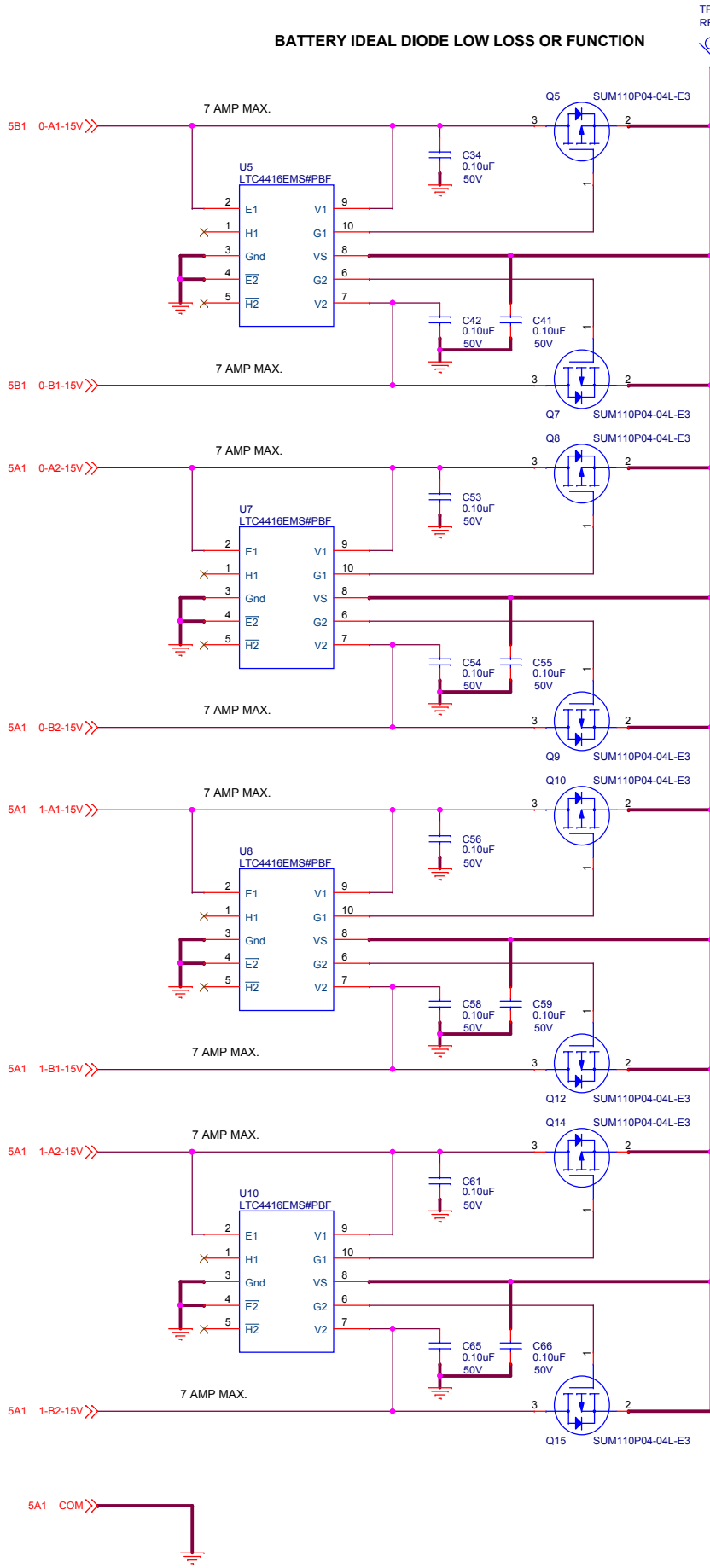
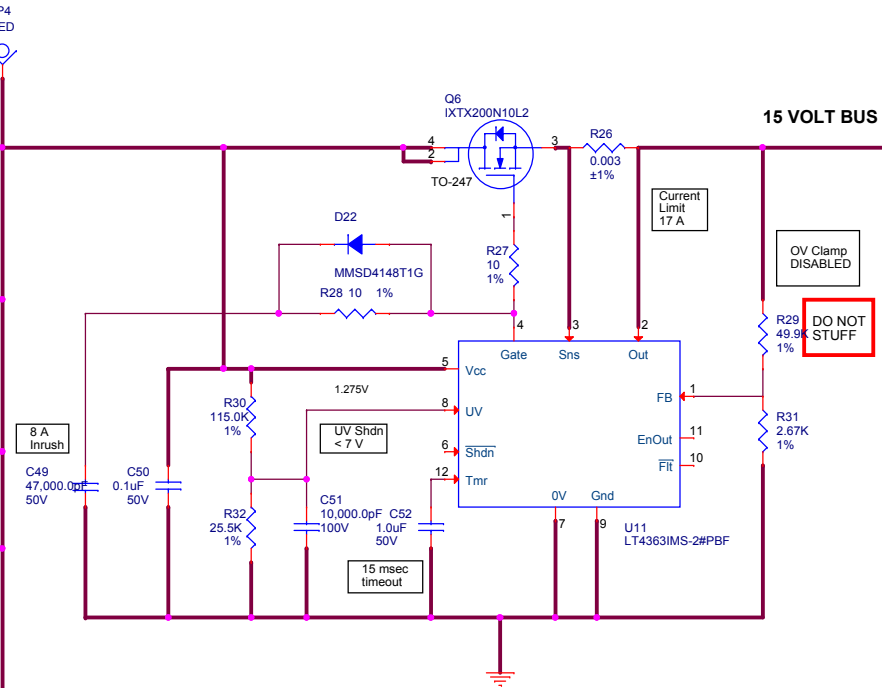


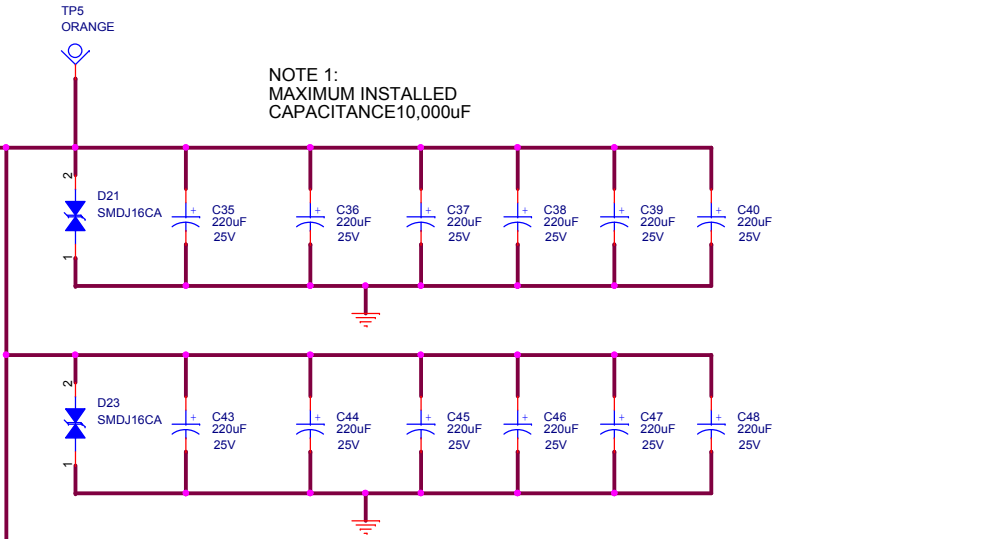
BATTERY IDEAL DIODE LOW LOSS OR FUNCTION



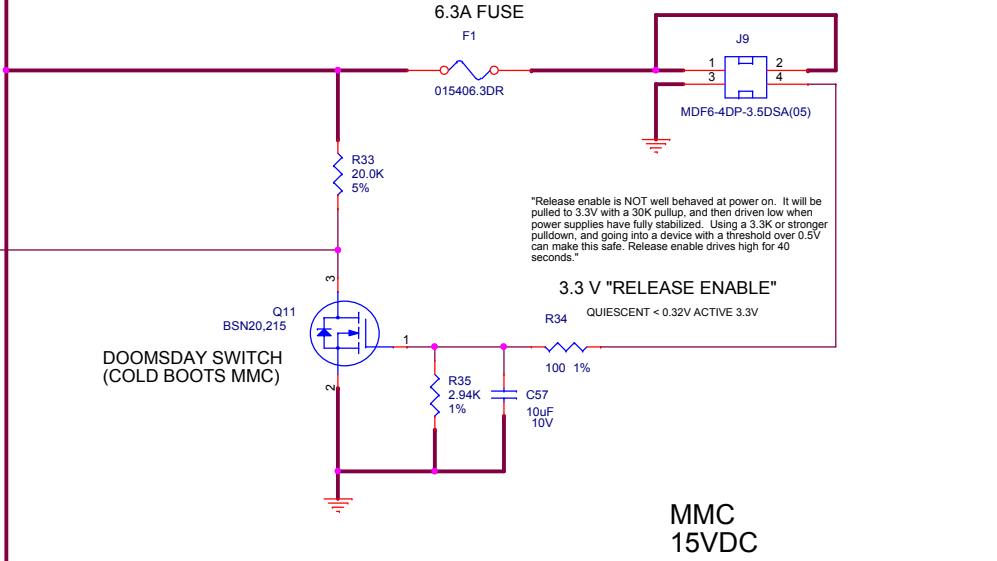
BATTERY PLUG-IN TRANSIENT PROTECTION
(WITH THANKS TO ED MELLINGER)



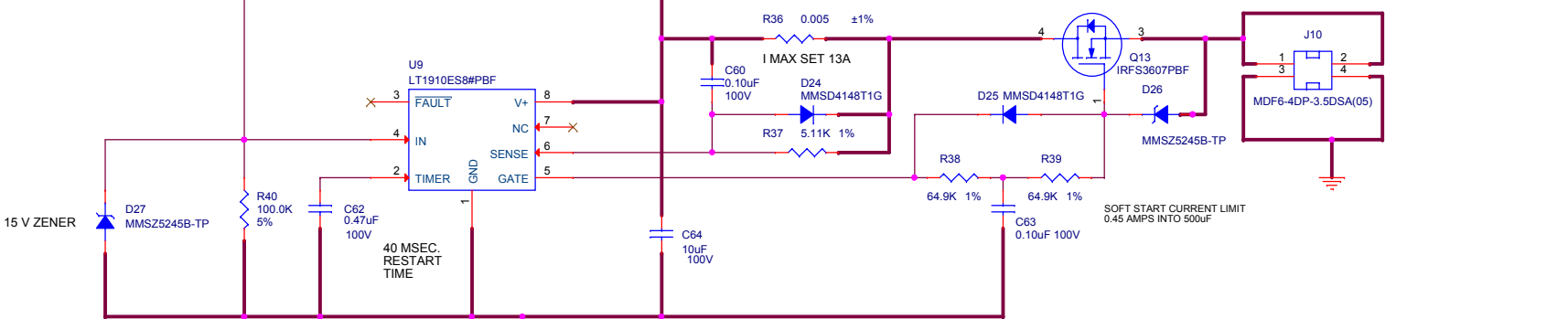
NOTE 1:
MAXIMUM INSTALLED
CAPACITANCE 10,000uF



BENTHOS MODEM
15VDC



MMC
15VDC



REVISION HISTORY:
A - 28AUG15 INITIAL RELEASE
B - 01SEP15 REVISED POST CDR.
C - 21SEP15 REVISED POST PCB LAYOUT. MATCHES AS-BUILT.
D - 26OCT15 ADDED DO NOT STUFF.

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M B A R I

Title

CCE BATTERY POWER MANAGEMENT PCB - PAGE 2

Size	Engineer: Mark Chaffey EMAIL: chma@mbari.org (831) 775-1708	Number	10021142-06-D	Rev	D
C	Date: Thursday, August 04, 2016	Filename	<Filename>	Sheet	2 of 3