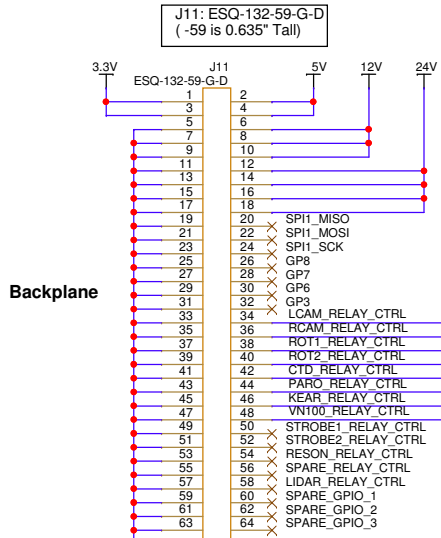
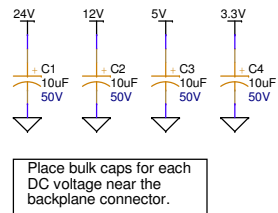




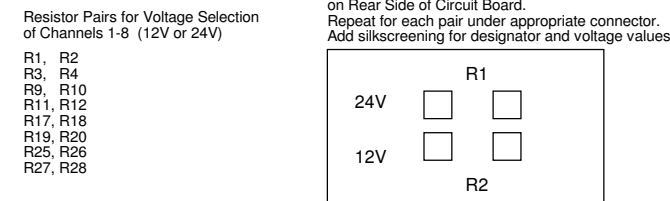
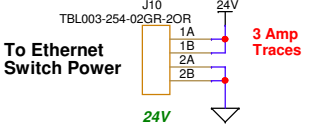
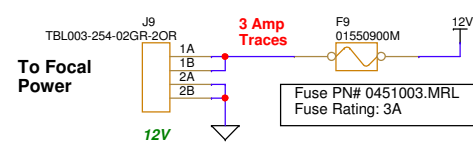
Revision B-2

Backplane connector J11 has been mirrored horizontally w/r to the older board revisions. This reflects the updated (correct) footprint of the ESQ series.

The physical location of each node is the same, however, the pins numbers have been swapped. For example, 3.3V is now on pins 1 and 3, whereas with the incorrect footprint they were on 2 and 4. 3.3V and the multiple ground connections are still on the outer row near the silkscreening.



Place bulk caps for each DC voltage near the backplane connector.



Suggested Layout for Voltage Selection Resistors on Rear Side of Circuit Board. Repeat for each pair under appropriate connector. Add silkscreening for designator and voltage values.

Schematic Notes:

- General design notes in boxes.
- Connector destinations in bold.
- Additional silkscreen comments in italicized green.

Channel	Device	Volts	Fuse
1	LCAM	24V	1A
2	RCAM	24V	1A
3	ROT1	24V	2.5A
4	ROT2	24V	2.5A
5	CTD	24V	1A
6	KEAR	24V	1A
7	PARO	12V	2A
8	VN100	12V	1A



Schematic Name: OI - LASS			
Page Title: Low Pwr Relay Board		Engineer: James McClure	
Project Name: Ocean Imaging	Group Name: OceanImaging	Designer: James McClure	
System: OI LASS	Subsystem: OI Power Can	Last Updated By: cckey	
Drawing Number: 100221838	Release: B-2	Version: 6	Status: Active
Page Modify Date: Monday, October 23, 2023	Check In Date: 10/20/2023 11:18:26 AM		Size: C
		Sheet 1 of 1	