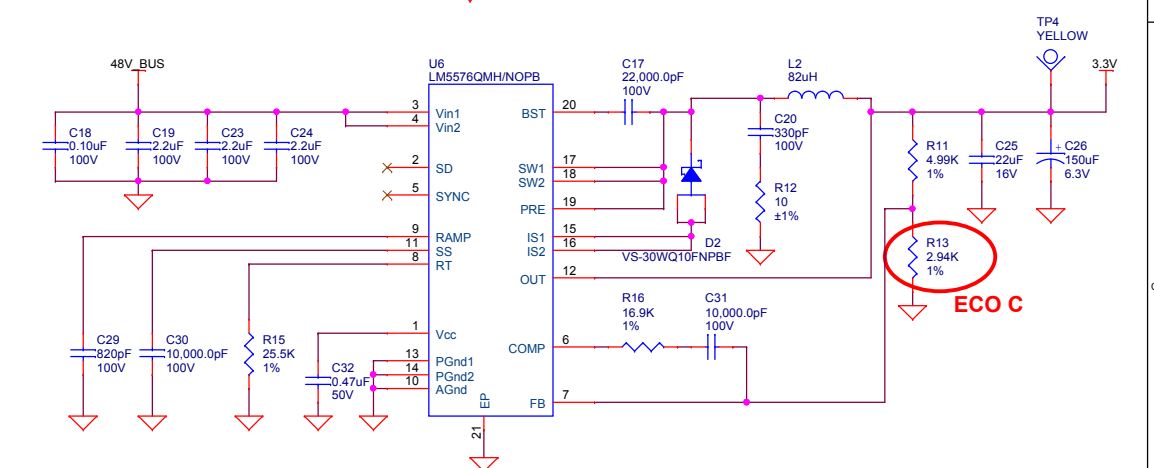
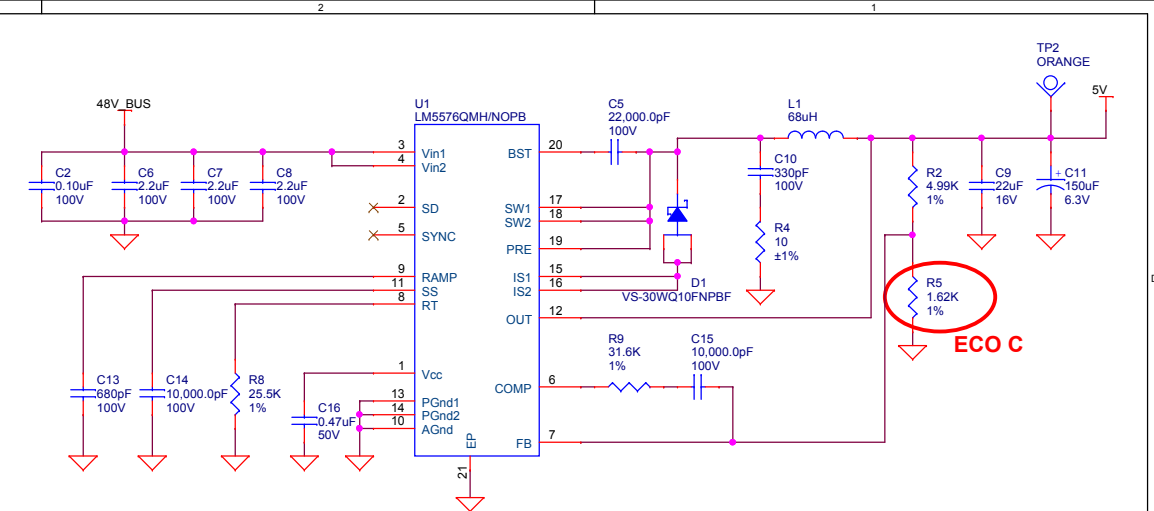
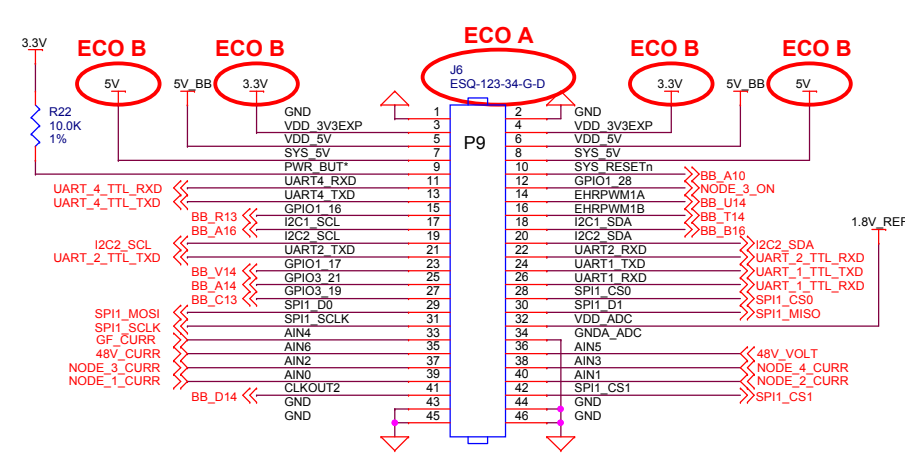


[illegible]

A	New Headers for J5 and J6
B	Logic Power to Capes
C	Logic Power Swap
D	Current Sense Circuit Modification



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Title Gateway Node DC-DC Converter Cape				Number <Doc>		
Size C	Engineer Chad Key		Layout <Layout Eng.>		Filename <Filename>	Rev A1
	Date: Monday, September 08, 2014		Sheet 1 of 1			