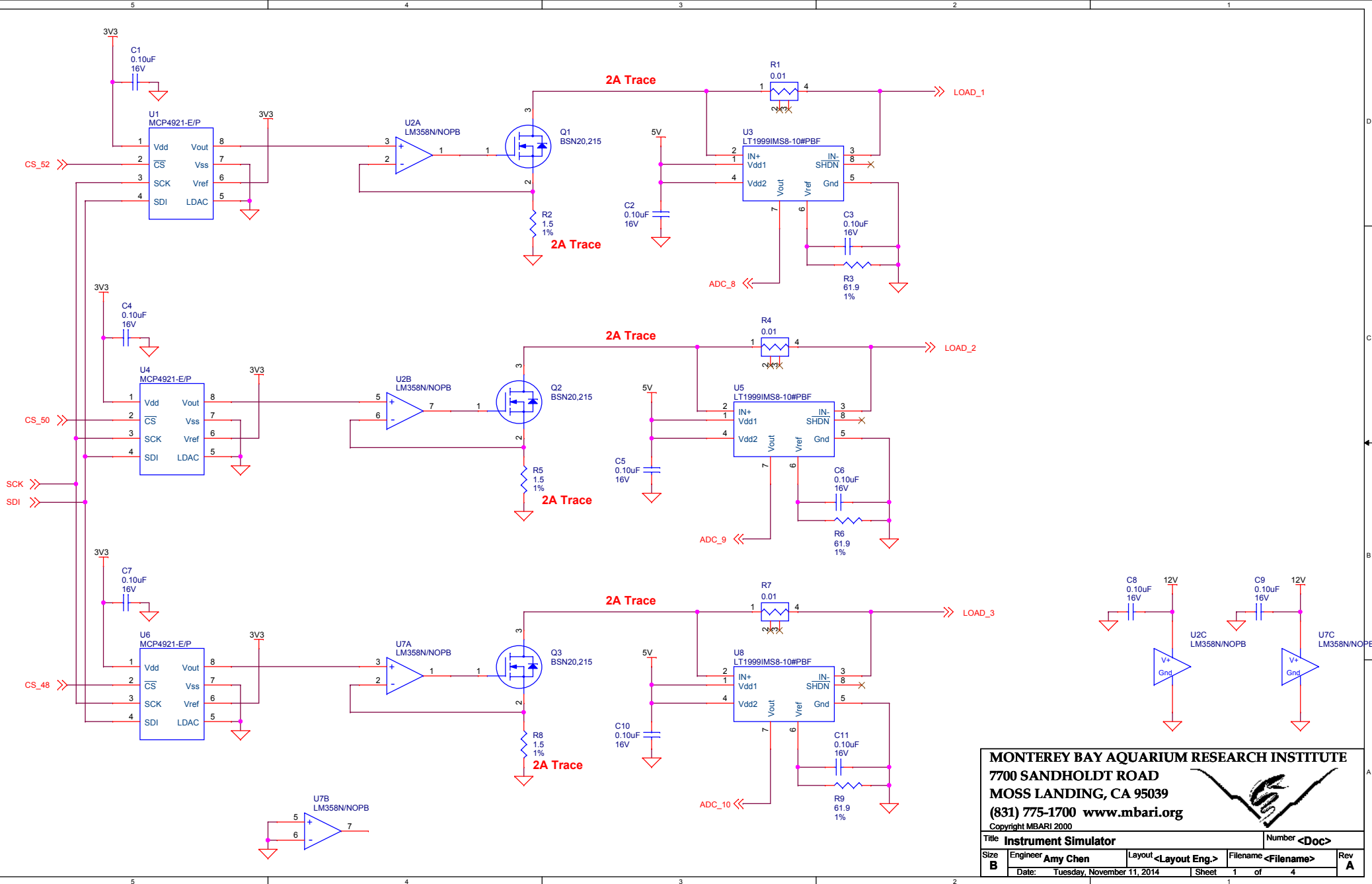
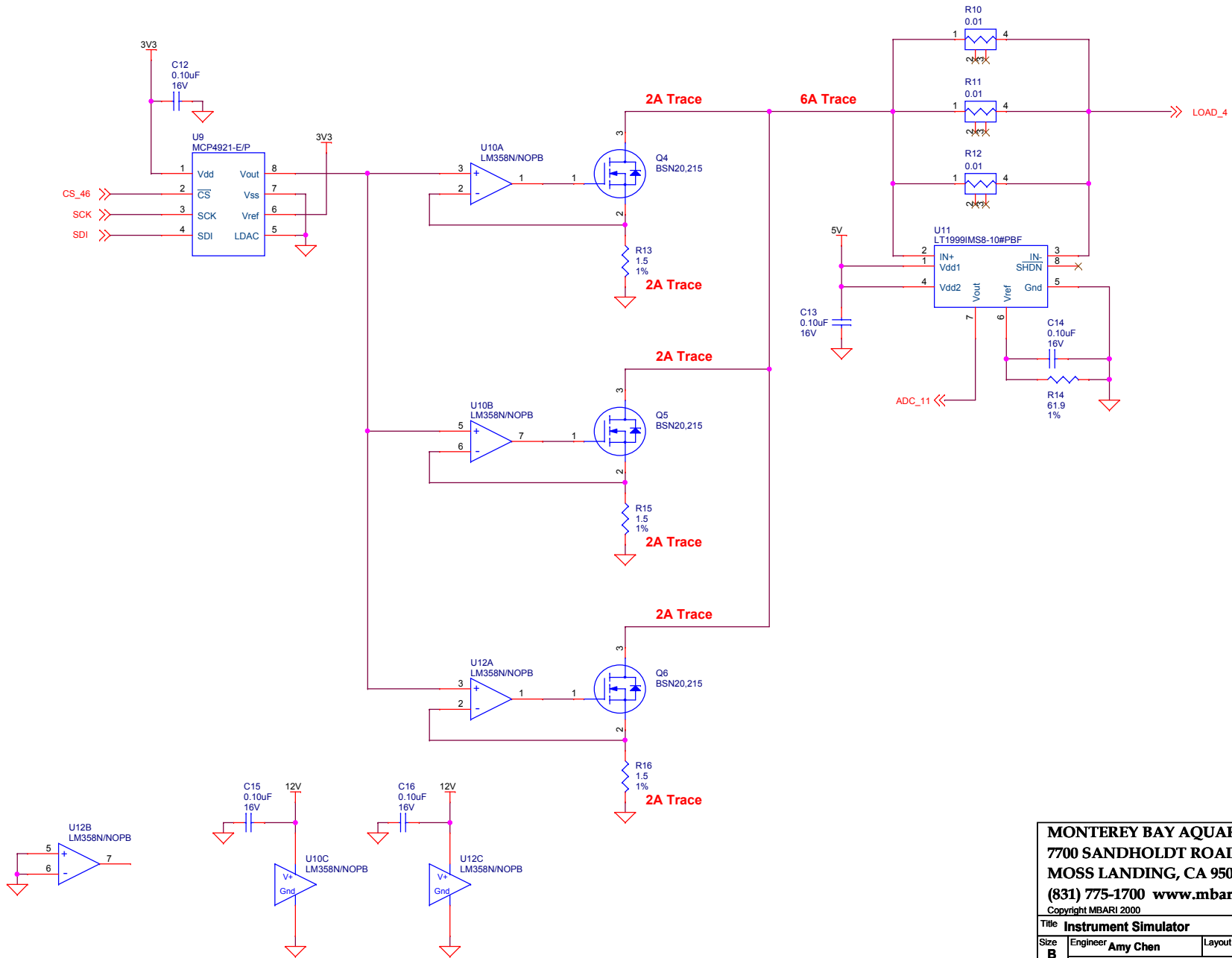
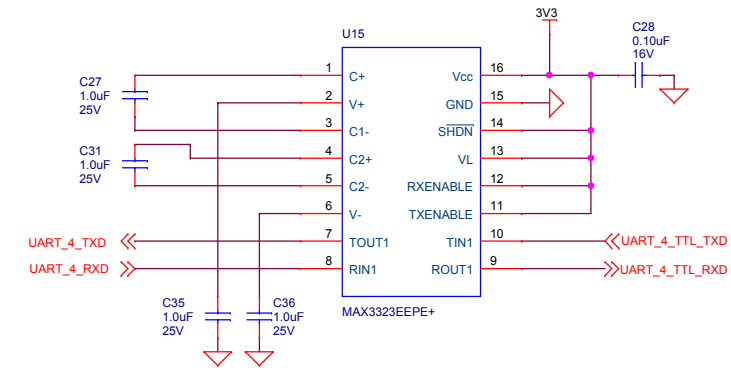
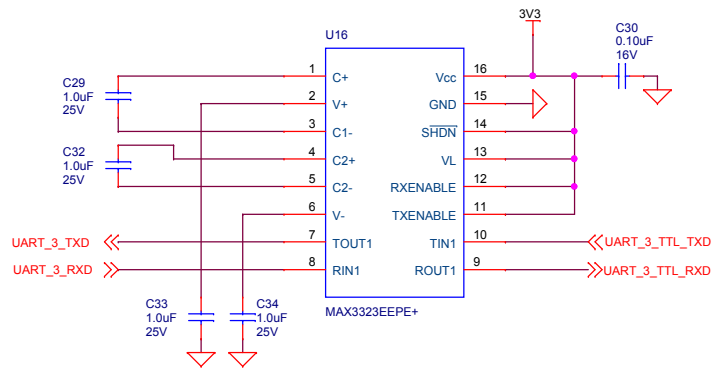
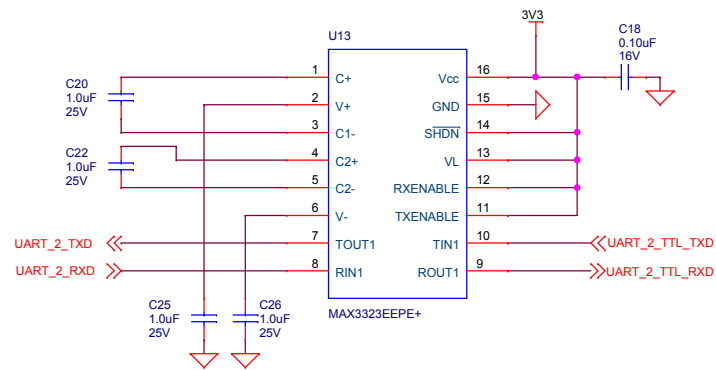
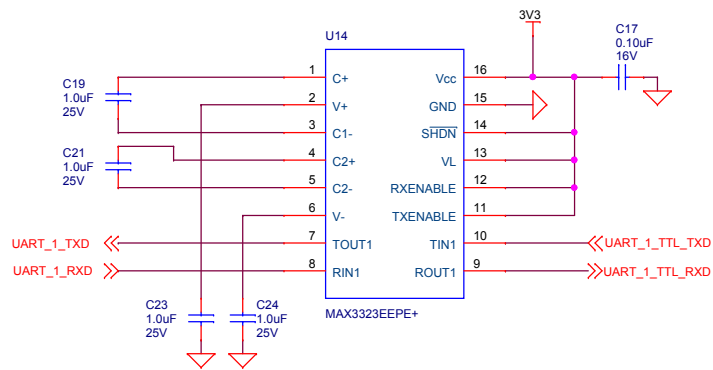




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Title Instrument Simulator					Number <Doc>	
Size B	Engineer Amy Chen		Layout <Layout Eng.>		Filename <Filename>	
	Date: Tuesday, November 11, 2014		Sheet 1 of 4			
						Rev A



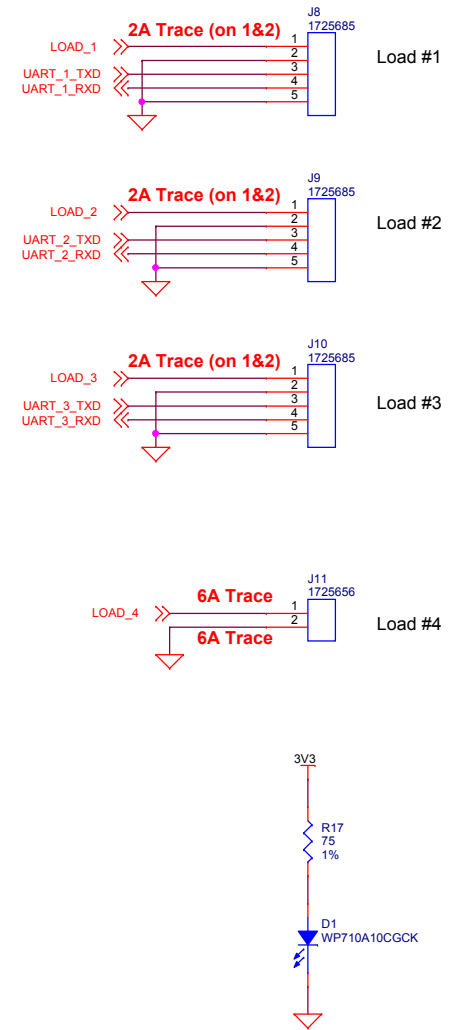
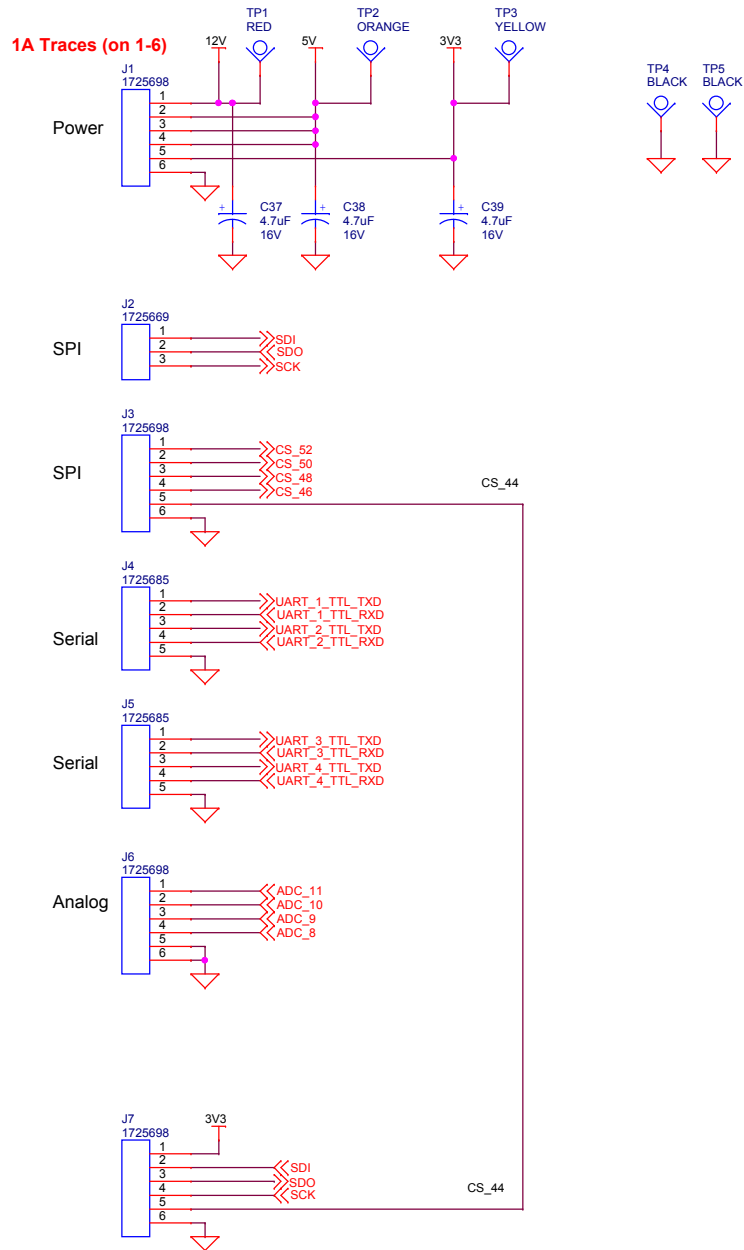


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Tin/Rout : TTL/CMOS
 Tout/Rin: EIA232

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