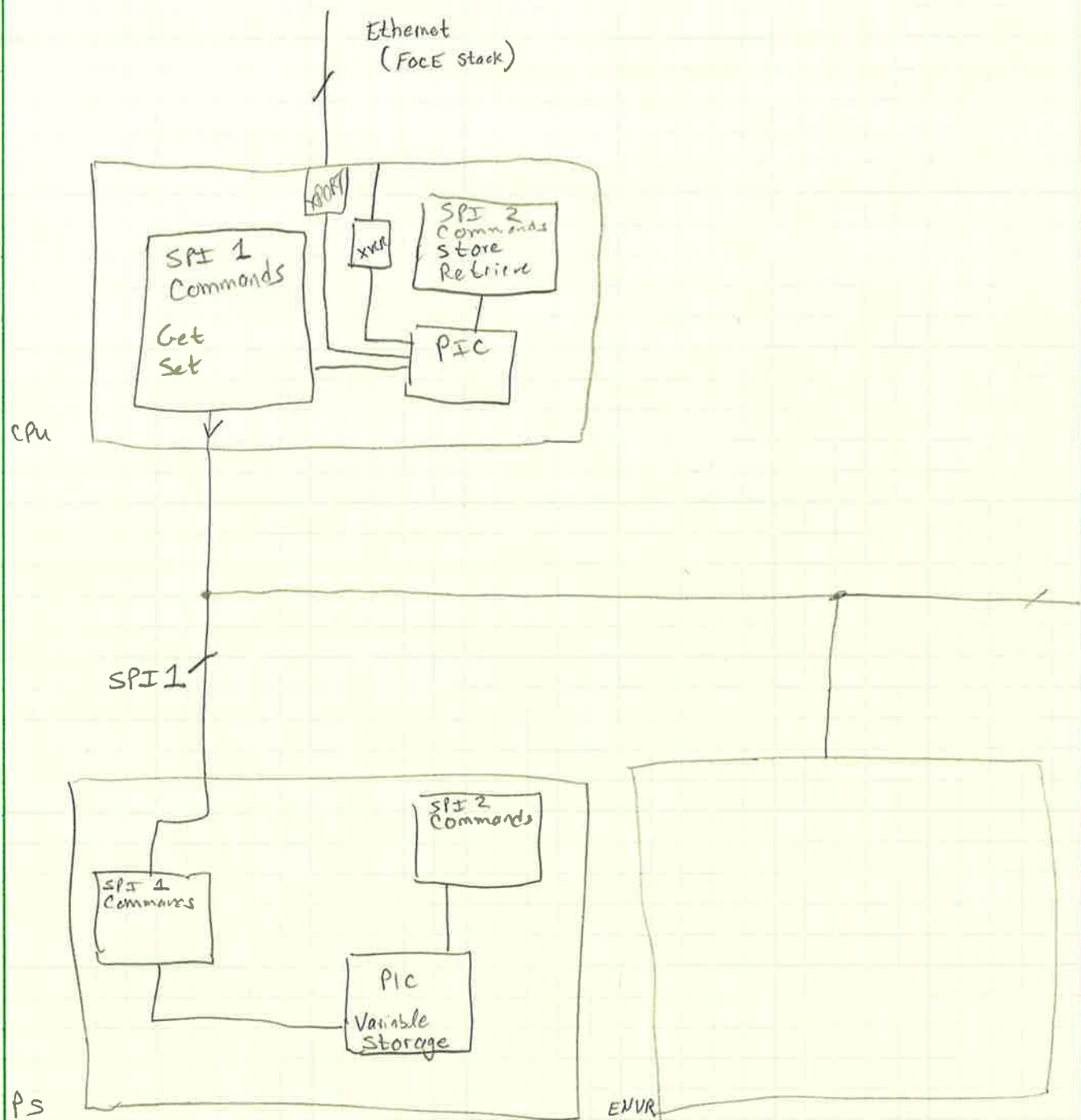




Use generic names for SPI 1 bus (HUM_01, TEMP_02, etc...). The code in the CPU board will have a lookup table or #defines to allow for external requests to be more specific -- (PH #4, Neck-Hum, etc...)