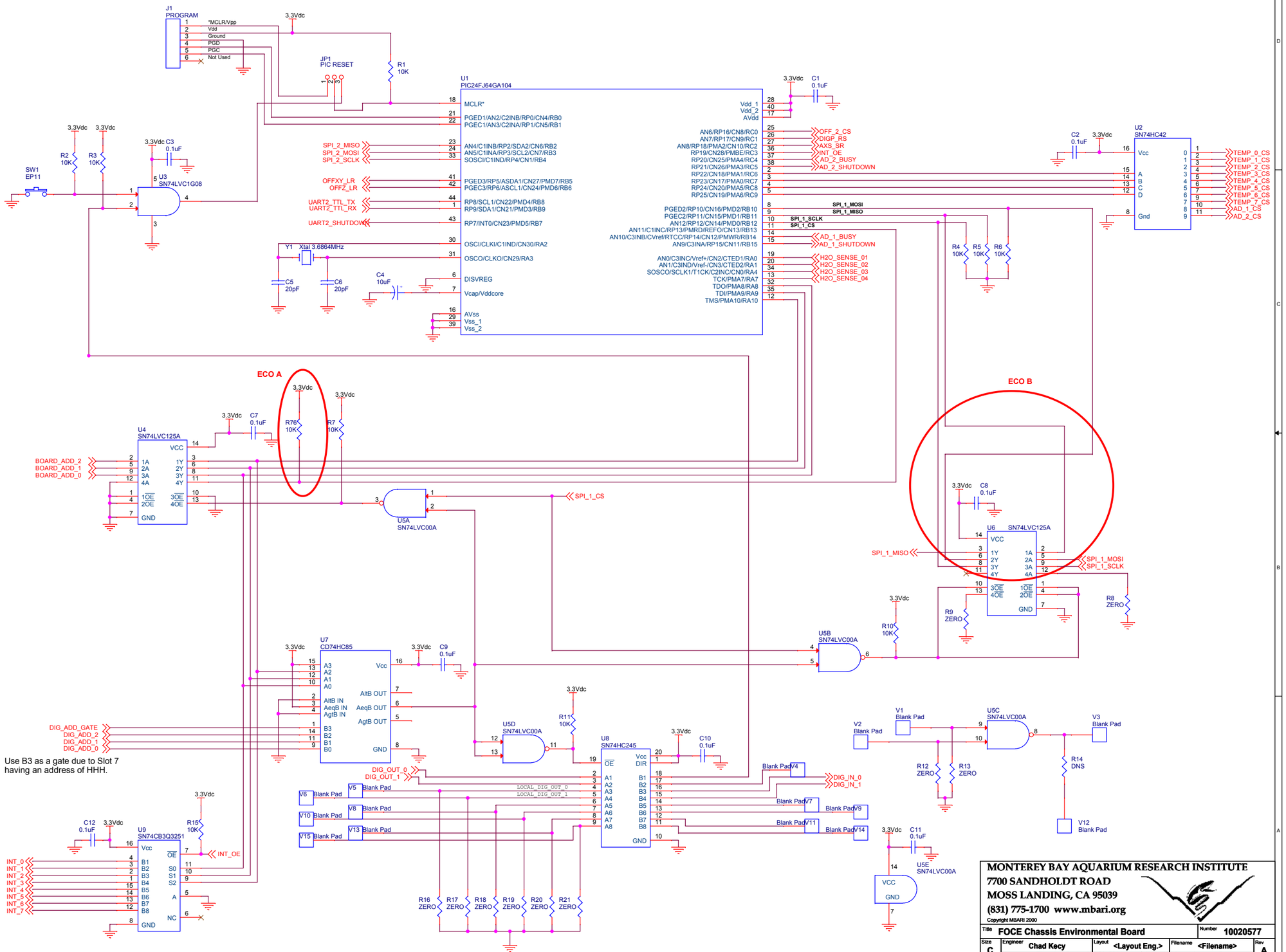
J1
PROGRAM
*MCLR/Vpp
Vdd
Ground
PGD
PGC
Not Used
3.3Vdc
JP1
PIC RESET
R1
10K
U1
PIC24FJ64GA104
MCLR*
PGED1/AN2/C2INB/RP0/CN4/RB0
PGEC1/AN3/C2INA/RP1/CN5/RB1
AN4/C1INB/RP2/SDA2/CN6/RB2
AN5/C1INA/RP3/SCL2/CN7/RB3
SOSCI/C1IND/RP4/CN1/RB4
PGED3/RP5/ASDA1/CN27/PMD7/RB5
PGEC3/RP6/ASCL1/CN24/PMD6/RB6
RP8/SCL1/CN22/PMD4/RB8
RP9/SDA1/CN21/PMD3/RB9
RP7/INT0/CN23/PMD5/RB7
OSCI/CLKI/C1IND/CN30/RA2
OSCO/CLKO/CN29/RA3
DISVREG
Vcap/Vddcore
AVss
Vss_1
Vss_2
Vdd_1
Vdd_2
AVdd
C1
0.1uF
SPI_2_MISO
SPI_2_MOSI
SPI_2_SCLK
OFFXY_LR
OFFZ_LR
UART2_TTL_TX
UART2_TTL_RX
UART2_SHUTDOWN
Y1 Xtal 3.6864MHz
C5
20pF
C6
20pF
C4
10uF
AN6/RP16/CN8/RC0
AN7/RP17/CN9/RC1
AN8/RP18/PMA2/CN10/RC2
RP19/CN28/PMBE/RC3
RP20/CN25/PMA4/RC4
RP21/CN26/PMA3/RC5
RP22/CN18/PMA1/RC6
RP23/CN17/PMA0/RC7
RP24/CN20/PMA5/RC8
RP25/CN19/PMA6/RC9
PGED2/RP10/CN16/PMD2/RB10
PGEC2/RP11/CN15/PMD1/RB11
AN12/RP12/CN14/PMD0/RB12
AN11/C1INC/RP13/PMRD/REF0/CN13/RB13
AN10/C3INB/CVref/RTCC/RP14/CN12/PMWR/RB14
AN9/C3INA/RP15/CN11/RB15
AN0/C3INC/Vref+/CN2/CTED1/RA0
AN1/C3IND/Vref-/CN3/CTED2/RA1
SOSCO/SCLK1/T1CK/C2INC/CN0/RA4
TCK/PMA7/RA7
TDO/PMA8/RA8
TDI/PMA9/RA9
TMS/PMA10/RA10
OFF_2_CS
DIGP_RS
AXS_SR
INT_OE
AD_2_BUSY
AD_2_SHUTDOWN
SPI_1_MOSI
SPI_1_MISO
SPI_1_SCLK
SPI_1_CS
AD_1_BUSY
AD_1_SHUTDOWN
H2O_SENSE_01
H2O_SENSE_02
H2O_SENSE_03
H2O_SENSE_04
R2
10K
R3
10K
SW1
EP11
U3
SN74LVC1G08
C3
0.1uF
C2
0.1uF
U2
SN74HC42
Vcc
A
B
C
D
Gnd
TEMP_0_CS
TEMP_1_CS
TEMP_2_CS
TEMP_3_CS
TEMP_4_CS
TEMP_5_CS
TEMP_6_CS
TEMP_7_CS
AD_1_CS
AD_2_CS
R4
10K
R5
10K
R6
10K
ECO A
ECO B
U4
SN74LVC125A
VCC
GND
C7
0.1uF
R76
10K
R7
10K
BOARD_ADD_2
BOARD_ADD_1
BOARD_ADD_0
U5A
SN74LVC00A
C8
0.1uF
U6
SN74LVC125A
R9
ZERO
R8
ZERO
R10
10K
U5B
SN74LVC00A
U7
CD74HC85
AltB OUT
AeqB OUT
AgtB OUT
AltB IN
AeqB IN
AgtB IN
C9
0.1uF
DIG_ADD_GATE
DIG_ADD_2
DIG_ADD_1
DIG_ADD_0
Use B3 as a gate due to Slot 7
having an address of HHH.
U5D
SN74LVC00A
R11
10K
U8
SN74HC245
Vcc
DIR
OE
C10
0.1uF
DIG_OUT_0
DIG_OUT_1
LOCAL_DIG_OUT_0
LOCAL_DIG_OUT_1
DIG_IN_0
DIG_IN_1
Blank Pad
V1
V2
V3
V5
V6
V7
V8
V9
V10
V11
V12
V13
V14
V15
V4
U5C
SN74LVC00A
R12
ZERO
R13
ZERO
R14
DNS
C12
0.1uF
U9
SN74CB3Q3251
R15
10K
INT_OE
INT_0
INT_1
INT_2
INT_3
INT_4
INT_5
INT_6
INT_7
R16
ZERO
R17
ZERO
R18
ZERO
R19
ZERO
R20
ZERO
R21
ZERO
C11
0.1uF
U5E
SN74LVC00A
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Title
FOCE Chassis Environmental Board
Number
10020577
Size
C
Engineer
Chad Kecy
Layout
<Layout Eng.>
Filename
<Filename>
Rev
A
Date:
Thursday, September 01, 2011
Sheet
1
of
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