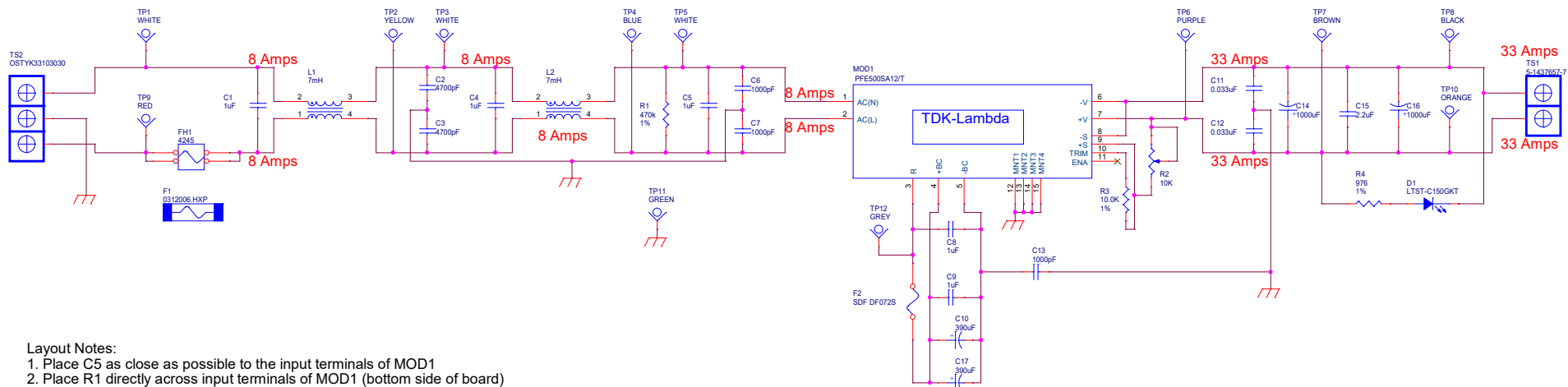




Layout Notes:

1. Place C5 as close as possible to the input terminals of MOD1
2. Place R1 directly across input terminals of MOD1 (bottom side of board)
3. Place C6 as close as possible to AC(N) pin.
4. Place C7 as close as possible to AC(L) pin.
5. Place C8 as close as possible to the R and -BC terminals
6. Place C9 as close as possible to the +BC and -BC terminals
7. Place C11 as close as possible to the +V terminal
8. Place C12 as close as possible to the -V terminal
9. Place C13 as close as possible to the -BC terminal
10. Place C14 as close as possible to the +V and -V terminals
11. Place C15 and C16 50mm away from the +V and -V terminals
12. Refer to section 7-3 of the PFE-SA Instruction Manual for required clearances.
13. Place test points near the board edges (when possible) for safety reasons

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Title		Number	
DeepPIV AC/DC Proto Board			
Engineer	Chad Key	Layout	<Layout Eng.>
Filename	<Filename>		
Rev	B		
Date	Monday, May 13, 2019	Sheet	1 of 1