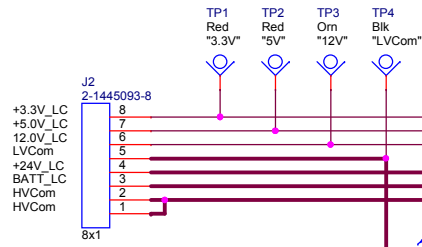


5 1
6 2
7 3
8 4
J1 Pinout
View from top
looking down

"LCB Pwr In"
Battery Power
from Mobo

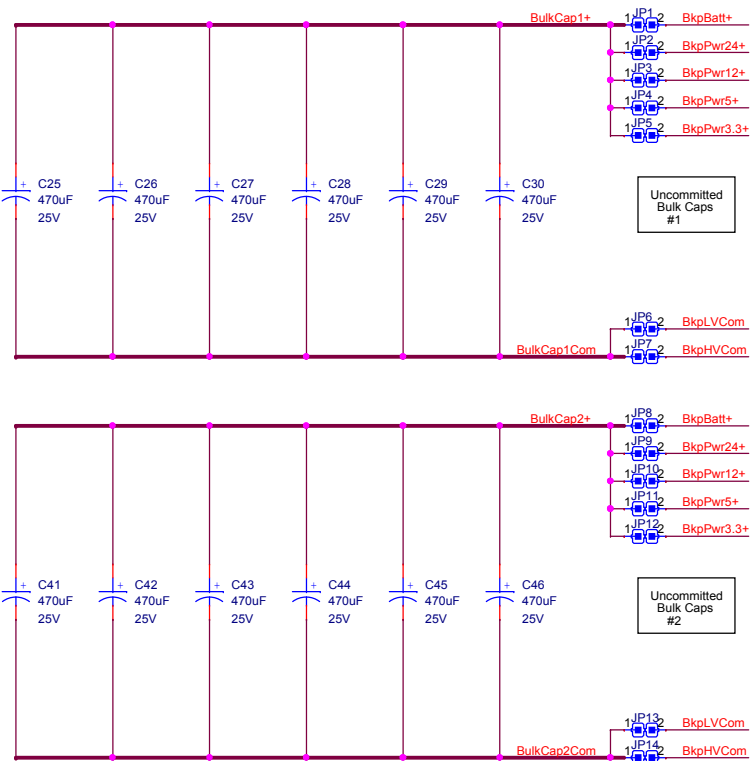
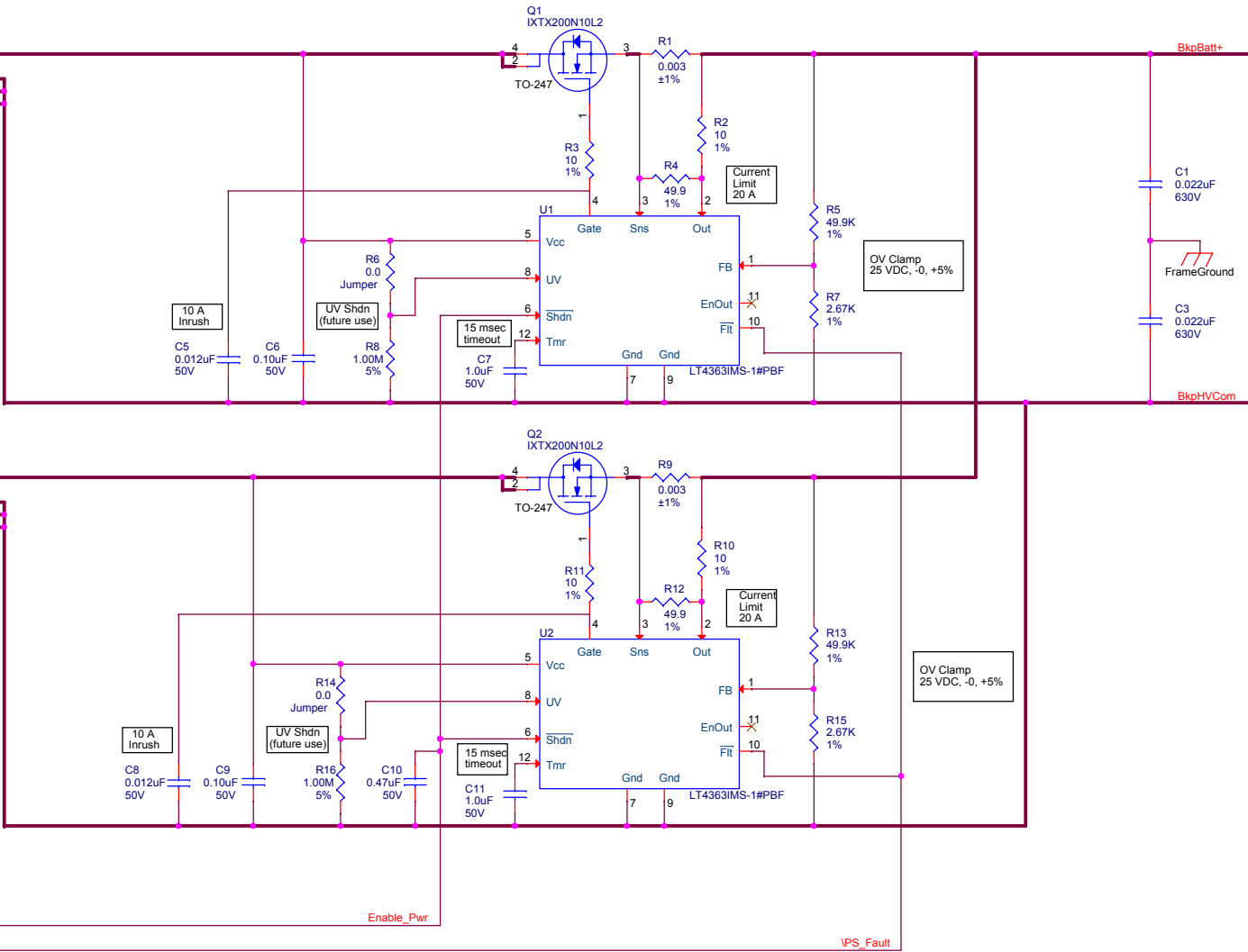
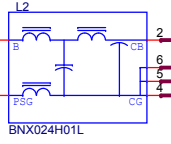
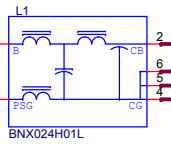
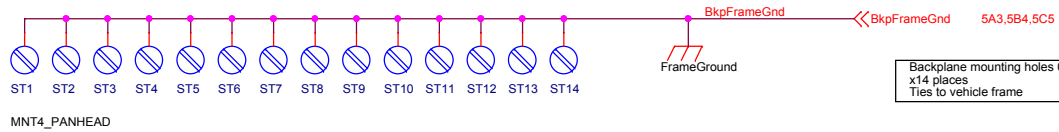
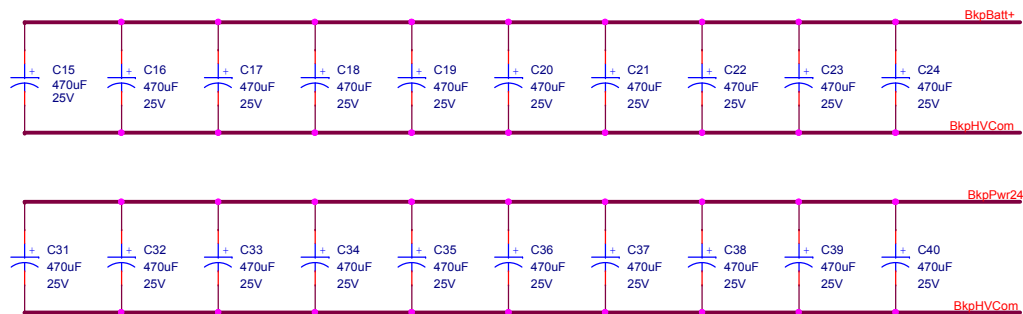
Note: Mobo_Gnd (4C2)
and VBATRET_LC
are tied on Motherboard



"Boost/Ext Pwr"
Boost or
External
Power

4C2 Enable_Pwr >>
4C2 \PS_Fault >>

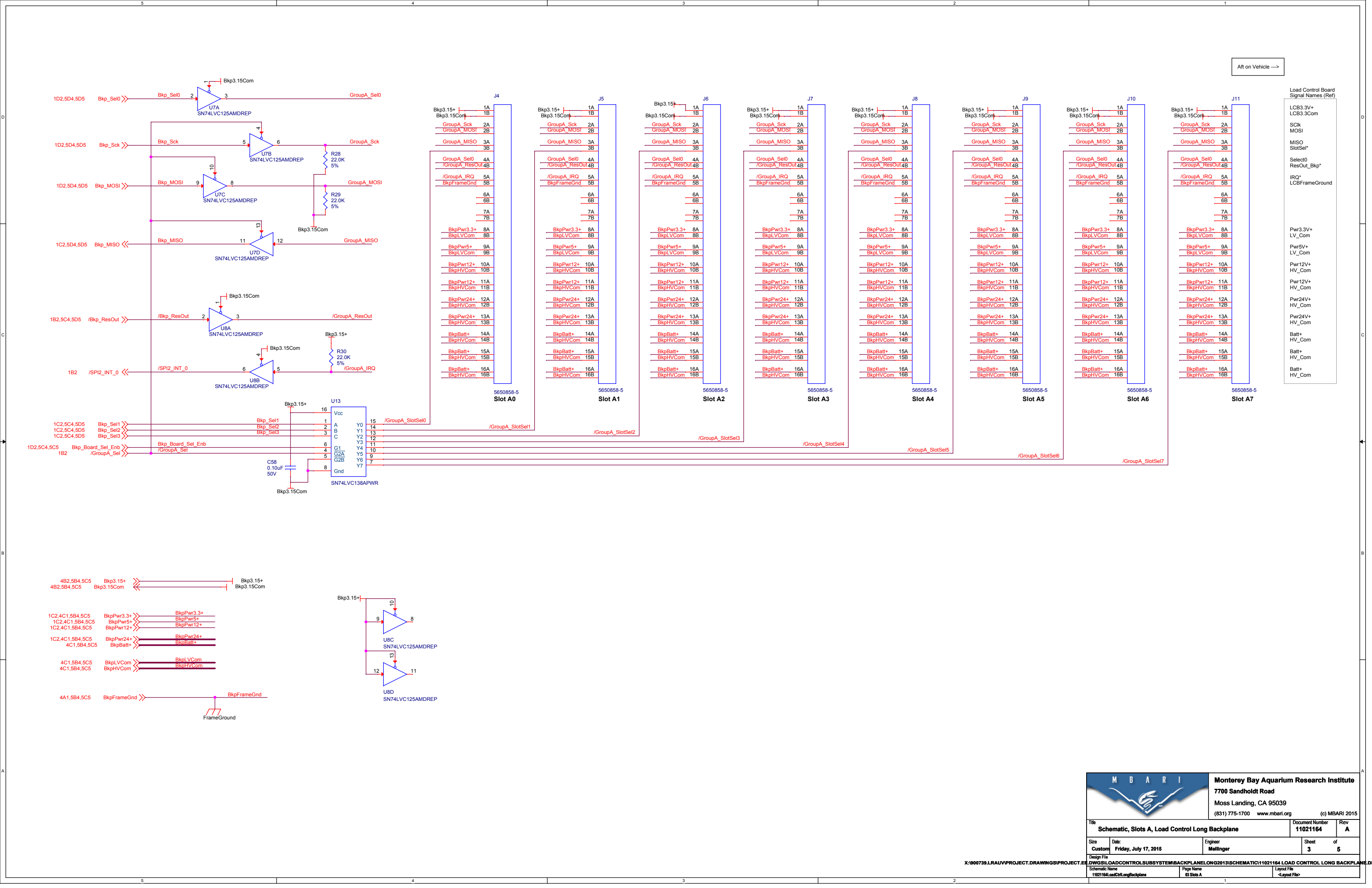
Control and
Status to
Mobo Signals page

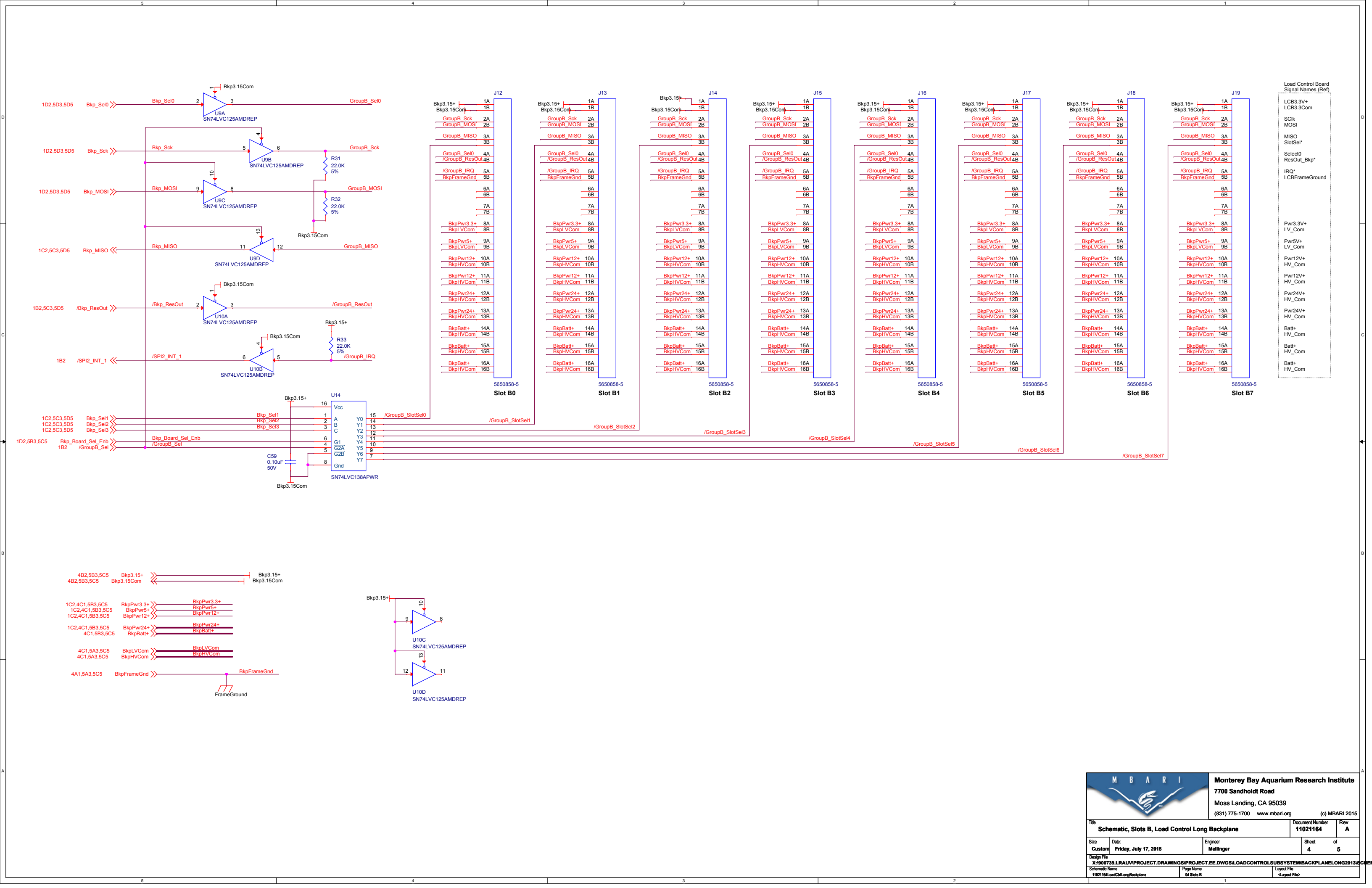


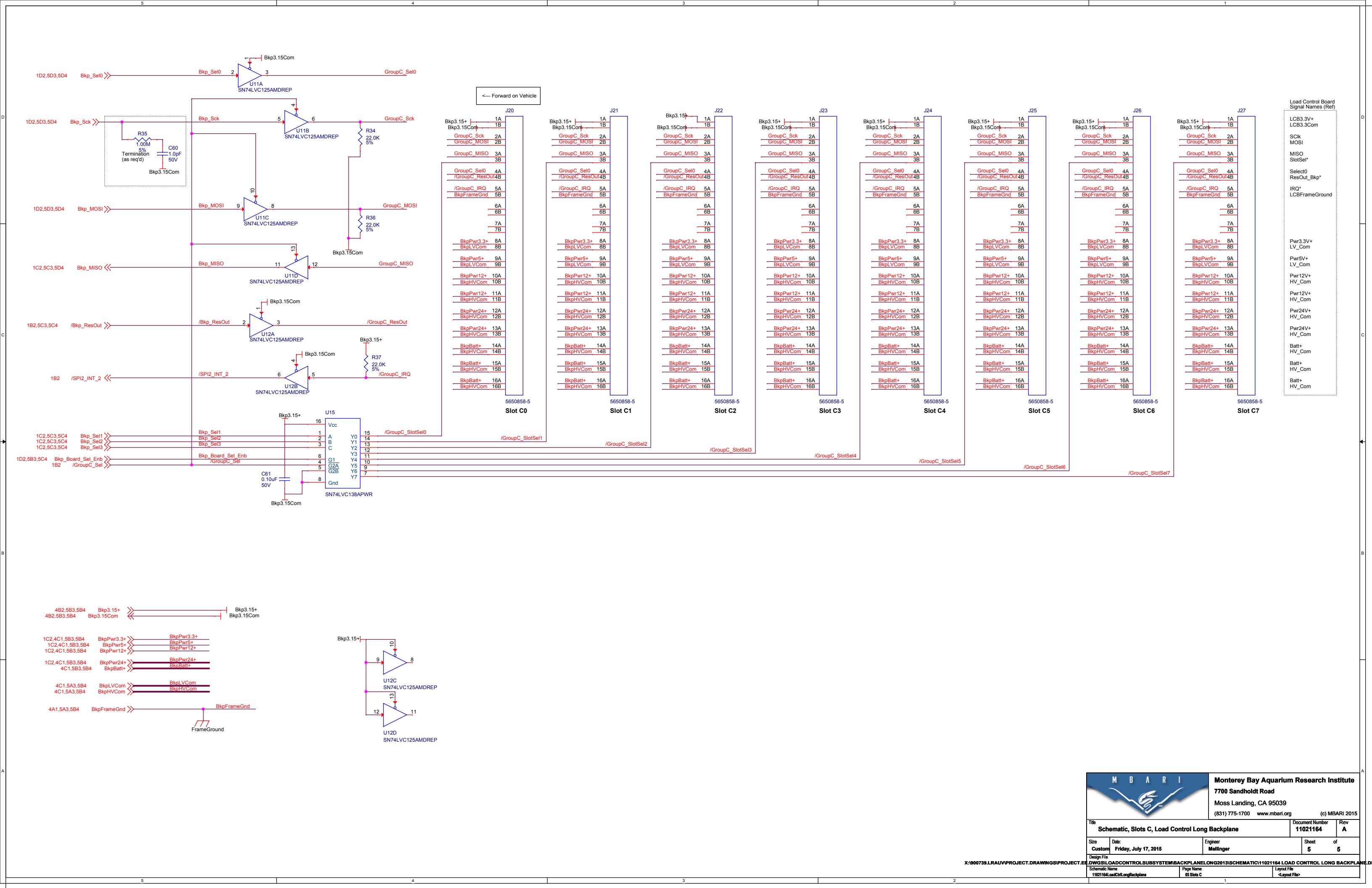
C	2015-Aug-05	ECM	fix Test Point part numbers
B	2015-Jul-29	ECM	Revise pinout of J1 power input connector, and reverse J2 boost connector, for better PWB routing.

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Title Schematic, Power Input, Load Control Long Backplane		Document Number 11021164	Rev C
Size Custom	Date Thursday, August 06, 2015	Engineer Mollinger	Sheet 1 of 5
Design File Schematic Name 11021164_LoadCtrlLongBackplane		Page Name #1 Power Input	Layout File <Layout File>









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Title		Document Number		Rev	
Schematic, Slots C, Load Control Long Backplane		11021164		A	
Size	Date:	Engineer	Sheet	of	
Custom	Friday, July 17, 2015	Mellinger	5	5	
Design File					
Schematic Name		Page Name		Layout File	
11021164_LoadCtrlLongBackplane		05 Slots C		<Layout File>	