

Socket	Type	Letter	Pin 1	Pin 2	Pin 3	Pin 4	Pin 5	Pin 6	Pin 7	Pin 8	Pin 9	Pin 10
1	GPIO	Y	+3.3V	+5V	PD11 (59)	PD1 (49)	PD0 (48)	PD10 (58)	PE3 (67)	PE4 (68)	PE15 (79)	GND
	CAN	C				CAN1_TX	CAN1_RX					
2	GPIO	X	+3.3V	+5V	PE7 (71)	PD8 (56)	PD9 (57)	PB9 (25)[1]	PB5 (21)[1]	PB4 (20)[1]	PB3 (19)[1]	GND
	SPI	S						CS	SPI1_MOSI	SPI1_MISO	SPI1_SCK	
	UART	U				USART3_TX	USART3_RX					
3	GPIO	Y	+3.3V	+5V	PE8 (72)	PD5 (53)	PD6 (54)	PD4 (52)	PD3 (51)	PE0 (64)	PE1 (65)	GND
	UART+	KU				USART2_TX	USART2_RX	USART2_RTS	USART2_CTS			
4	GPIO	Y	+3.3V	+5V	PE9 (73)	PC9 (41)	PA8 (8)	PD15 (63)	PD12 (60)	PD13 (61)	PD14 (62)	GND
	PWM	P						PWM_CH0	PWM_CH1	PWM_CH2		
	other				PWM_CH4[2]	I2C3_SDA[3]	I2C3_SCL[3]	PWM_CH3[2]				
5	GPIO	Y	+3.3V	+5V	PD2 (50)	PC6 (38)	PC7 (39)	PA15 (15)	PC12 (44)	PC11 (43)	PC10 (42)	GND
	SPI	S					CS	SPI3_MOSI	SPI3_MISO	SPI3_SCK		
	UART	U				USART6_TX	USART6_RX					
	other				USART5_RX[2]		USART5_TX[2]	USART4_RX[2]	USART4_TX[2]			
6	GPIO	X	+3.3V	+5V	PA6 (6)	PA5 (5)[4]	PA4 (4)[4]	PC8 (40)[2]		PB7 (23)[5]	PB6 (22)[5]	GND
	Analog In	A			ADC_CH2	ADC_CH1	ADC_CH0					
	I2C	I						I2C1_SDA	I2C1_SCL			
	Analog Out	O					DAC_CH0					
	other					DAC_CH1[2]						
7	GPIO	X	+3.3V	+5V	PE5 (69)	PA9 (9)	PA10 (10)	PE6 (70)[2]			PB2 (18)[6]	GND
	UART	U				USART1_TX	USART1_RX					
	Mf.specific								MCU_RST	BOOT0	BOOT1	
8	Mf.specific	Z	+3.3V	+5V	MCU_RST	PA13 (13)[2]	PA14 (14)[2]		VBAT	BOOT0	BOOT1	GND
9[7]	USB Device/Host other	D	+3.3V	PB13 (29)[8]	PE12 (76)[2]	PB14 (30)[2]	PB15 (31)[2]	PB12 (28)[2]	PB10 (26)[2]			GND
					USB2_Vbus[9]		USB2_DM	USB2_DP	USB2_ID			
							SPI2_MISO[2]	SPI2_MOSI[2]		SPI2_SCK[2]		
USB			PA11 (11)[2] USB1_DM	PA12 (12)[2] USB1_DP								
User LED			PE11 (75)[2] PWM_CH5[2] Green	PE13 (77)[2] PWM_CH6[2] Red	PE14 (78)[2] PWM_CH7[2] Blue	active high (the blue LED is used as Gadgeteer <i>DebugLed</i>)						
User Button			PE10 (74)				active high					
Board ID			PD7 (55)[2]				0: Ethernet Mainboard, 1: USB Mainboard					
External Flash			PC0 (32)[1] CS-	PB5 (21)[1] SPI1_MOSI	PB4 (20)[1] SPI1_MISO	PB3 (19)[1] SPI1_SCK	SPI1 shared with socket 2					
USB2 Vbus Driver			PB8 (24)[10] Enable-	PA3 (3)[10] Error-	Enable- (output) controls whether 5 V is fed over this socket Error- (input) indicates whether current is too high (overload)							

- [1] These GPIOs can be accessed from NETMF, but they are shared with the external SPI Flash chip
- [2] Indicates other CPU functions that are available from "plain vanilla NETMF", but not from Gadgeteer
- [3] These I2C pins can be accessed from NETMF. They have no associated pull-ups. They could be used for a hardware-accelerated DaisyLink implementation.
- [4] These pins are not 5 V tolerant
- [5] These GPIOs can be accessed from NETMF, but they have associated pull-up resistors
- [6] This pin has an associated weak pull-down
- [7] This socket is only available on the USB mainboard
- [8] Might be used by USB controller to determine whether 5 V is available
- [9] Pin 2 of this socket has a switched 5 V supply
- [10] Only relevant if socket 9 is used as USB host