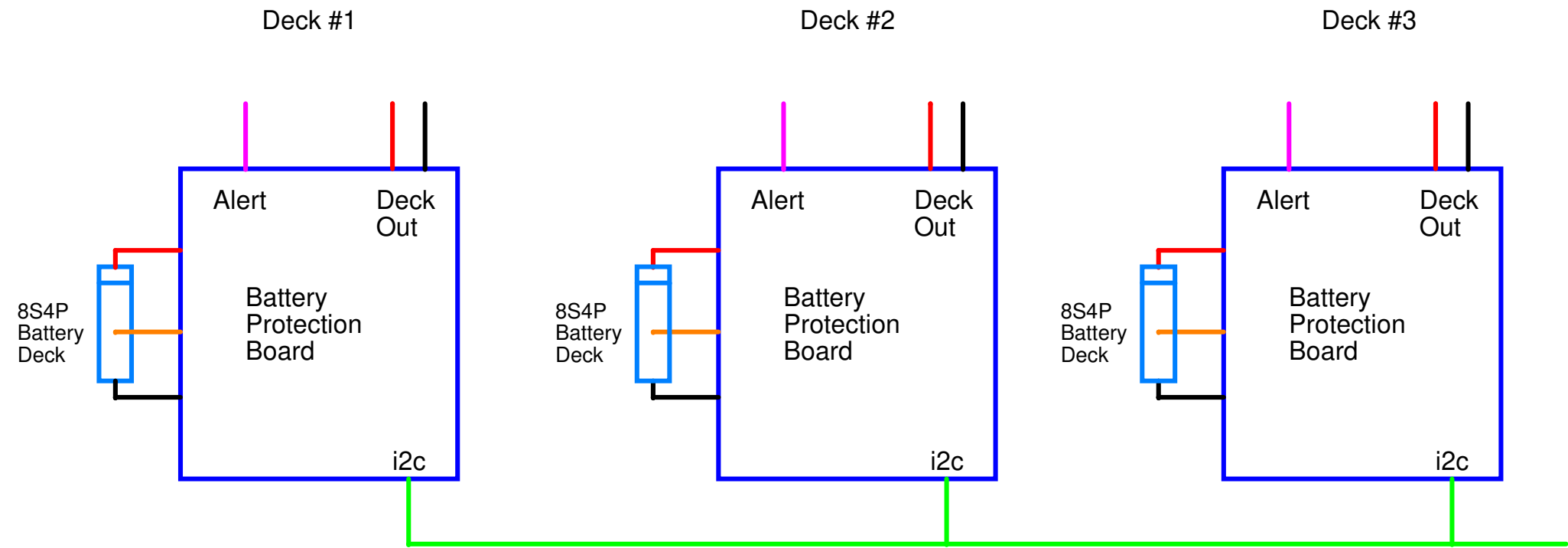


Battery Protection Block Diagram



Notes:

Does Alert need to go off board or only to the board level MCU?
Diode OR functionality handled in the LHE CCA.

 Monterey Bay Aquarium Research Institute 7700 Sandholdt Road, Moss Landing, CA 95039	Schematic Name: Schematic1		
	Page Title: Batt Sys Block Diagram		Engineer: <Engineer>
Project Name: <Project Name>		Designer: <Designer>	
Group Name: <Group>	System: <System>	Subsystem: <Subsystem>	Last Updated By: <Updated By>
Drawing Number: <MBARI DN>		Release: <Rev>	Status: <Status>
Page Modify Date: Monday, March 01, 2021		Check In Date: <LCID>	Size: B
Sheet <Page Number>		Page Count: <Page Count>	