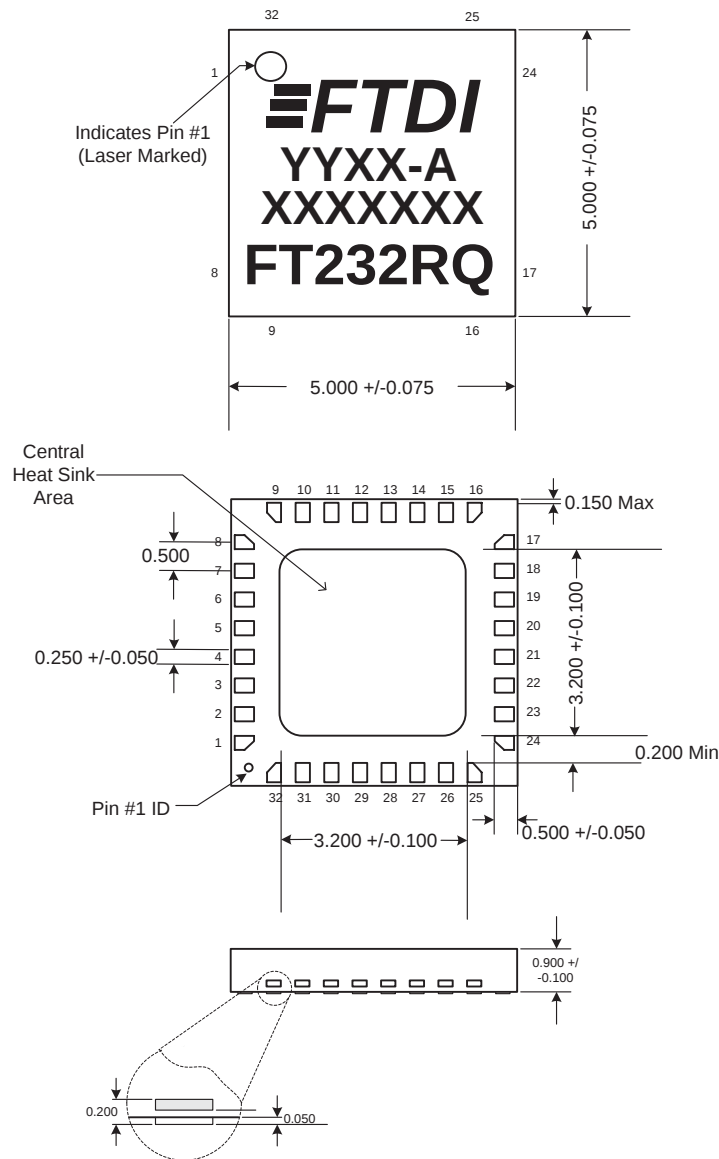


9.2 QFN-32 Package Dimensions



Note: The pin #1 ID is connected internally to the device's central heat sink area . It is recommended to ground the central heat sink area of the device.

Dimensions in mm.

Figure 9.2 QFN-32 Package Dimensions

The FT232RQ is supplied in a RoHS compliant leadless QFN-32 package. The package is lead (Pb) free, and uses a 'green' compound. The package is fully compliant with European Union directive 2002/95/EC.

This package is nominally 5.00mm x 5.00mm. The solder pads are on a 0.50mm pitch. The above mechanical drawing shows the QFN-32 package. All dimensions are in millimetres.

The centre pad on the base of the FT232RQ is not internally connected, and can be left unconnected, or connected to ground (recommended).

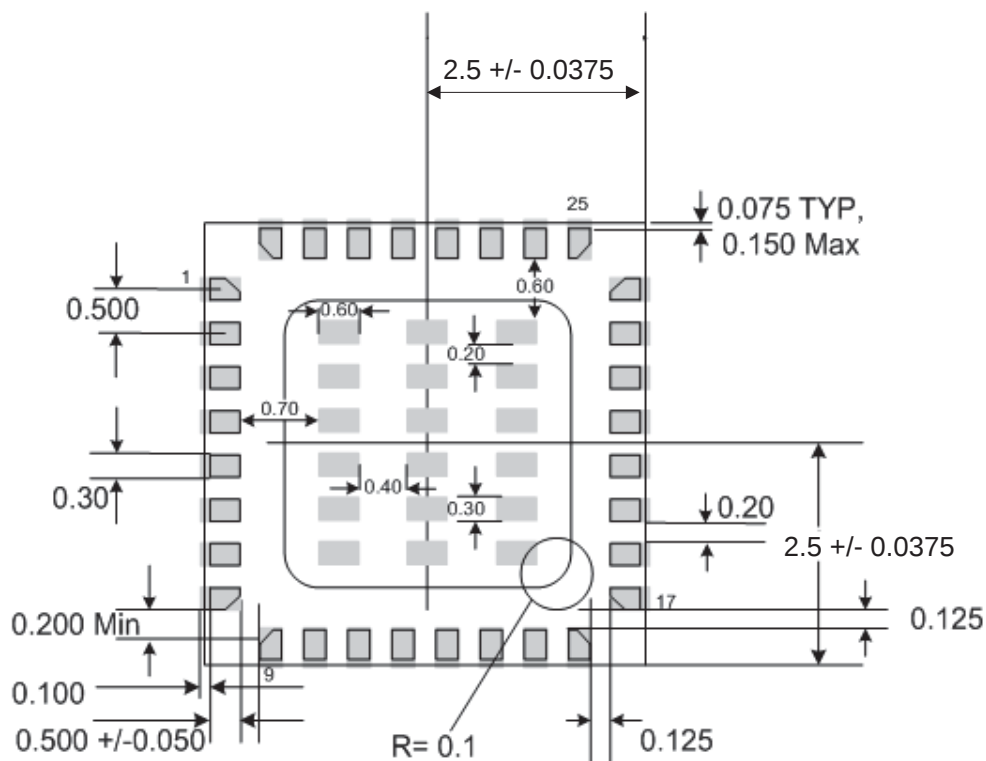
The date code format is **YYXX** where XX = 2 digit week number, YY = 2 digit year number.

The code **XXXXXXXX** is the manufacturing LOT code. This only applies to devices manufactured after April 2009.

The bottom view of the PCB shows a central square area with rounded corners, measuring 3.200 ± 0.100 in both width and height. This central area is surrounded by a border of 25 pins. The overall dimensions of the board are 2.50 in width and 2.50 in height. Key dimensions and features include:

- Pin 1:** Located at the top-left corner.
- Pin 25:** Located at the top-right corner.
- Pin 17:** Located at the bottom-right corner.
- Pin 9:** Located at the bottom-left corner.
- Optional GND Connection:** Indicated by arrows pointing to specific pins on the left and right sides.
- Dimensions:**
 - Overall width: 2.50
 - Overall height: 2.50
 - Central square area: 3.200 ± 0.100
 - Pin pitch (horizontal): 0.500
 - Pin pitch (vertical): 0.500
 - Pin width: 0.30
 - Pin height: 0.20
 - Pin spacing from edge: 0.100
 - Pin spacing from center: 0.200 Min
 - Pin spacing from corner: 0.150 Max
 - Pin spacing from center (horizontal): 0.100
 - Pin spacing from center (vertical): 0.100

9.4 QFN-32 Package Typical Solder Paste Diagram



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