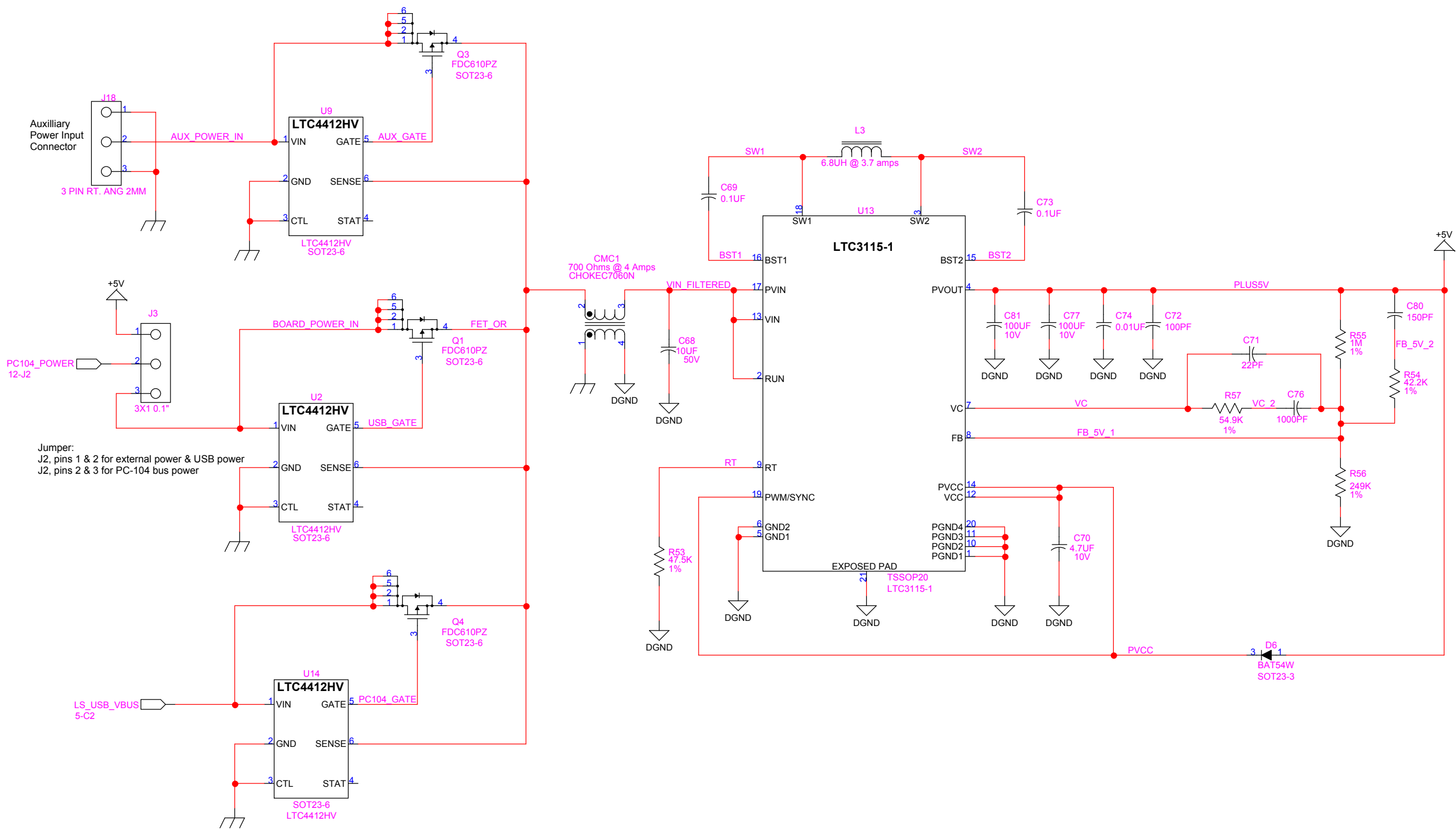
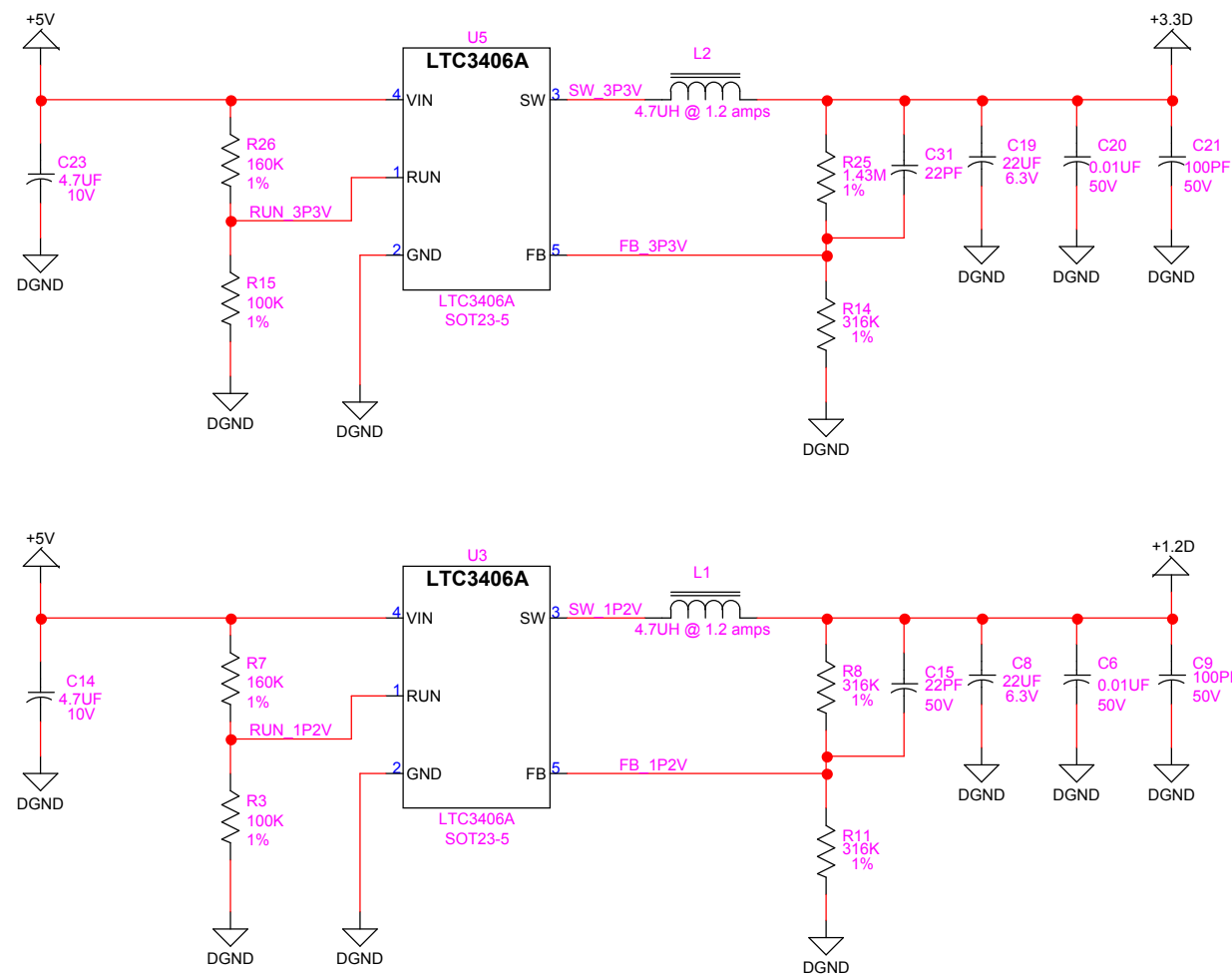


POWER SUPPLY

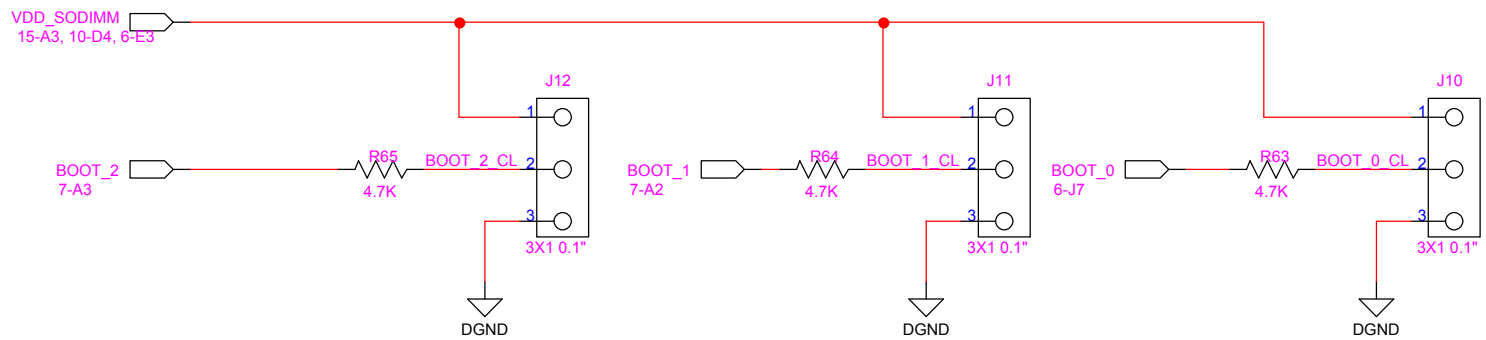


CORE POWER SUPPLIES

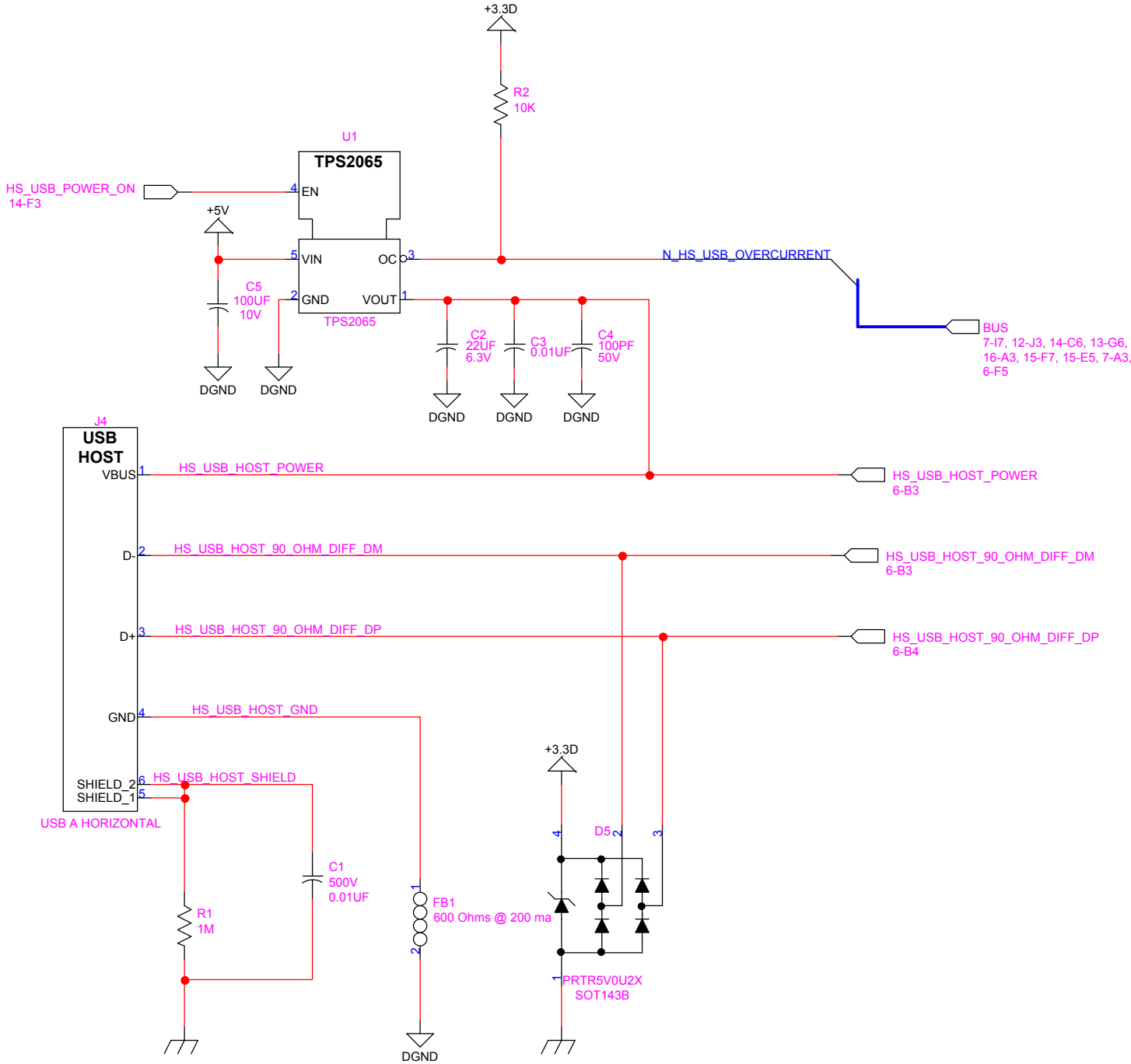


BOOTSTRAP JUMPERS

GPIO-0	GPIO-1	GPIO-2	Boot From
Low	Low	Low	NAND
Low	Low	High	SPI
Low	High	Low	USB DFU
Low	High	High	MMC/SD Card
High	Low	High	Functional Test Mode
High	High	Low	UART 115.2K Baud, 8N1
High	High	High	Test Mode

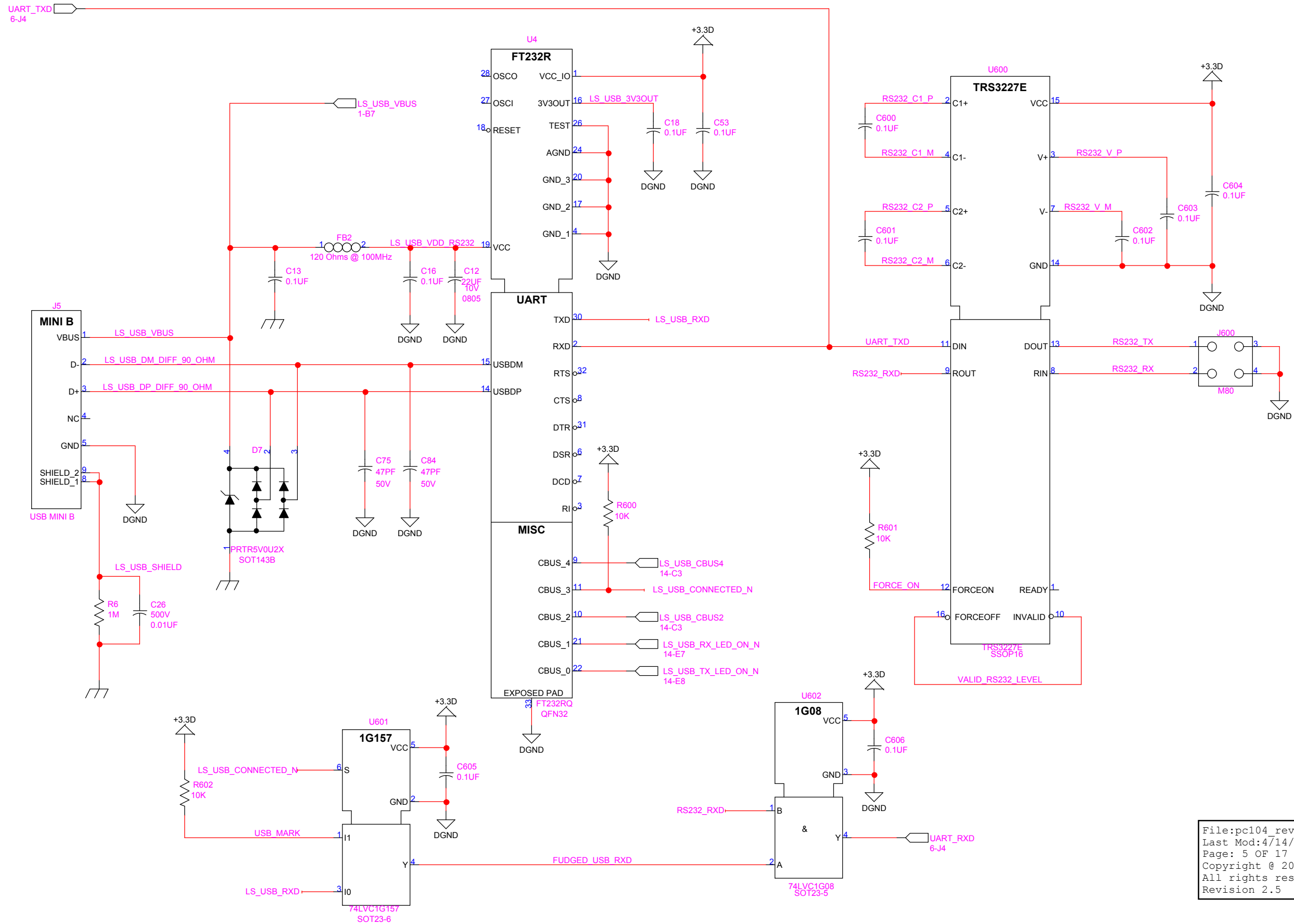


H/S USB HOST INTERFACE



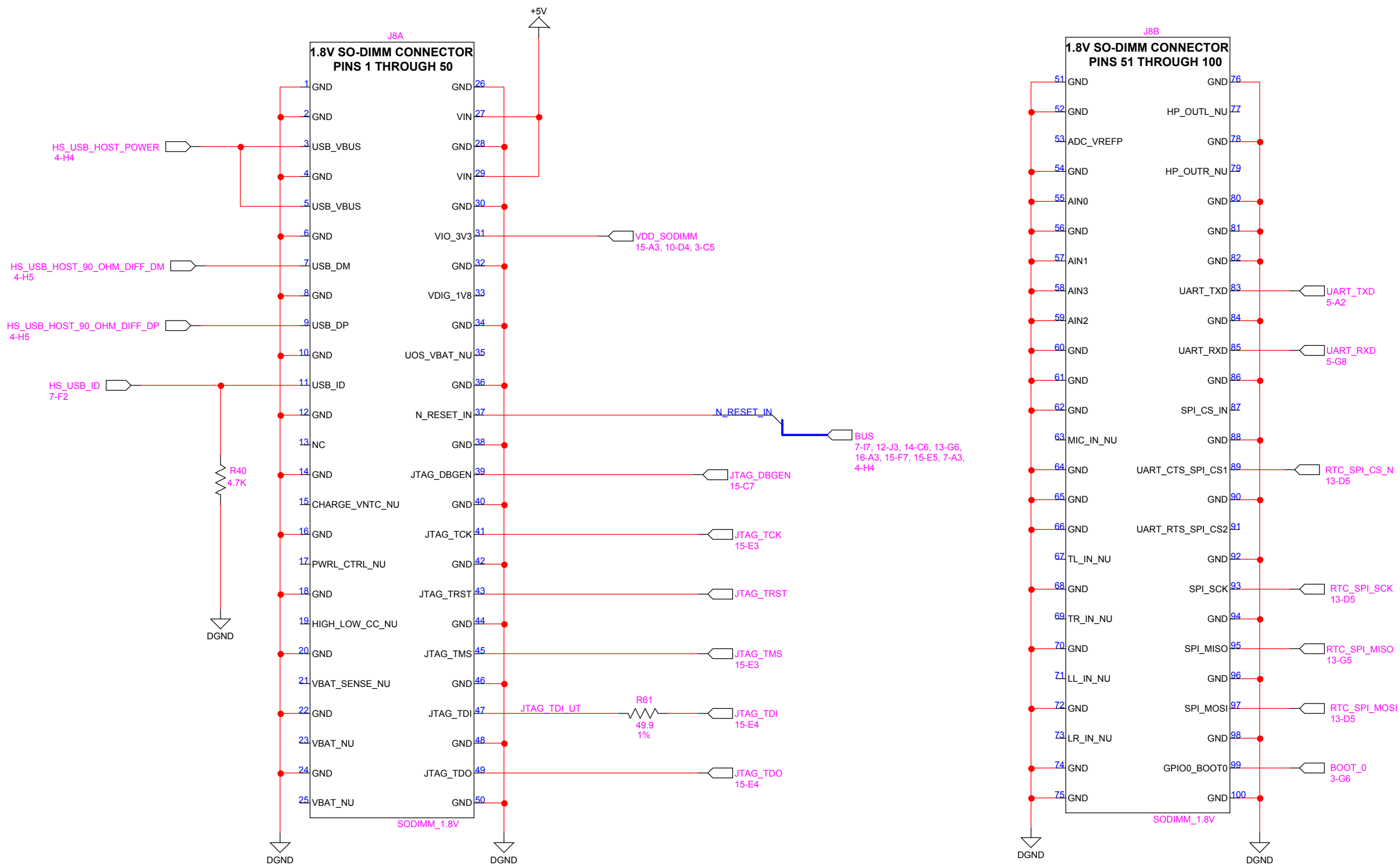
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Last Mod:4/14/2015
Page: 4 OF 17
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SERIAL INTERFACES

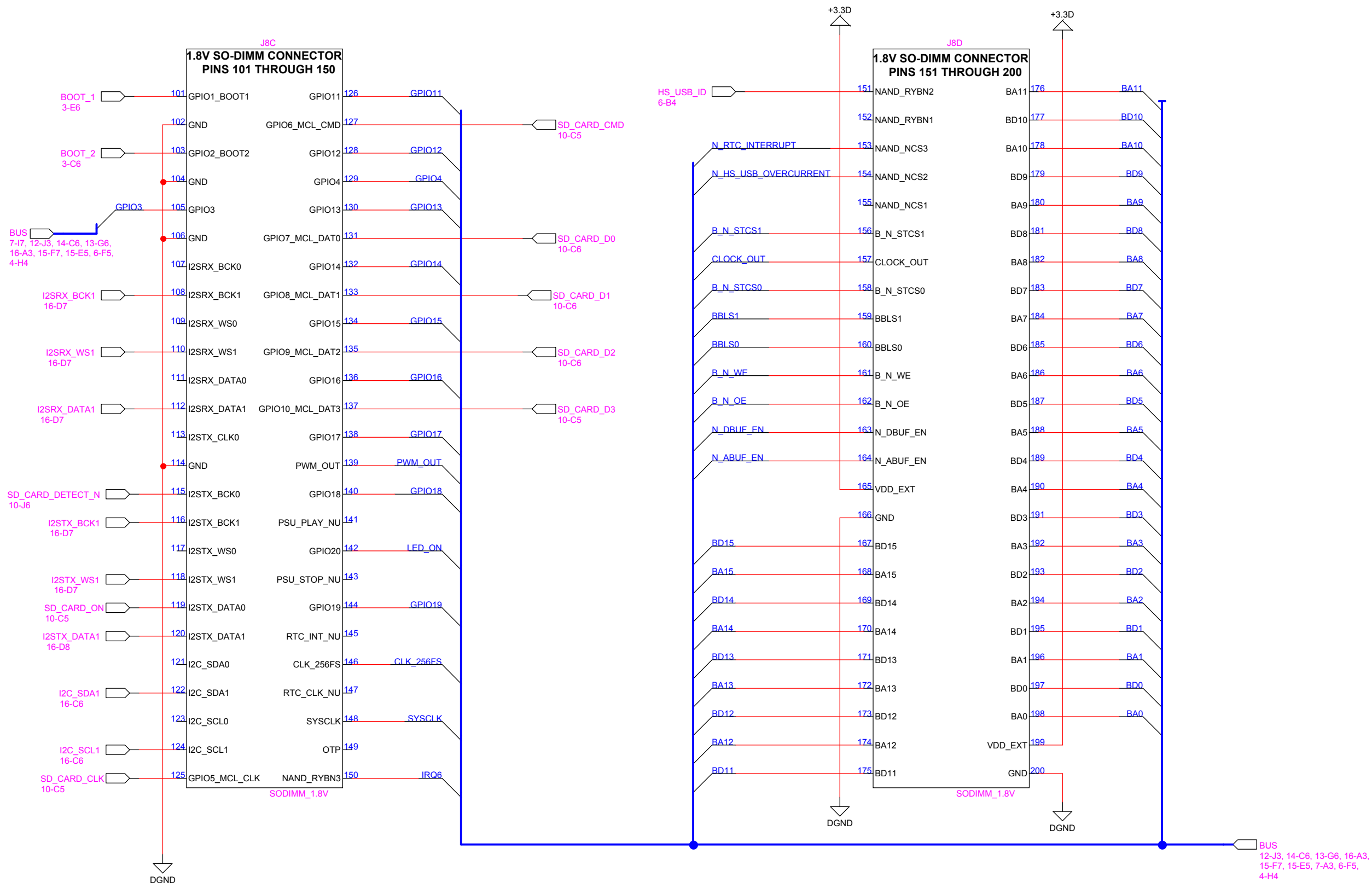


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 Last Mod:4/14/2015
 Page: 5 OF 17
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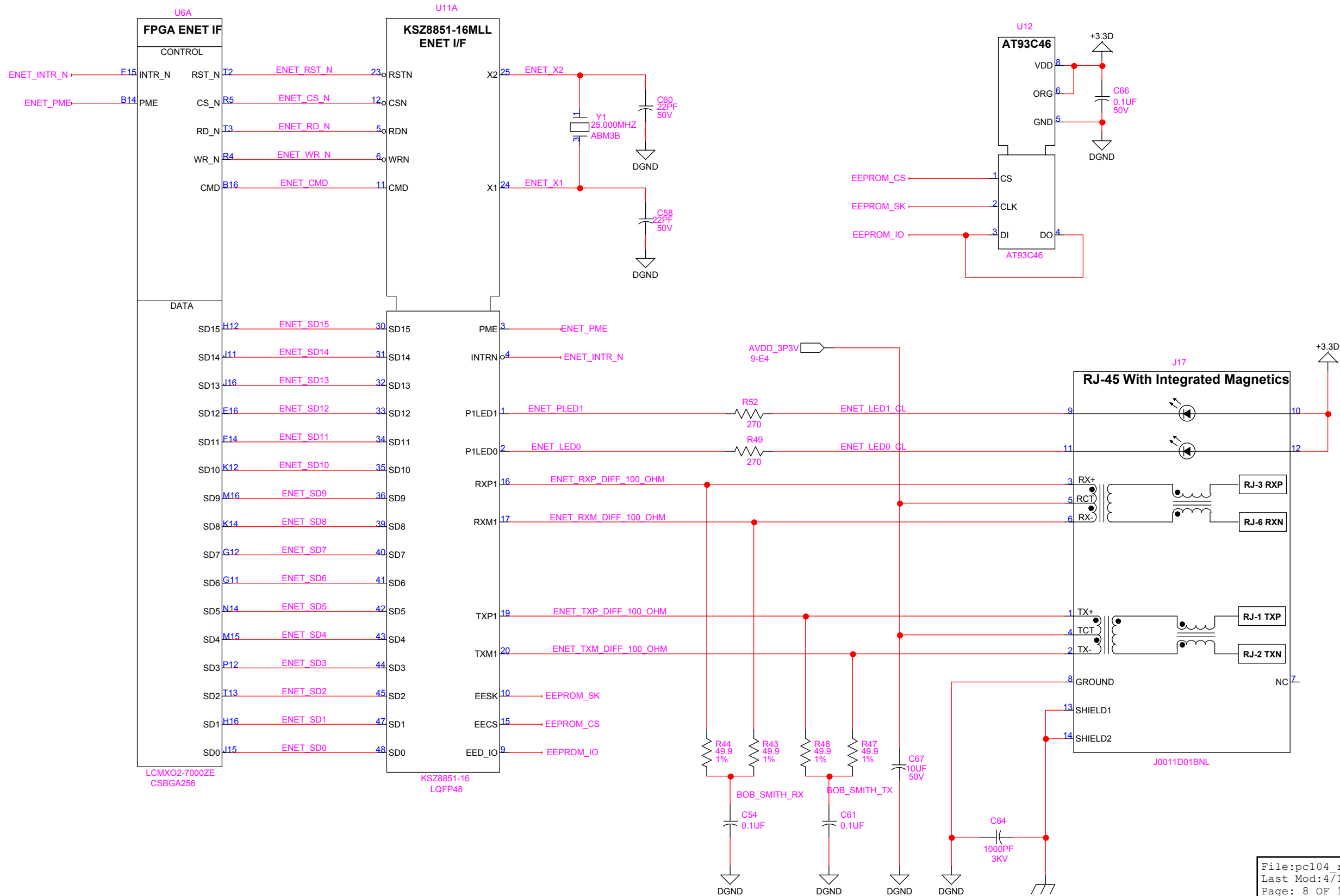
PINS 1-100, OEM MODULE CONNECTOR



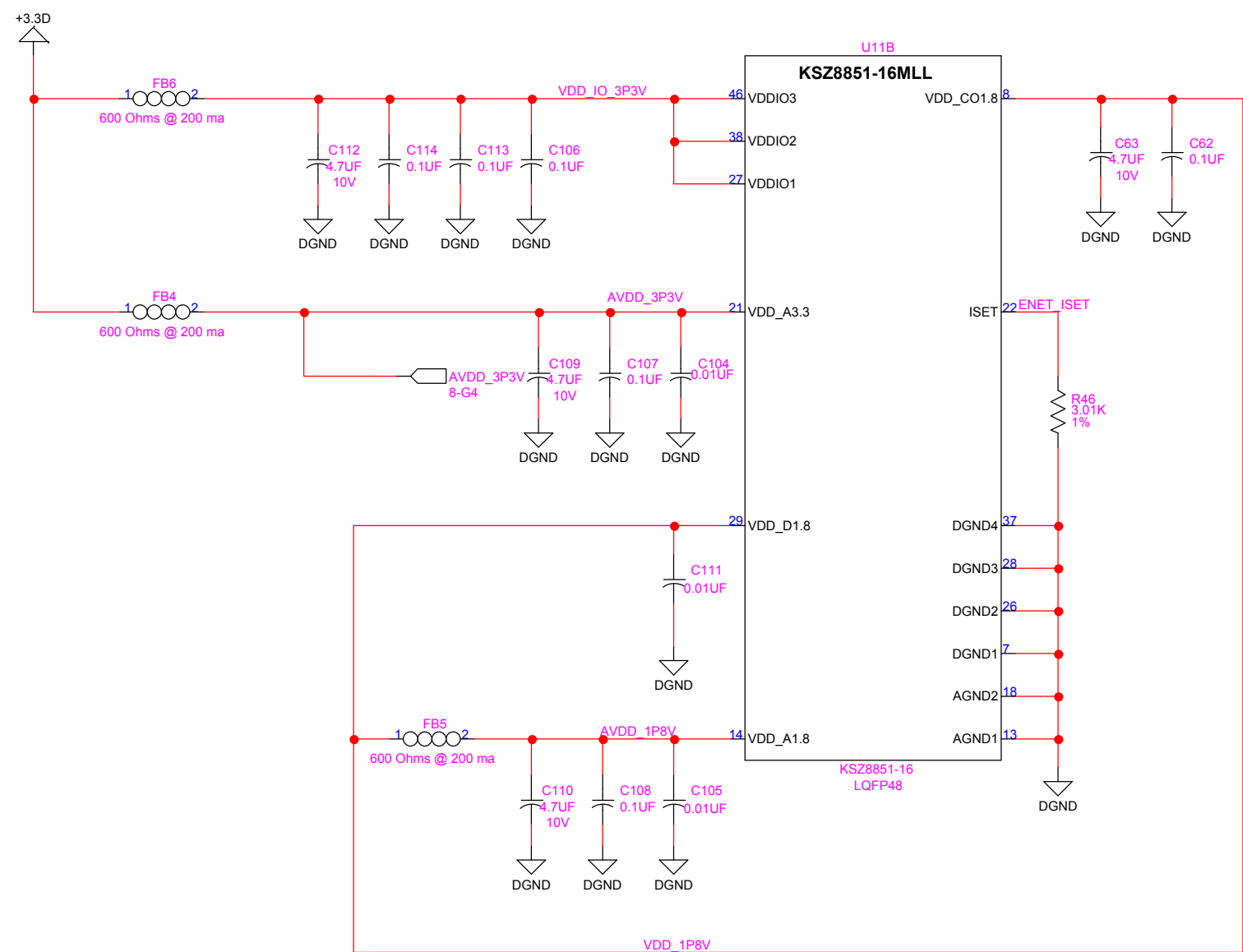
PINS 101-200, OEM MODULE CONNECTOR



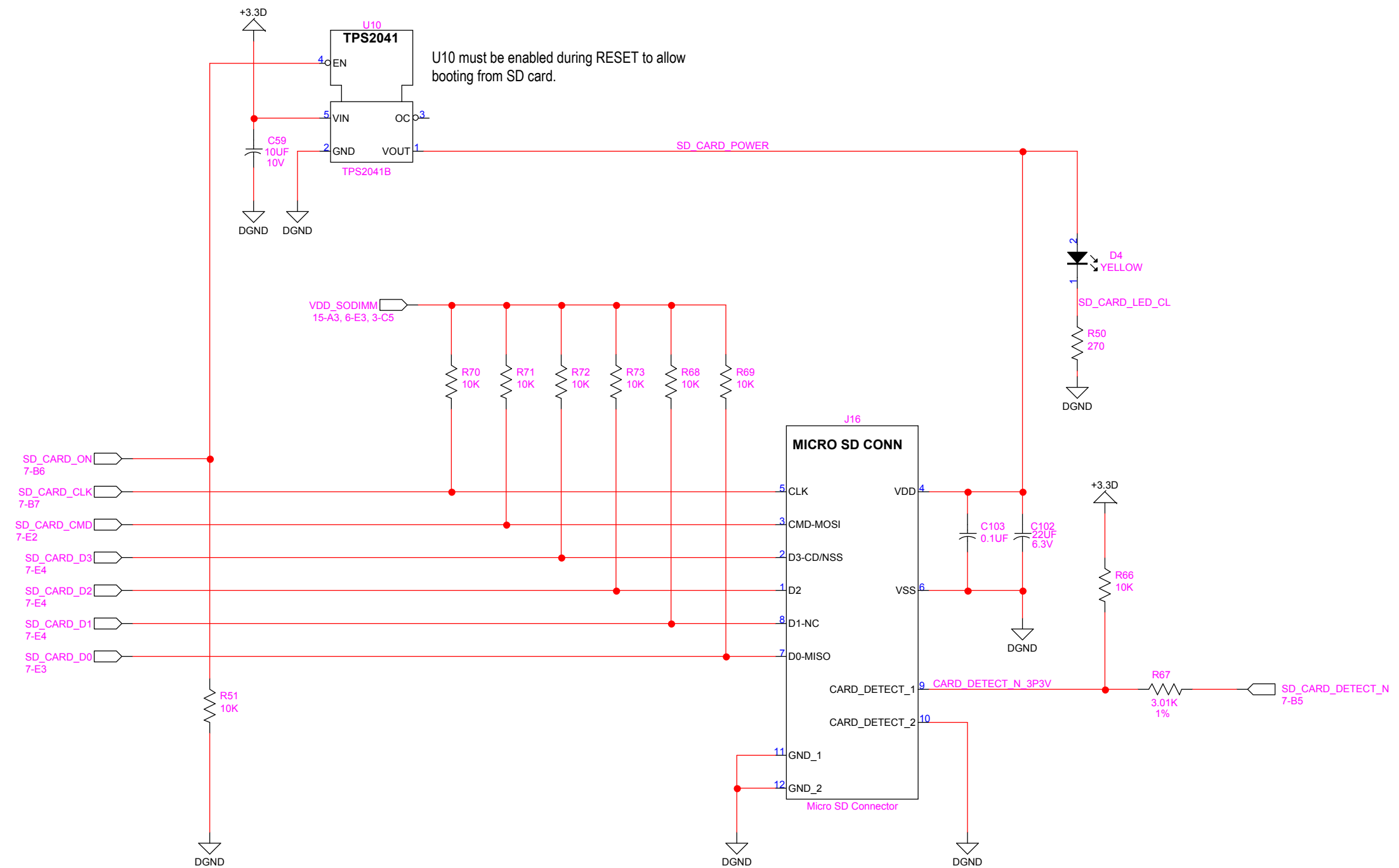
ETHERNET INTERFACE



ETHERNET POWER DISTRIBUTION

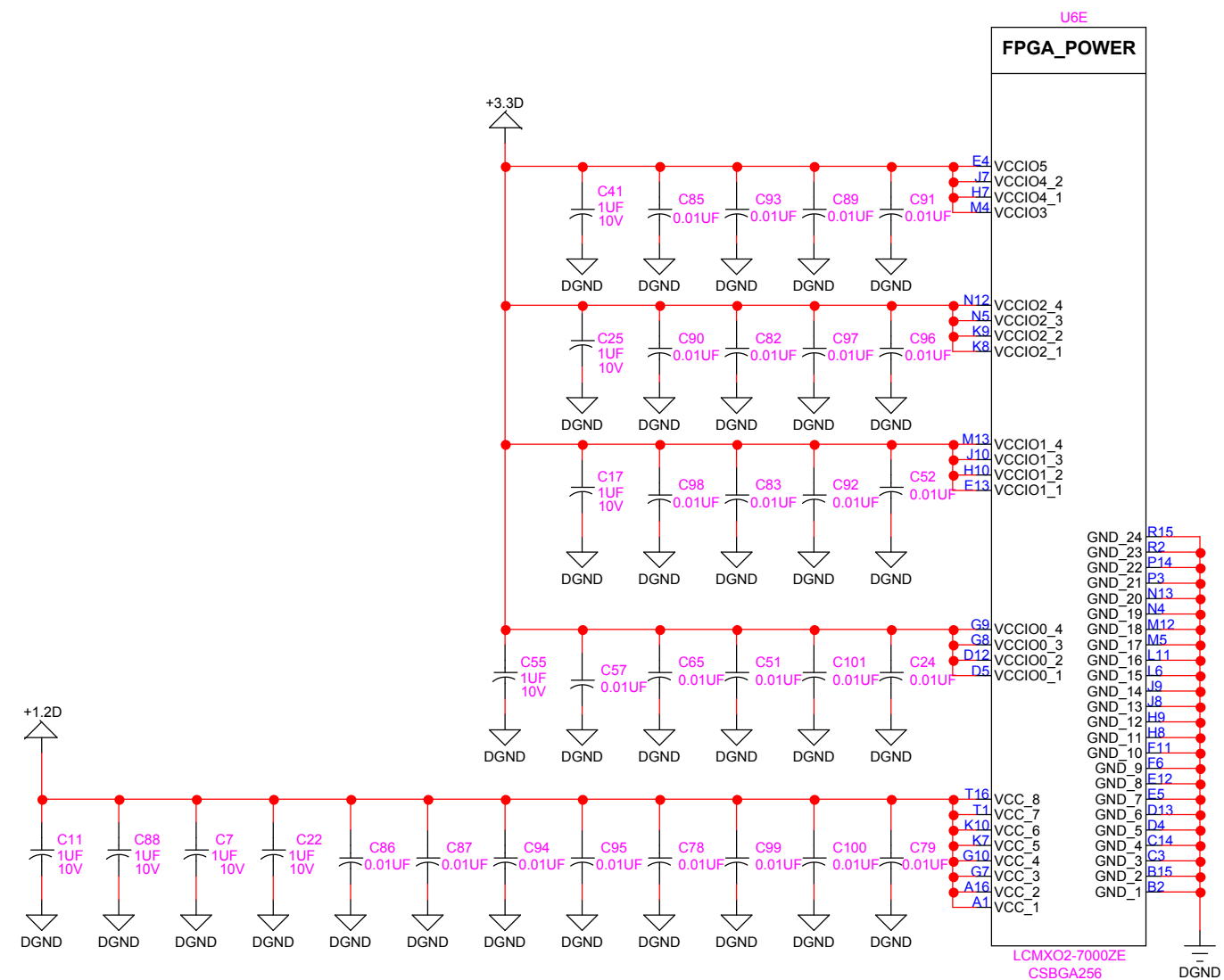


MICRO SD CARD INTERFACE

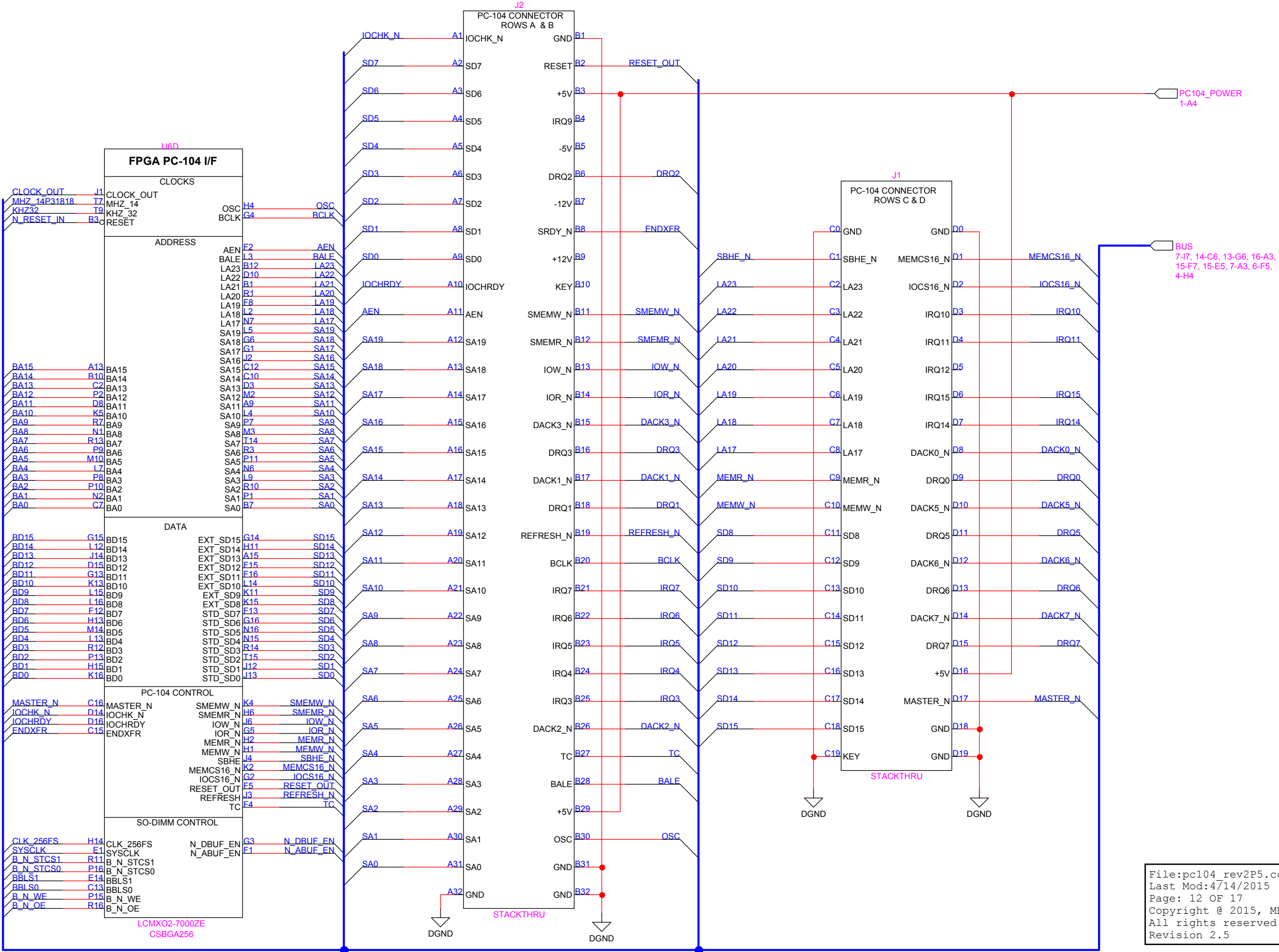


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Last Mod:4/14/2015
Page: 10 OF 17
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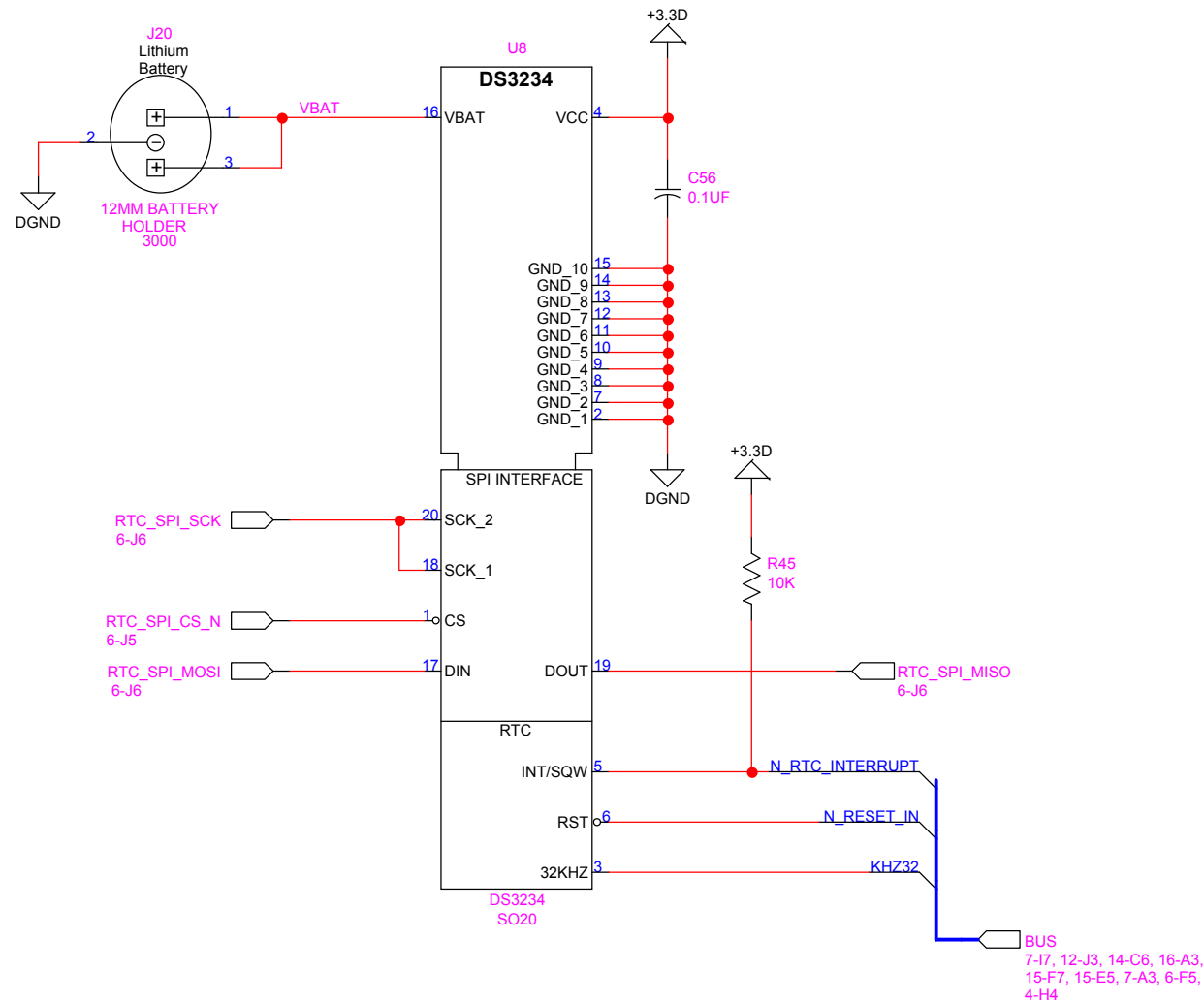
FPGA POWER DISTRIBUTION



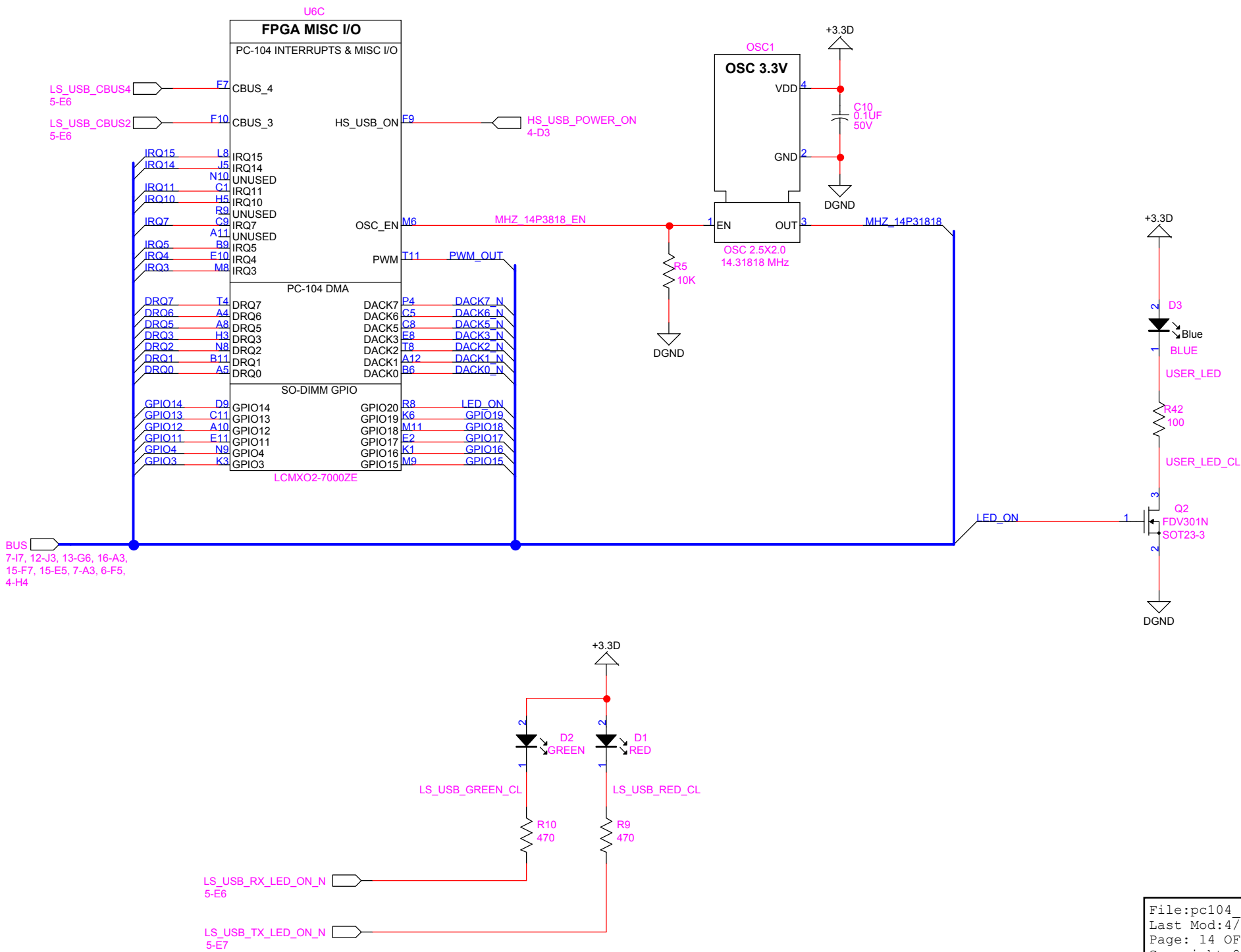
PC-104 INTERFACE



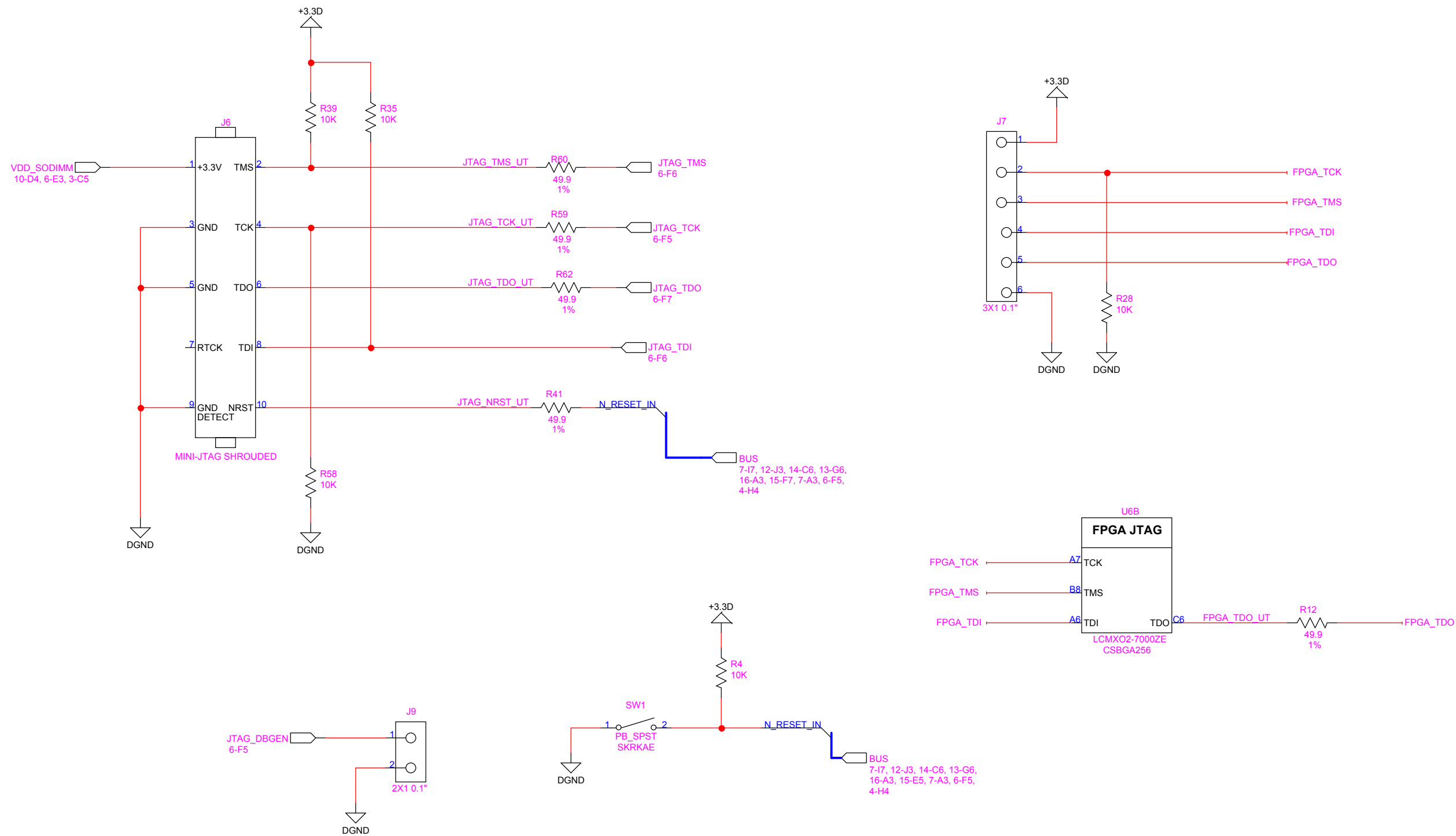
REAL TIME CLOCK



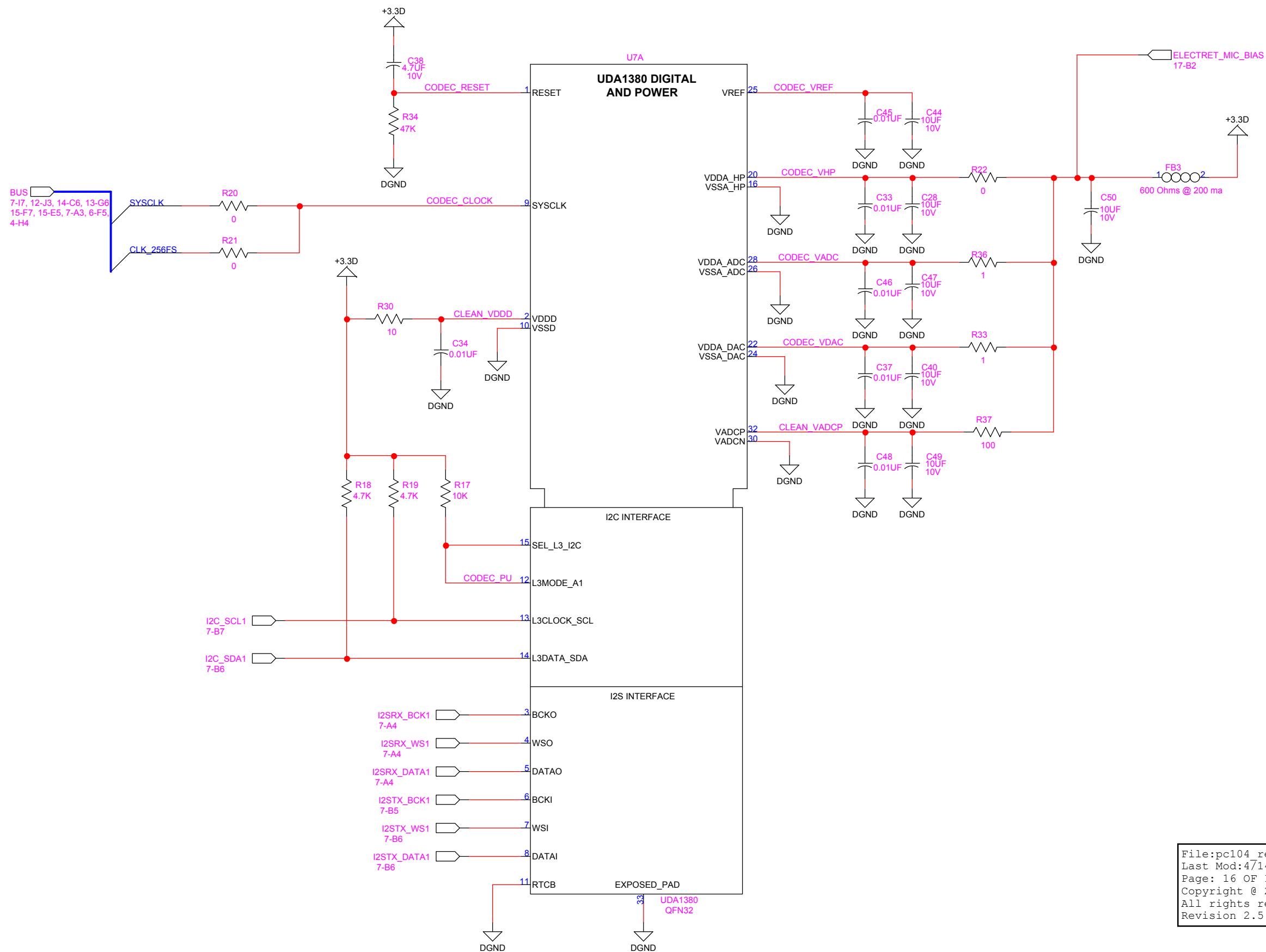
MISCELLANOUS I/O



CPU AND FPGA JTAG



BRENT'S INFAMOUS AUDIO CODEC



File:pc104_rev2P5.cct
Last Mod:4/14/2015
Page: 16 OF 17
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TWO TURNTABLES AND A MICROPHONE

