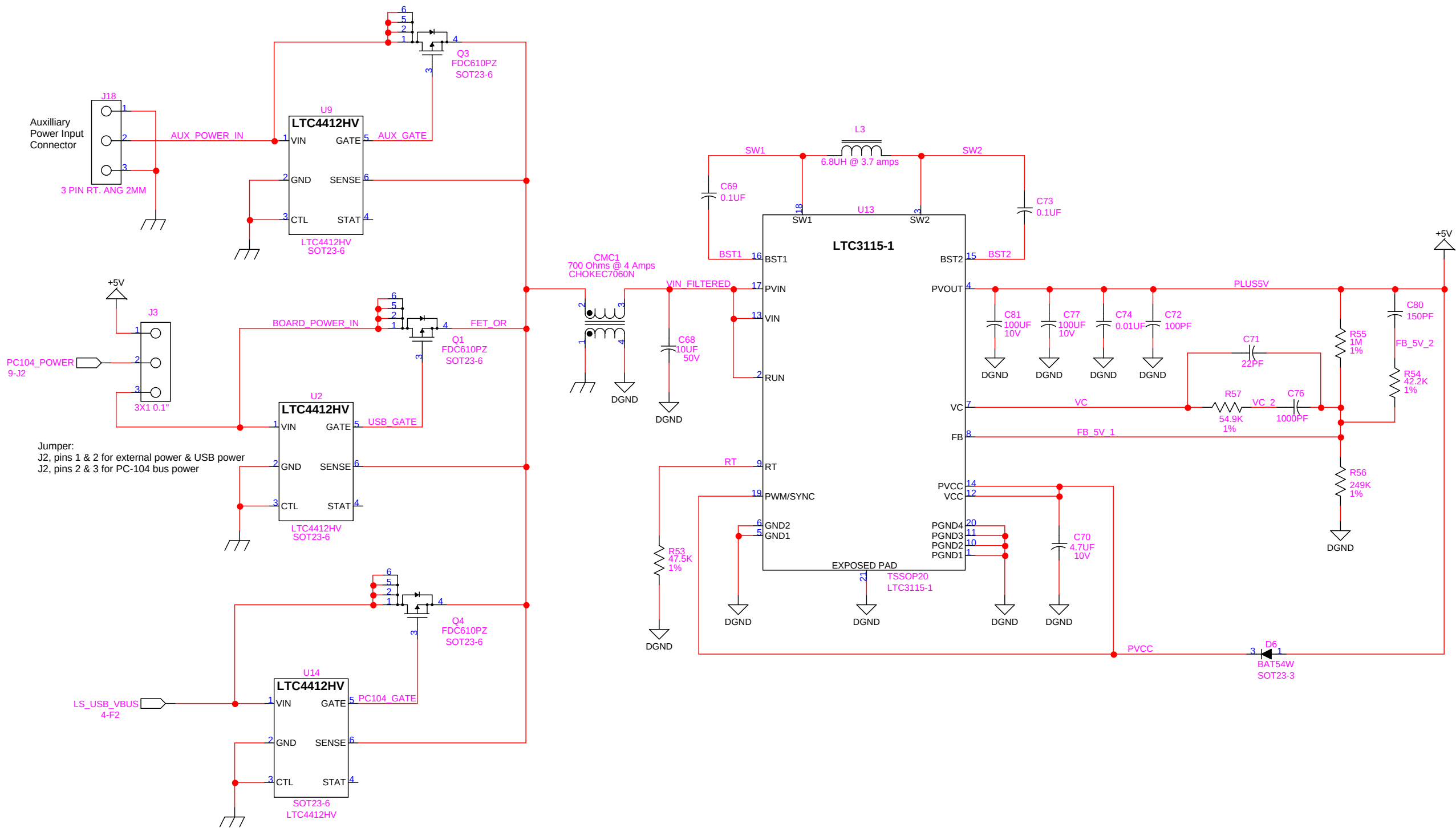
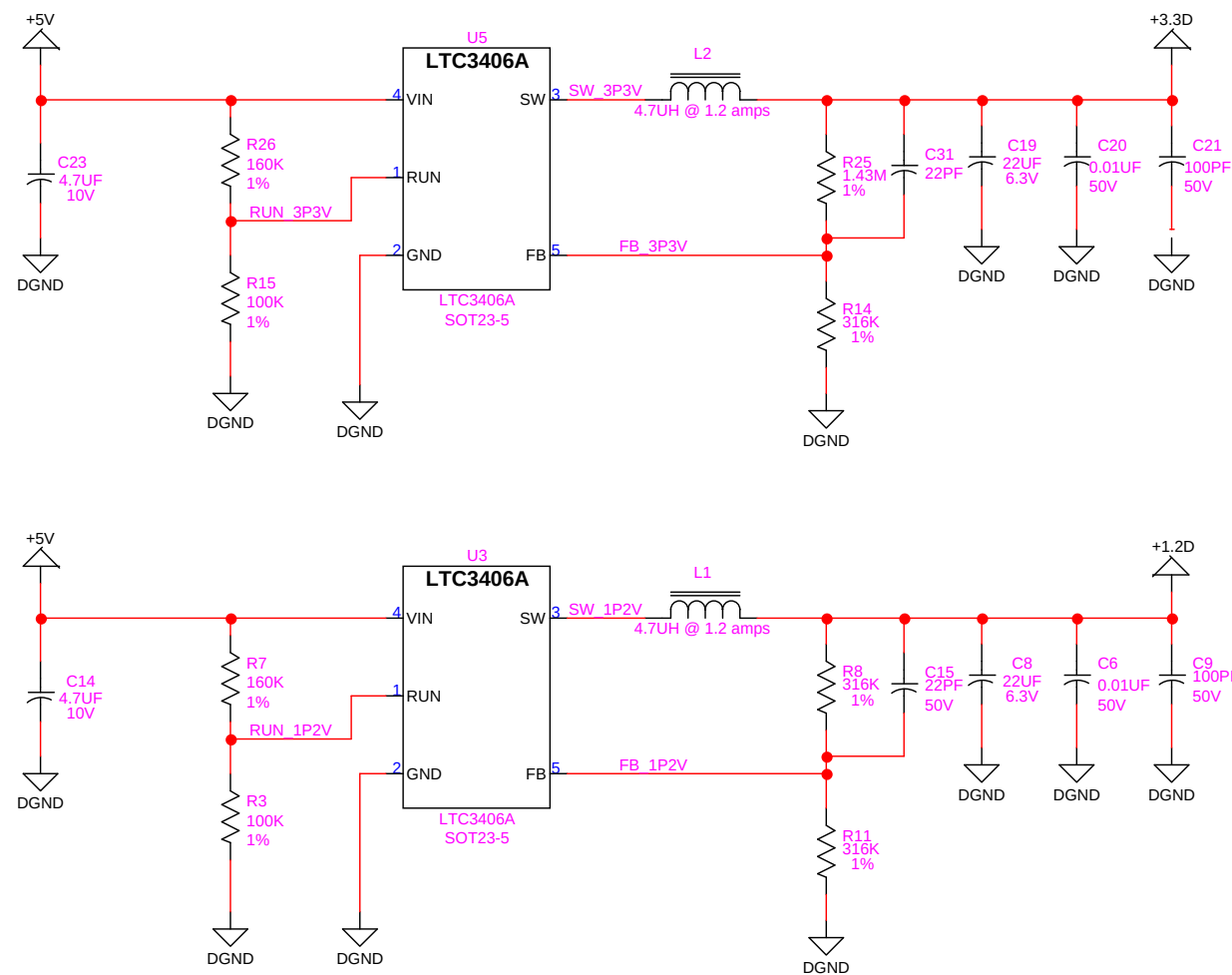


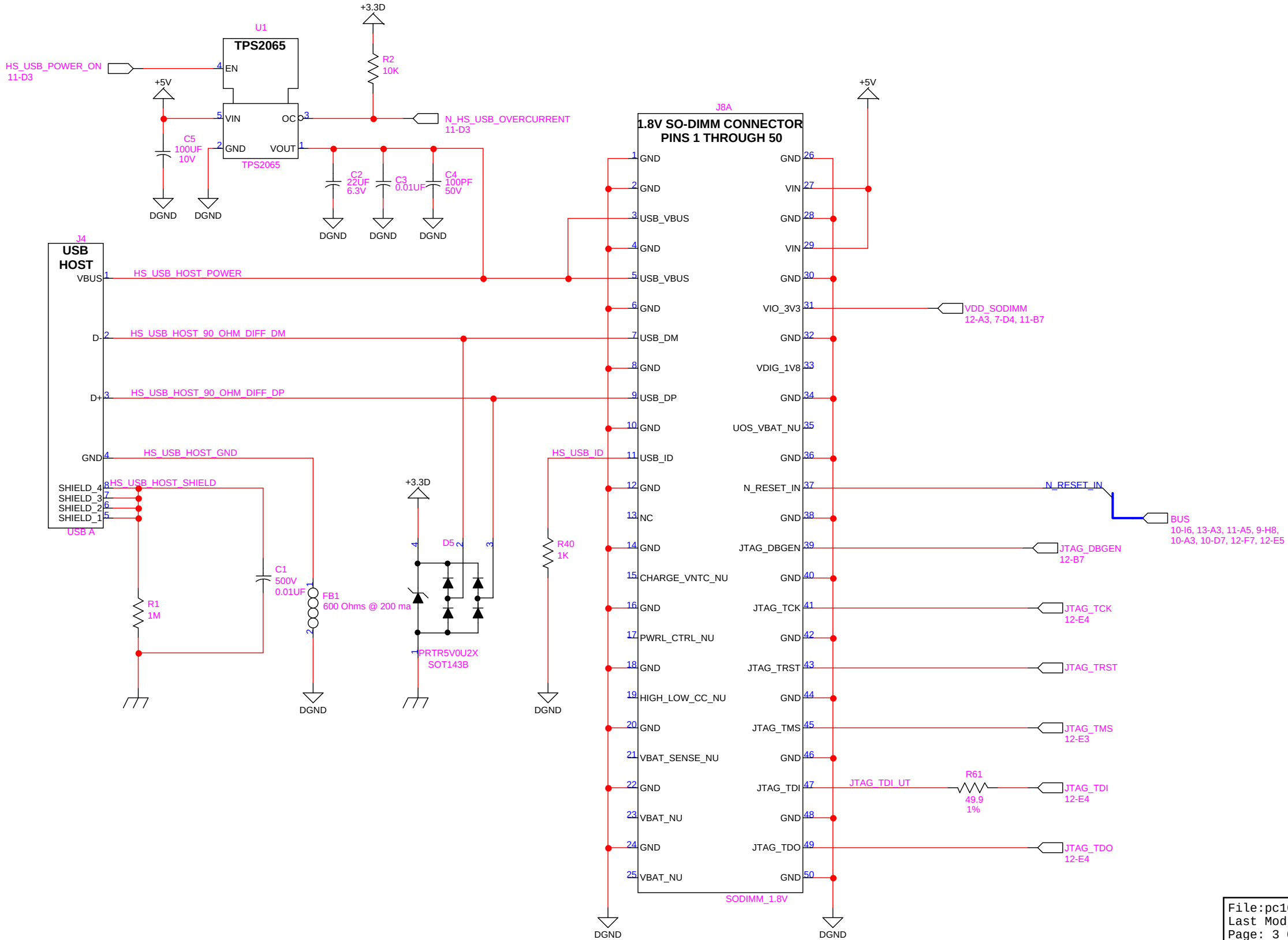
POWER SUPPLY



CORE POWER SUPPLIES

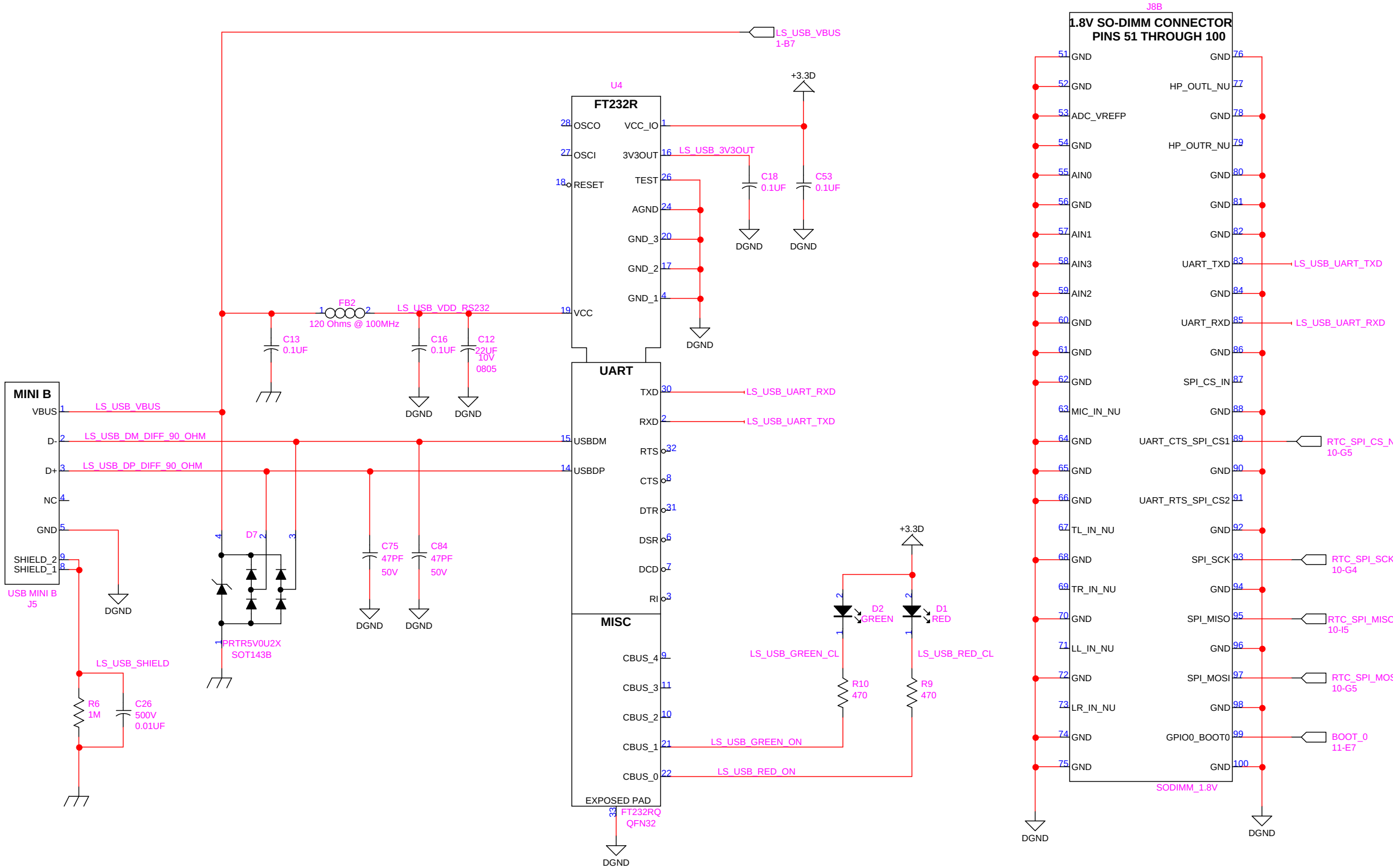


H/S USB HOST



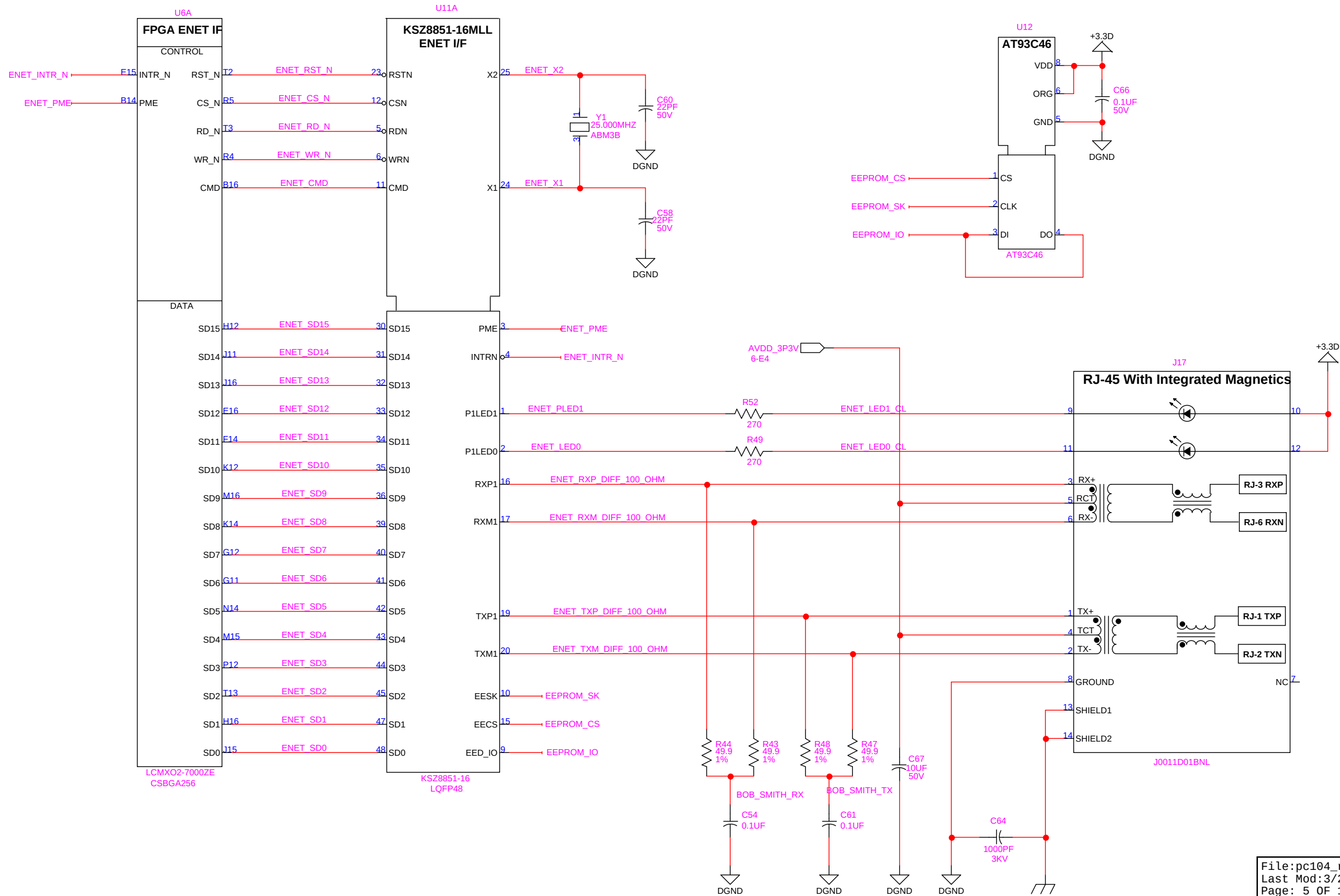
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USB TO SERIAL INTERFACE

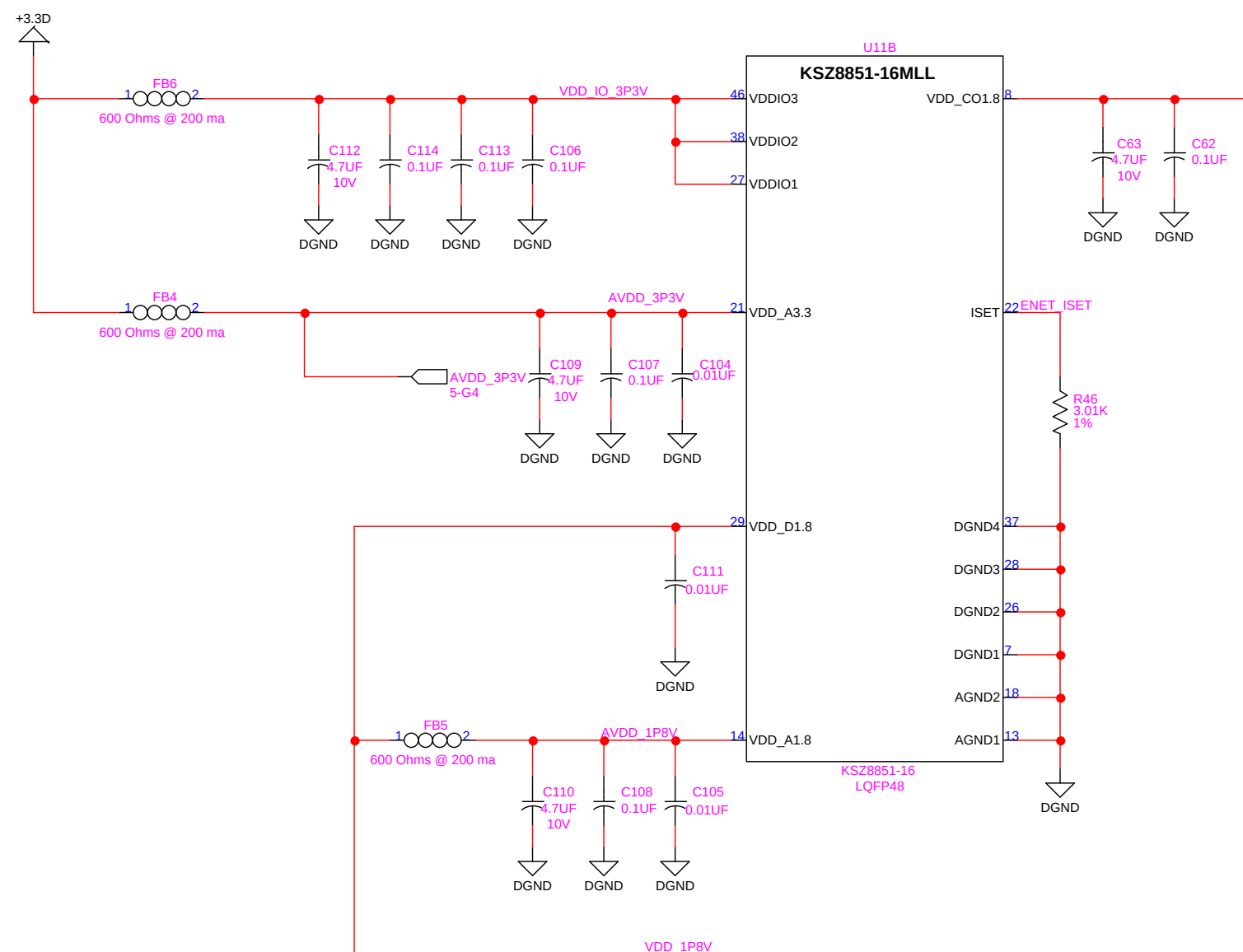


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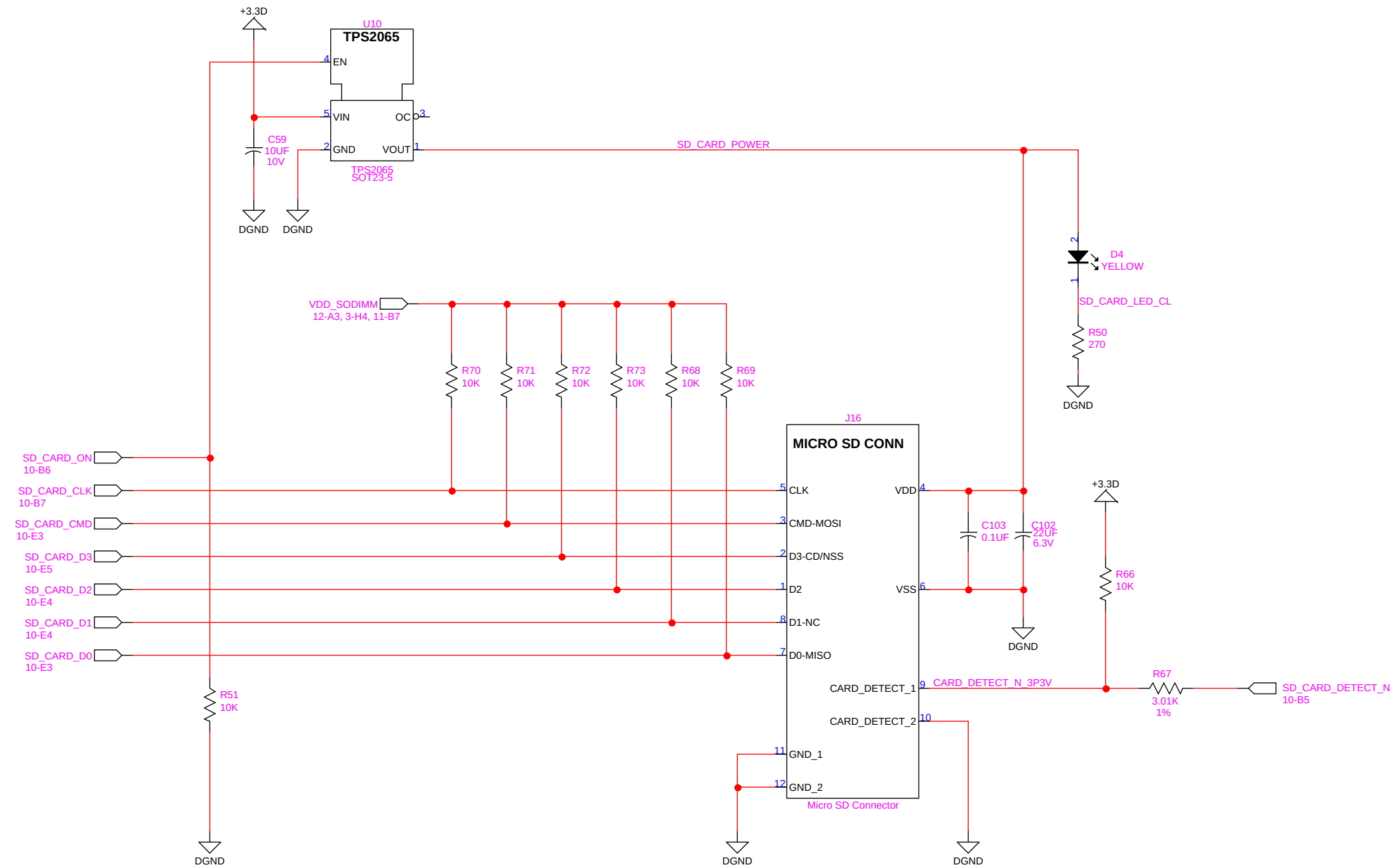
ETHERNET INTERFACE



ETHERNET POWER DISTRIBUTION

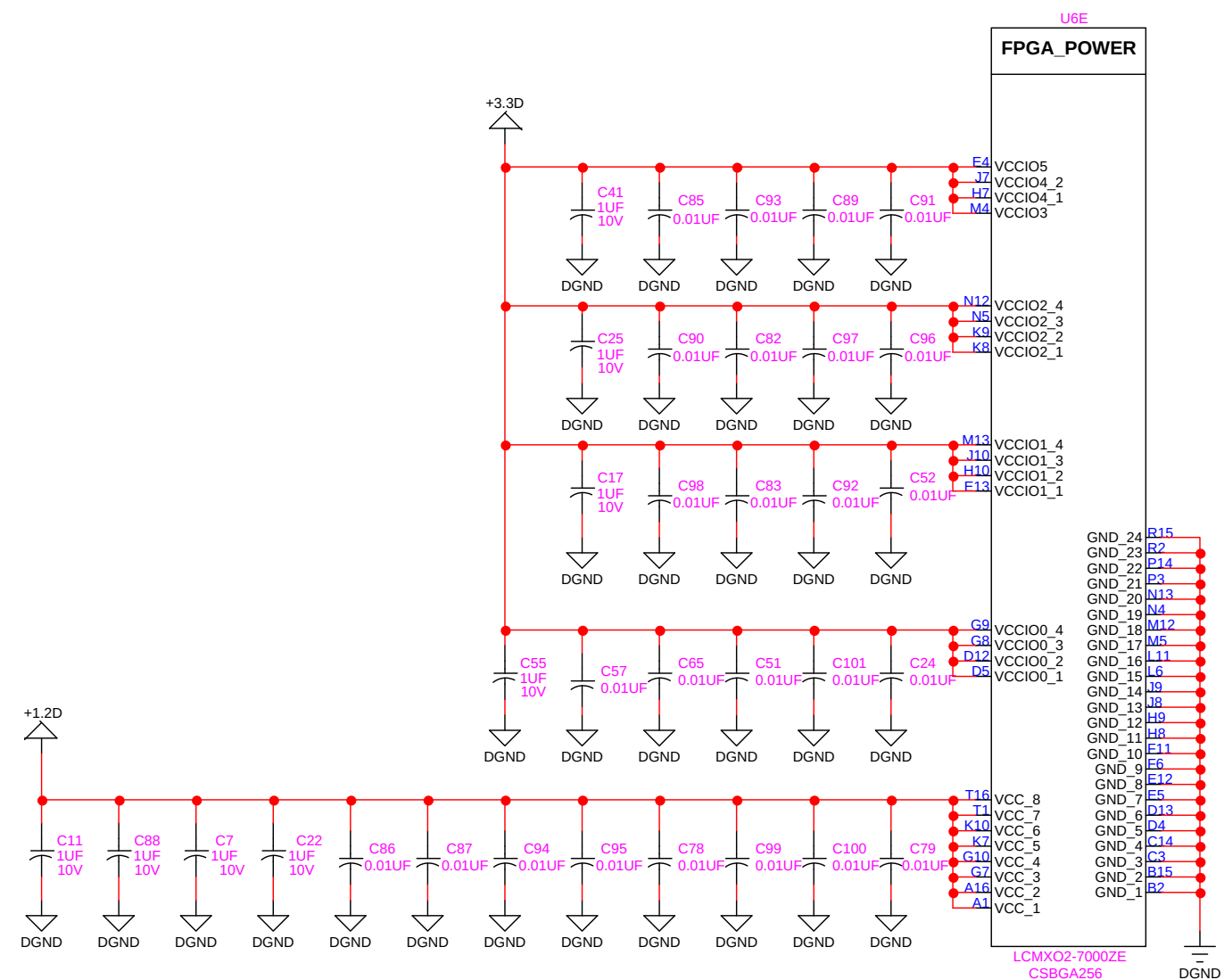


MICRO SD CARD INTERFACE

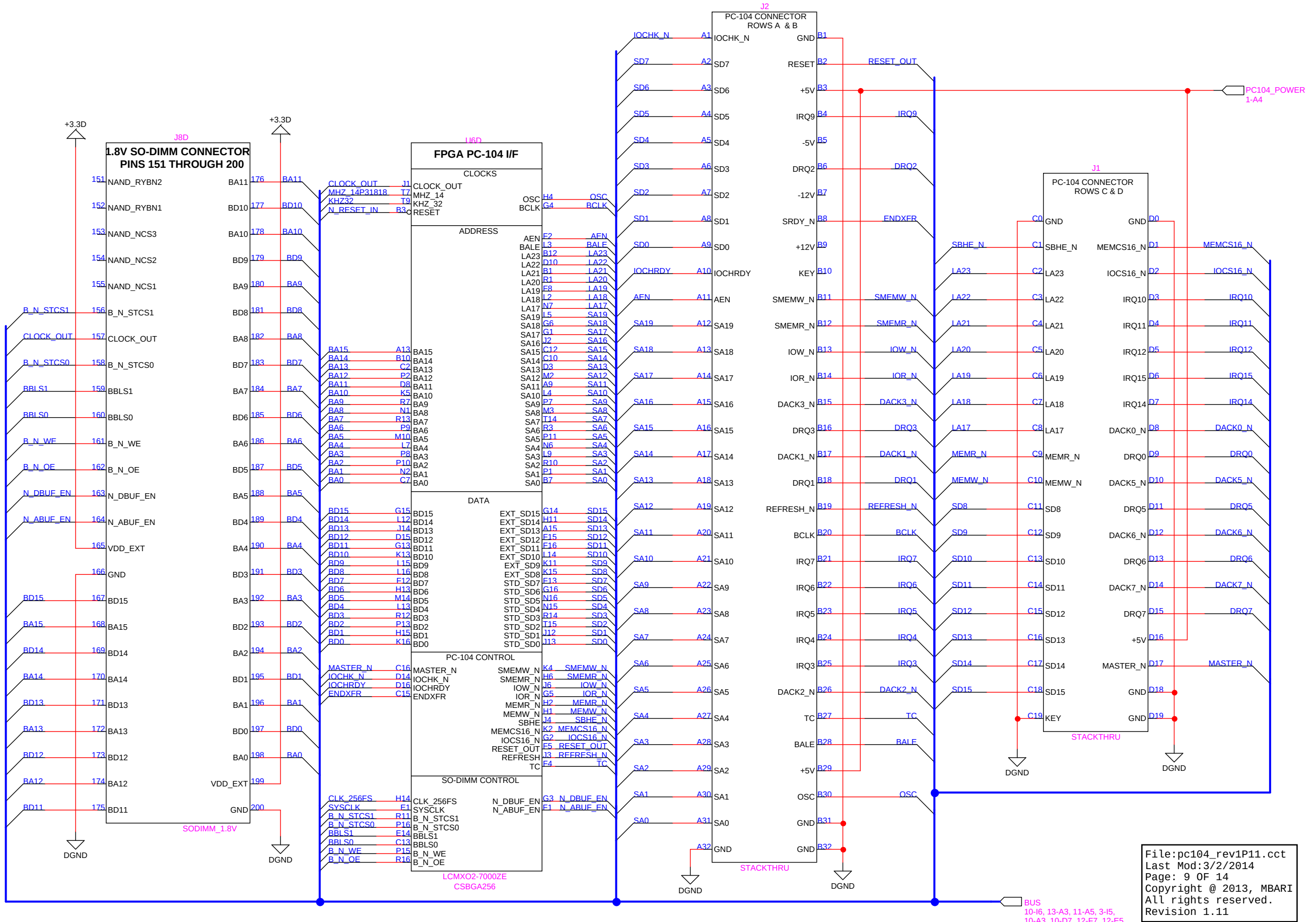


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FPGA POWER DISTRIBUTION

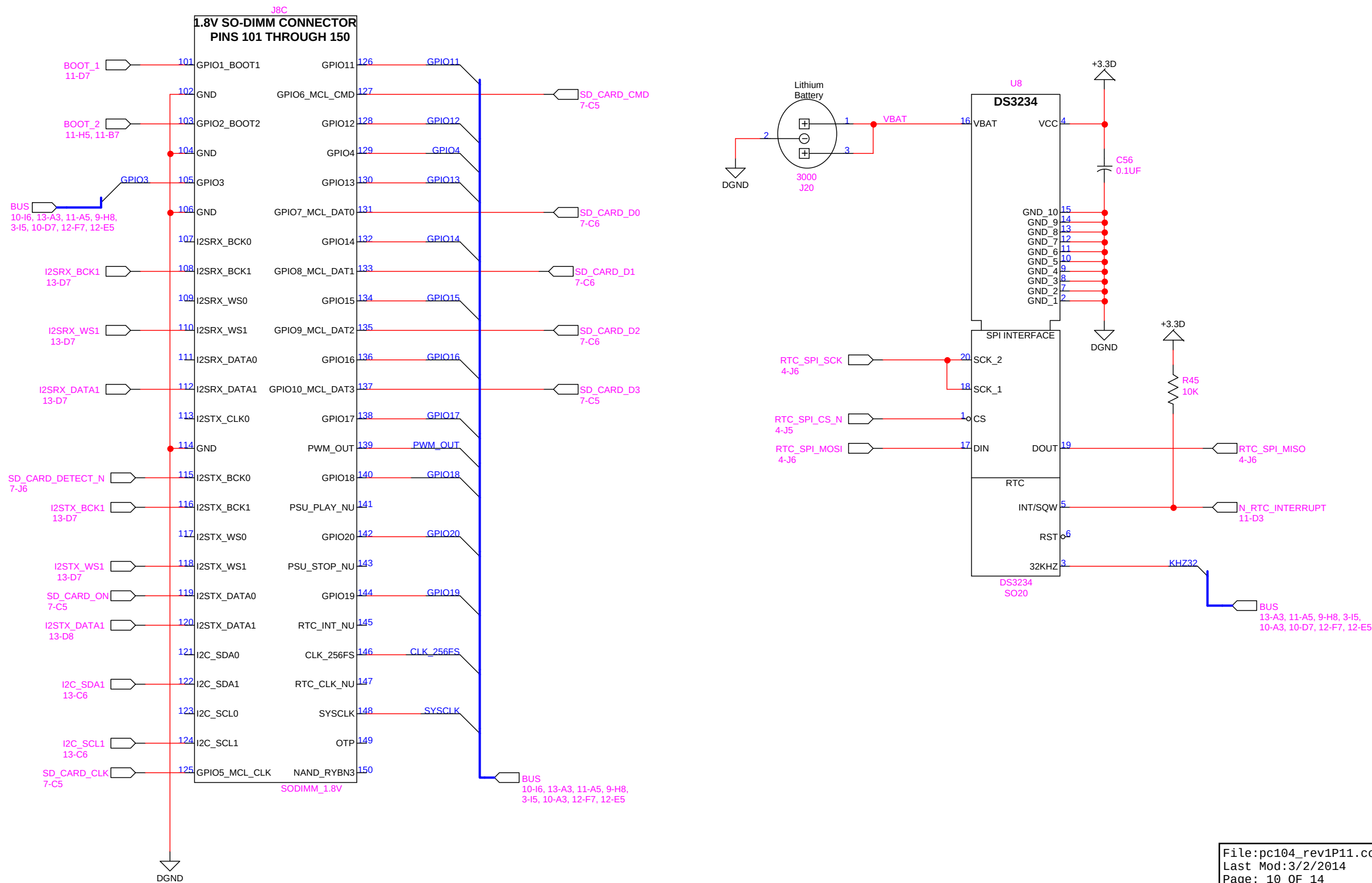


PC-104 INTERFACE

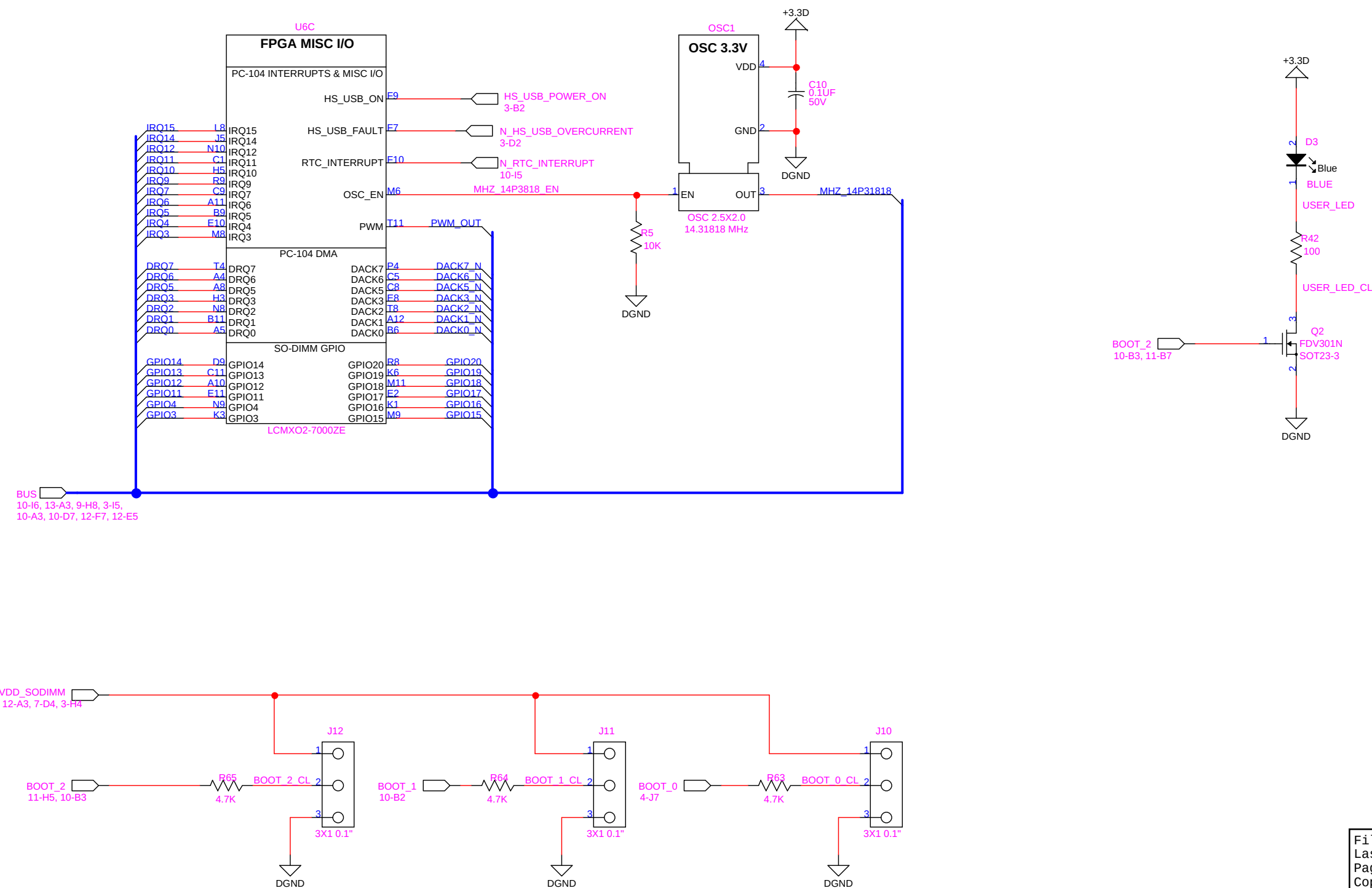


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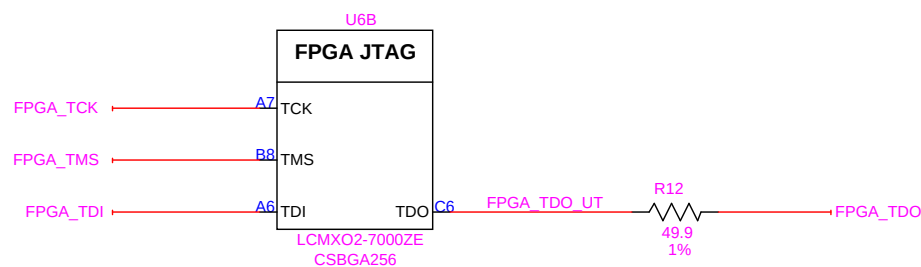
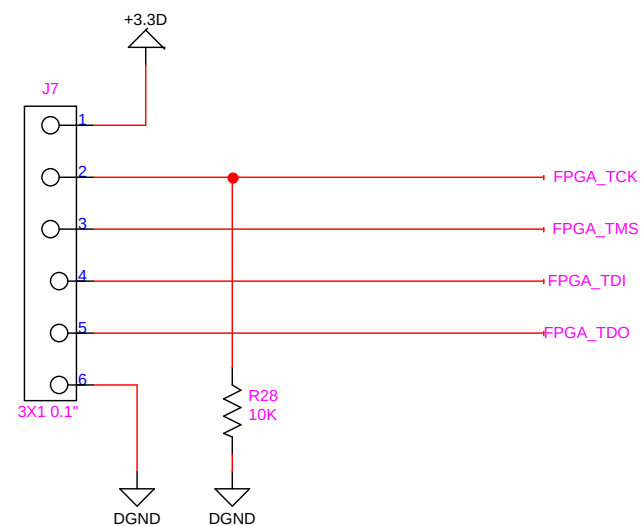
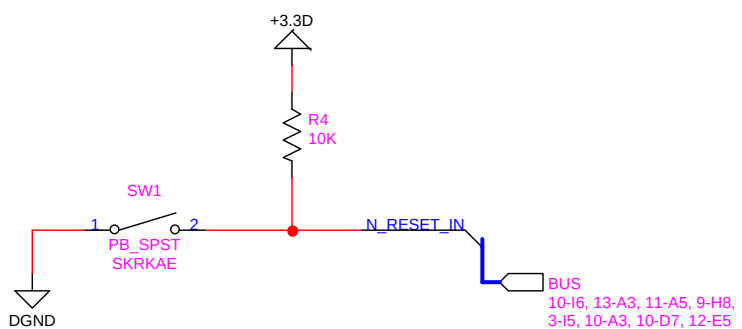
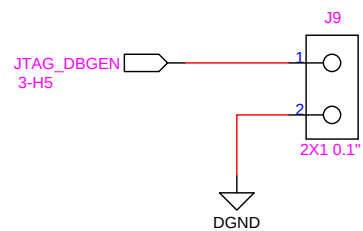
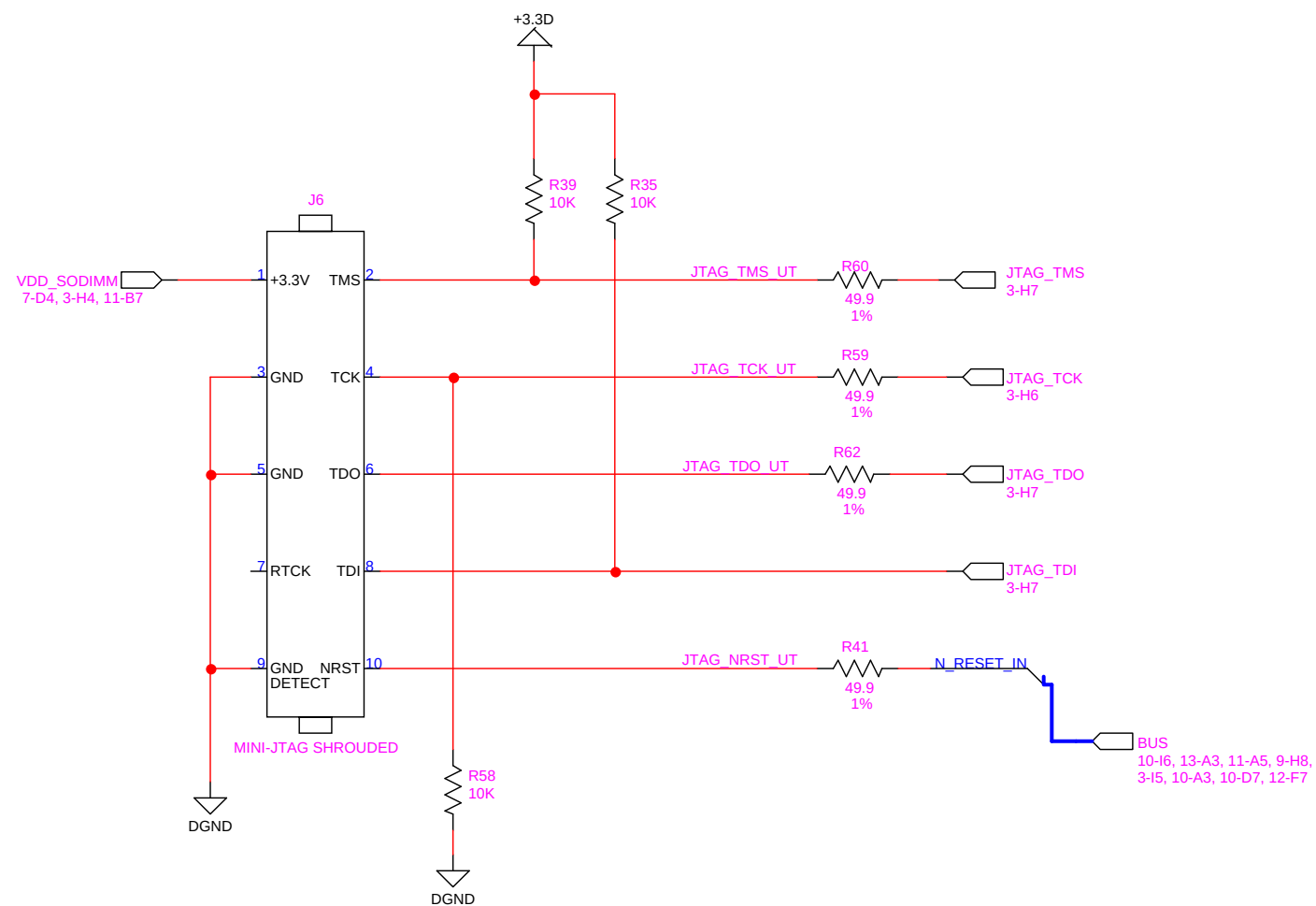
REAL TIME CLOCK



MISCELLANEOUS I/O AND BOOTSTRAP JUMPERS

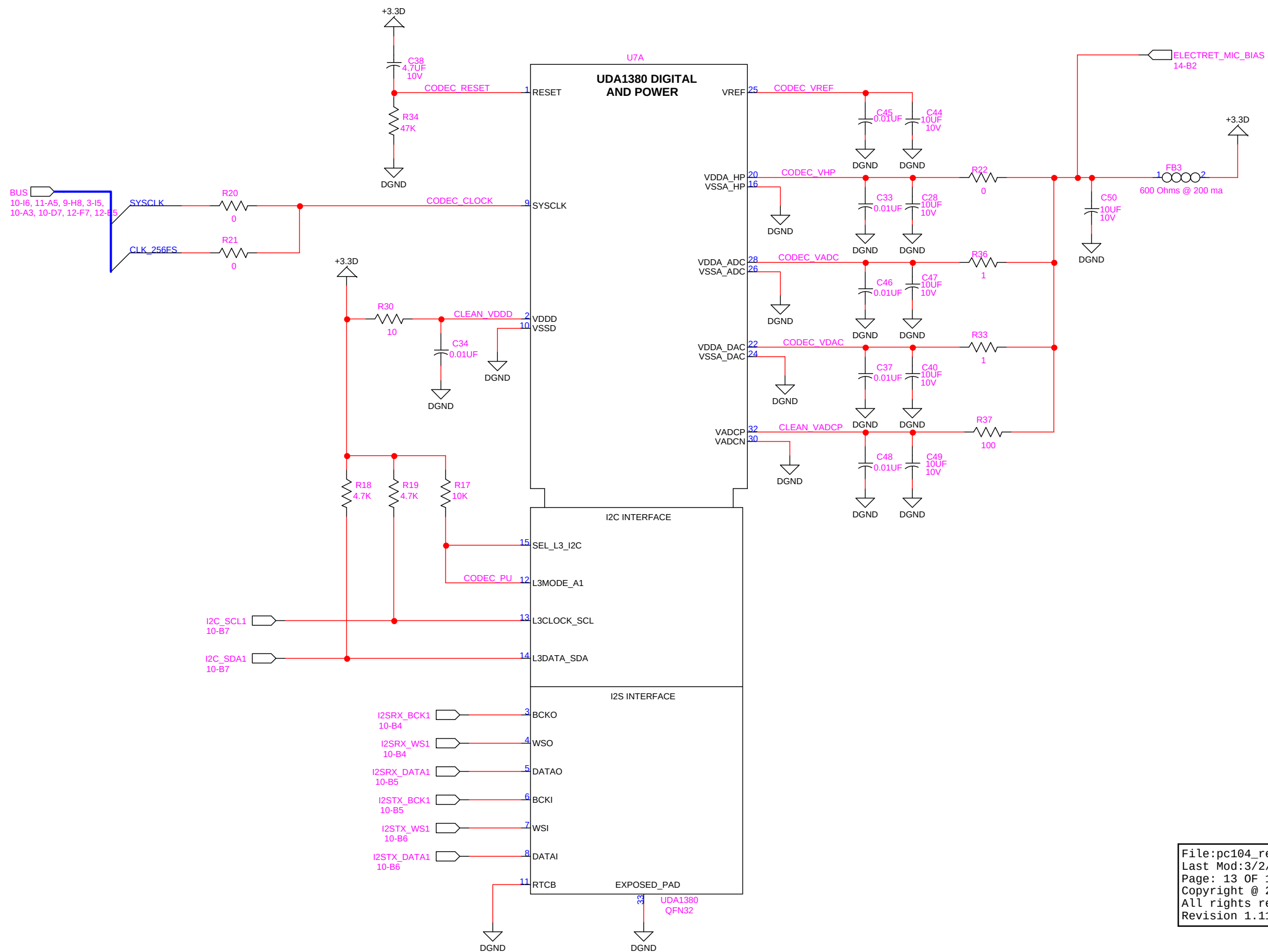


CPU AND FPGA JTAG



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BRENT'S INFAMOUS AUDIO CODEC



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TWO TURNTABLES AND A MICROPHONE

