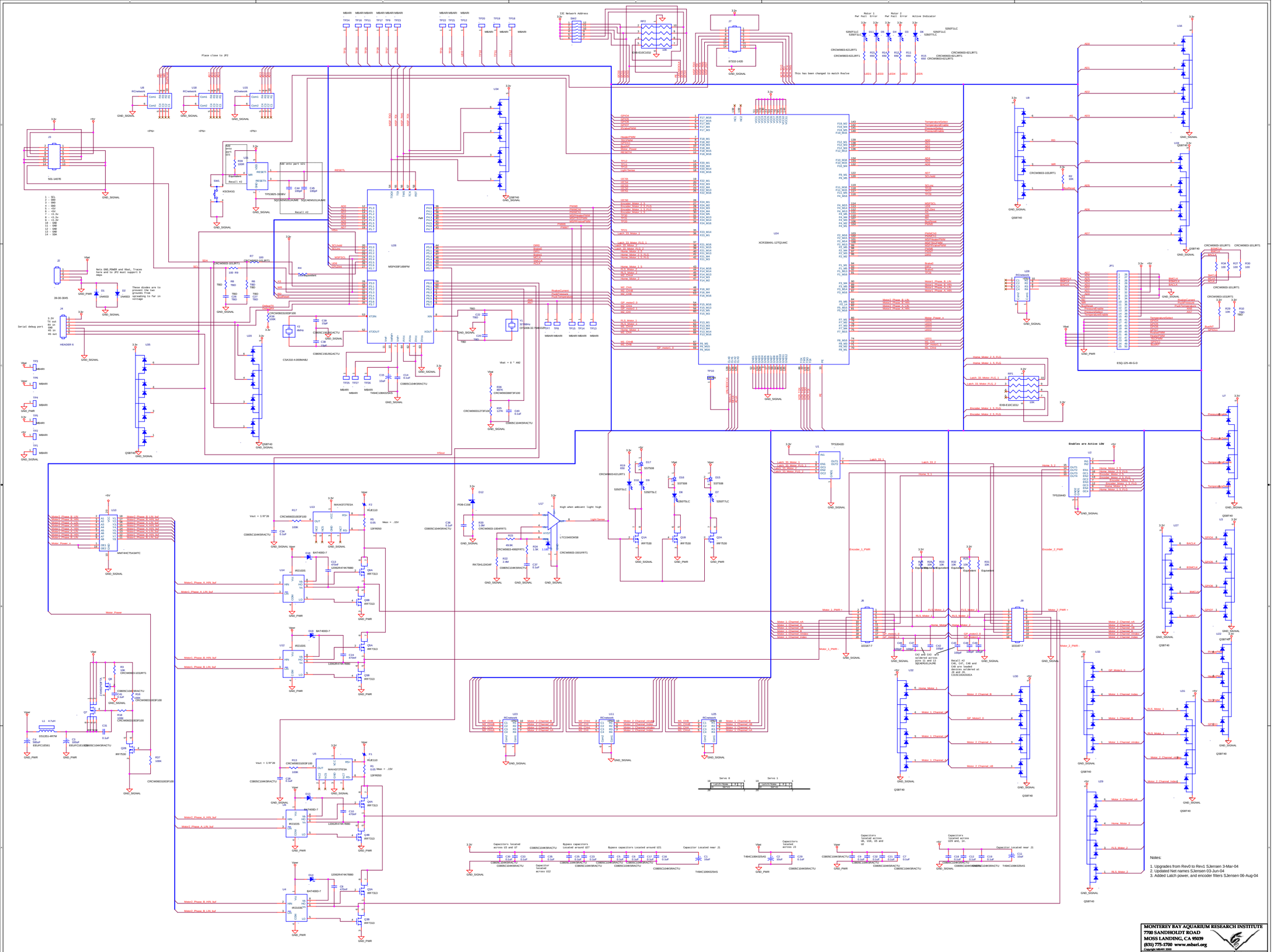
Place close to JP2
I2C Network Address
Motor 1 Par Fail Error
Motor 2 Par Fail Error
Active Indicator
This has been changed to match Ovalve
Enables are Active LOW
Encoder_1_PWR
Encoder_2_PWR
Motor_Power
Motor_1_PWR+
Motor_1_PWR-
Motor_2_PWR+
Motor_2_PWR-
Serial debug port
Vout = 1/8*20
Vout = 1/8*20
Vmax = .15V
Vmax = .15V
High when ambient light high
Light Sense
Servo 0
Servo 1
Capacitors located across U3 and U7
Bypass capacitors located around U27
Bypass capacitors located around U21
Capacitor located near J1
Capacitors located across U29 and, 14.
Capacitor located near J1
Capacitor located across U32
Capacitors located across U6, U10, U5 and U2
Capacitors located across J3
Capacitor located near J1
Notes:
1. Upgrades from Rev0 to Rev1 SJensen 3-Mar-04
2. Updated Net names SJensen 03-Jun-04
3. Added Latch power, and encoder filters SJensen 06-Aug-04
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Copyright MBARI 2000
Title: Dwarf Core
Number: 1002250
Size: D
Engineer: Scott Jensen
Author: Jose Rosal
Filename: Core_REV_A_X_X
Rev: 2.0.4
Date: Tuesday, March 24, 2015
Sheet 1 of 1