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Power supply

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Digital and Analogue IO

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Expansion connector (SODIMM)

Rev 1.3

Added U18 and R58-R60 to create more flexible reset generation. R59 mouned by default = same as rev 1.2.

Rev 1.2

Updated design to also fit LPC314x.
Changed value on R18, R19, R25.

Rev 1.1

Added test points for voltages.
Added R56/R57 to isolate I2C communication of U1.

Rev 1.0

Original revision



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TITLE: LPC313x_4x SODIMM v1.3

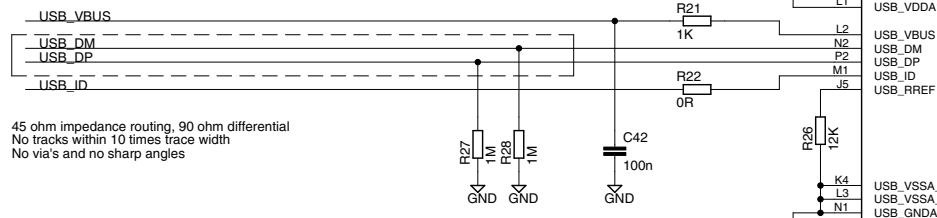
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REV:

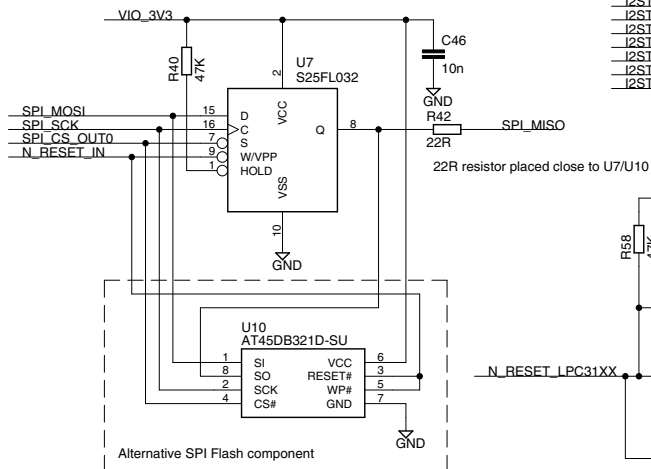
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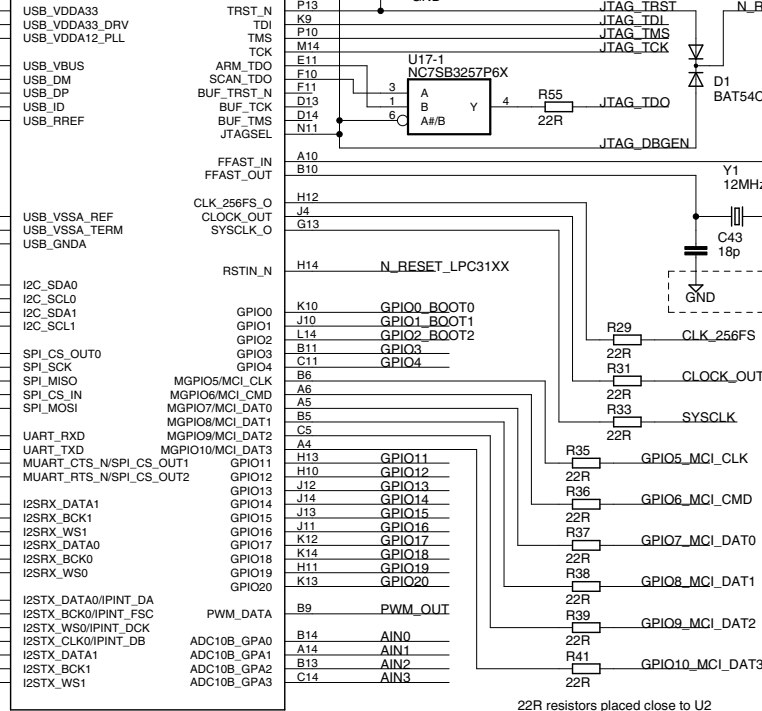
USB 2.0 OTG Interface



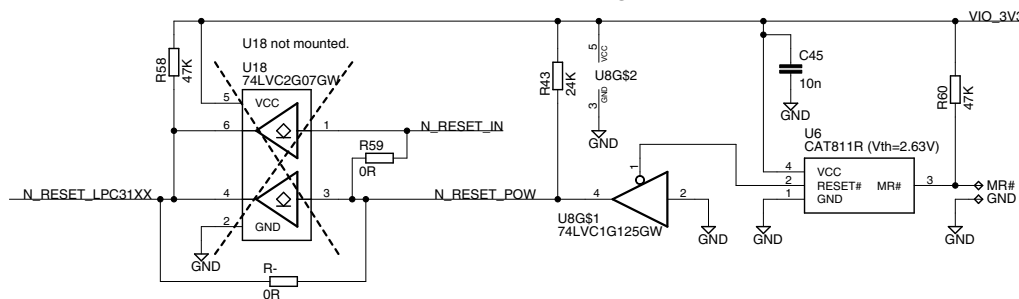
32Mbit SPI-FLASH



U2-2 LPC313x4x



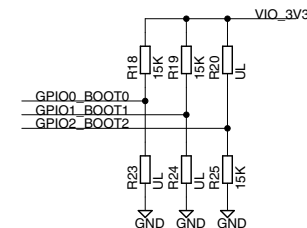
Reset generation



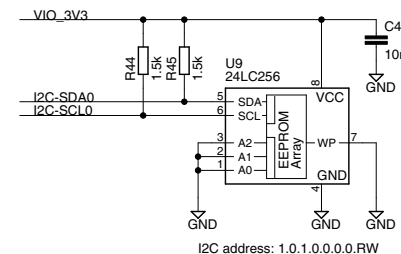
Boot mode

GPIO0	GPIO1	GPIO2	Boot source
LOW	LOW	LOW	NAND boot
LOW	LOW	HIGH	SPI boot
LOW	HIGH	LOW	USB via DFU class
LOW	HIGH	HIGH	MMC/SD card
HIGH	HIGH	LOW	UART 115.2kbps 8N1
HIGH	HIGH	HIGH	Test mode

Default



256kbit EEPROM



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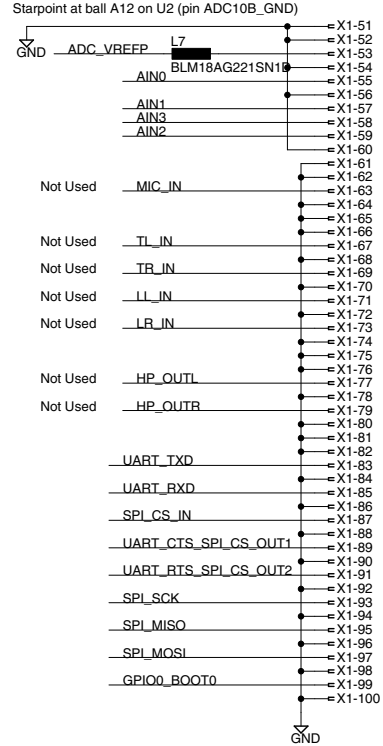
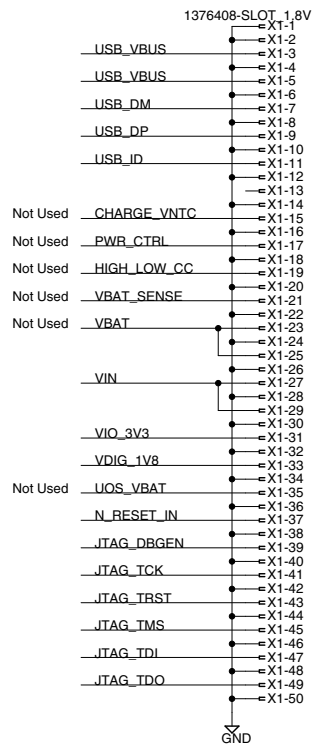
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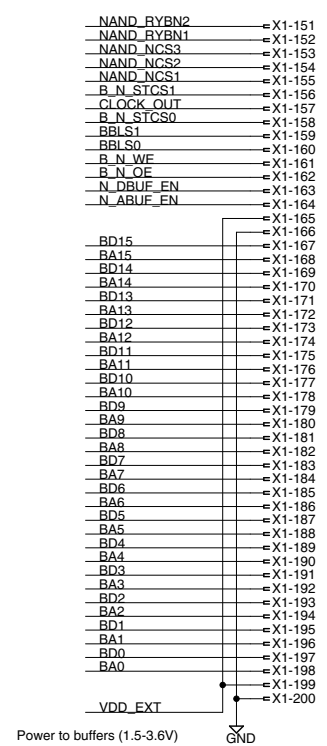
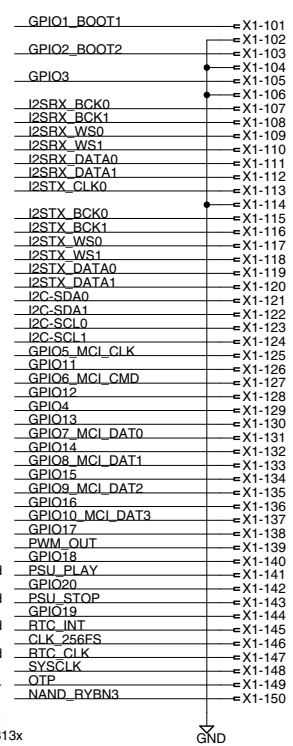
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Used on LPC314x
Not used on LPC313x



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