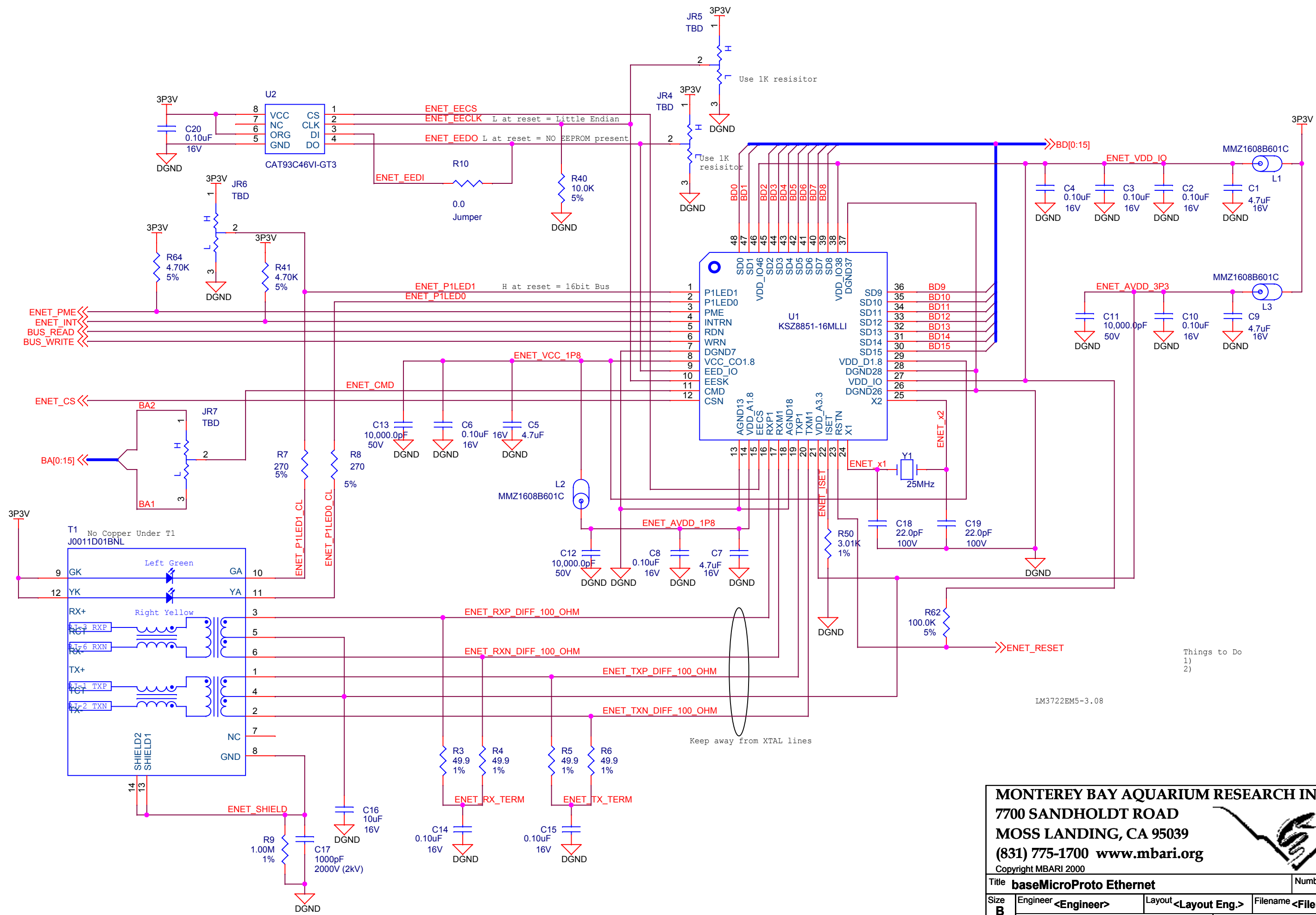
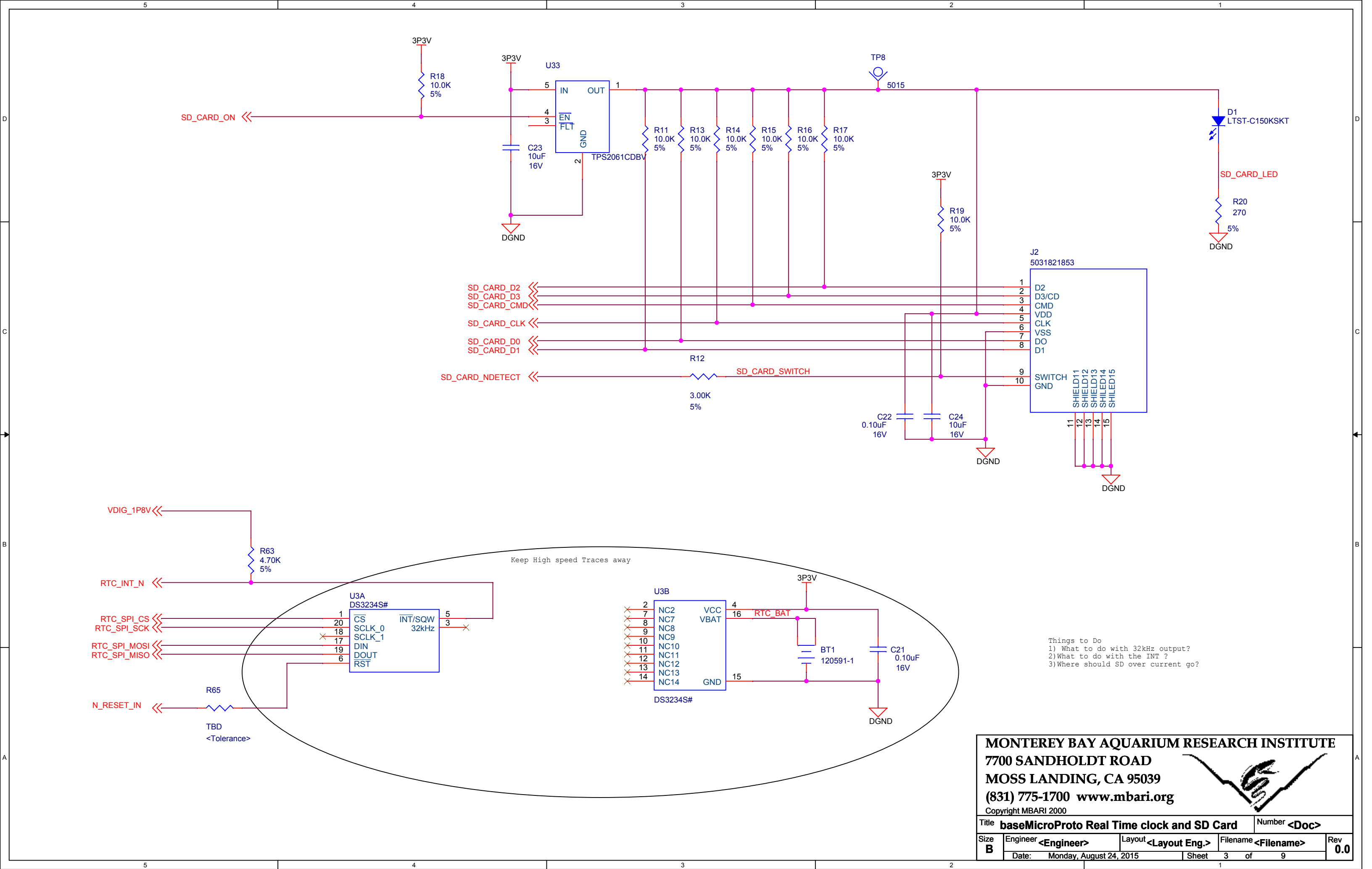
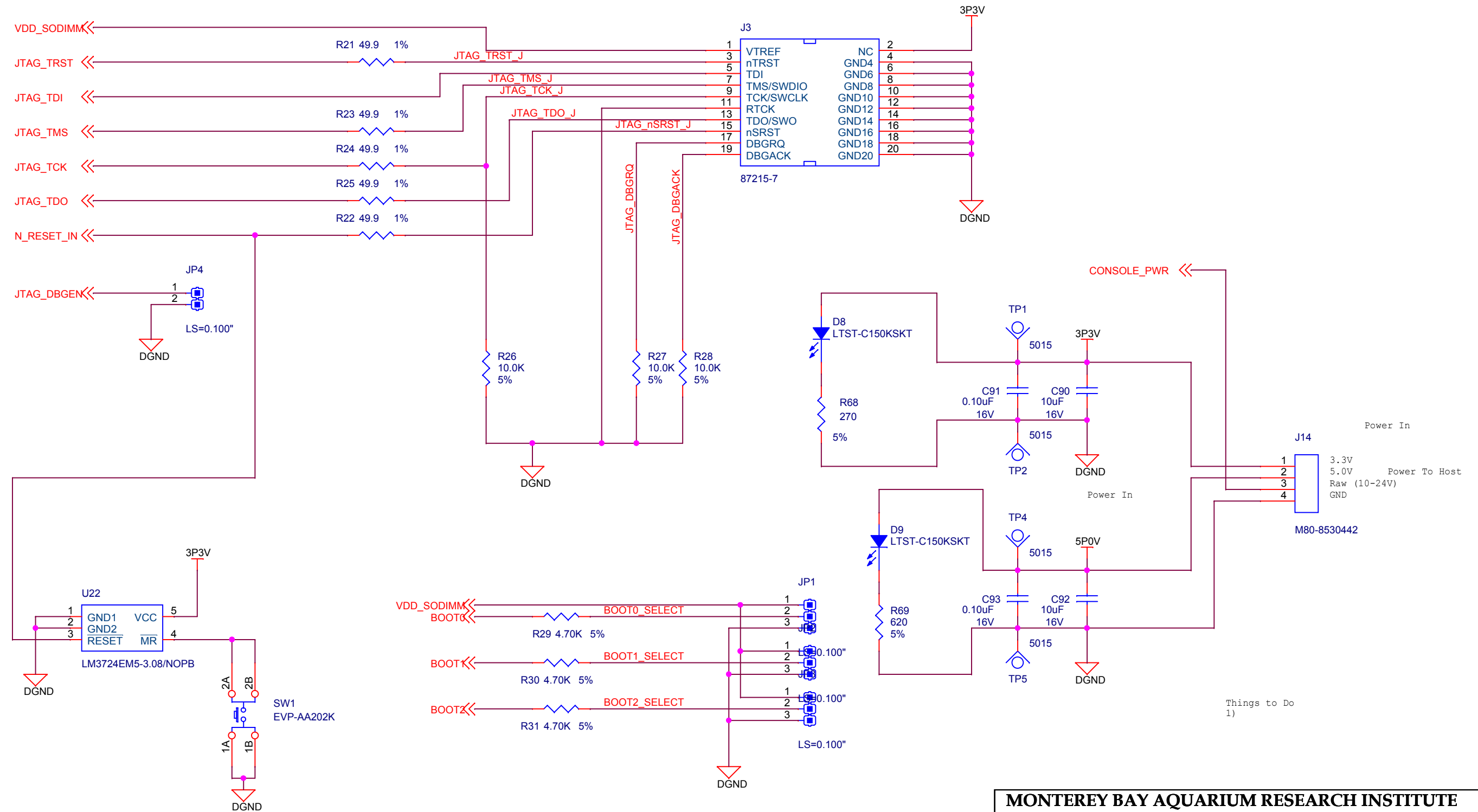
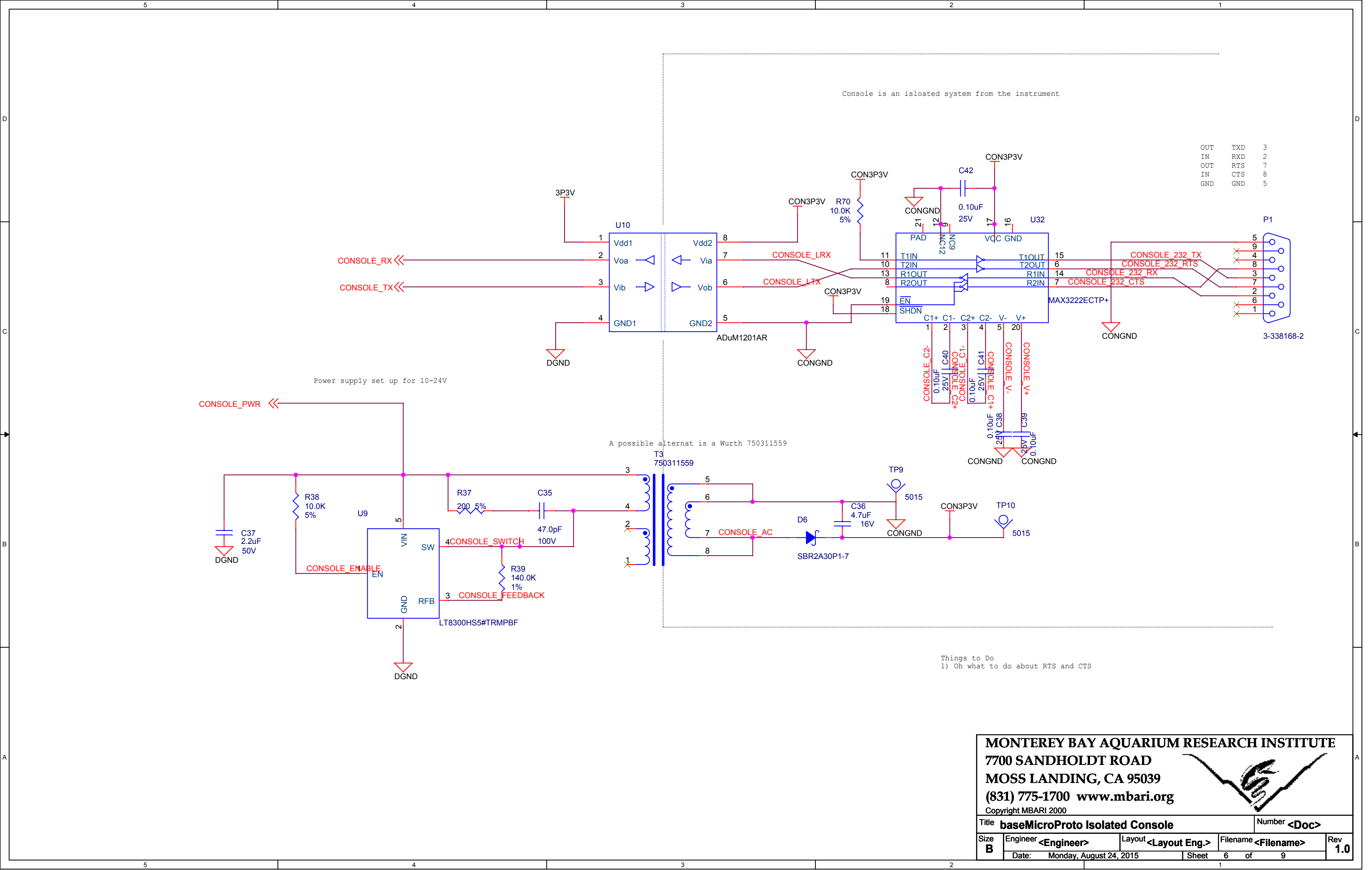










100 RX0

99 CTS0#

98 DSR0#

97 CD0#

96 RI0#

95 RTS0#

94 DTR0#

93 TX0

92 RX1

91 CTS1#

90 DSR1#

89 CD1#

88 RI1#

87 RTS1#

86 DTR1#

85 TX1

73 RX2

74 CTS2#

75 DSR2#

76 CD2#

77 RI2#

78 RTS2#

79 DTR2#

80 TX2

65 RX3

66 CTS3#

67 DSR3#

68 CD3#

69 RI3#

70 RTS3#

71 DTR3#

72 TX3

57 RX4

58 CTS4#

59 DSR4#

60 CD4#

61 RI4#

62 RTS4#

63 DTR4#

64 TX4

49 RX5

50 CTS5#

51 DSR5#

52 CD5#

53 RI5#

54 RTS5#

55 DTR5#

56 TX5

39 RX6

40 CTS6#

41 DSR6#

42 CD6#

43 RI6#

44 RTS6#

45 DTR6#

46 TX6

31 RX7

32 CTS7#

33 DSR7#

34 CD7#

35 RI7#

36 RTS7#

37 DTR7#

38 TX7

100 BUFFER1_RX

98 BUFFER1_CTS

93 BUFFER1_RTS

93 BUFFER1_TX

92 BUFFER2_RX

90 BUFFER2_CTS

87 BUFFER2_RTS

85 BUFFER2_TX

73 BUFFER3_RX

75 BUFFER3_CTS

79 BUFFER3_RTS

80 BUFFER3_TX

65 BUFFER4_RX

67 BUFFER4_CTS

71 BUFFER4_RTS

72 BUFFER4_TX

57 BUFFER5_RX

59 BUFFER5_CTS

63 BUFFER5_RTS

64 BUFFER5_TX

49 BUFFER6_RX

51 BUFFER6_CTS

55 BUFFER6_RTS

56 BUFFER6_TX

39 BUFFER7_RX

41 BUFFER7_CTS

45 BUFFER7_RTS

46 BUFFER7_TX

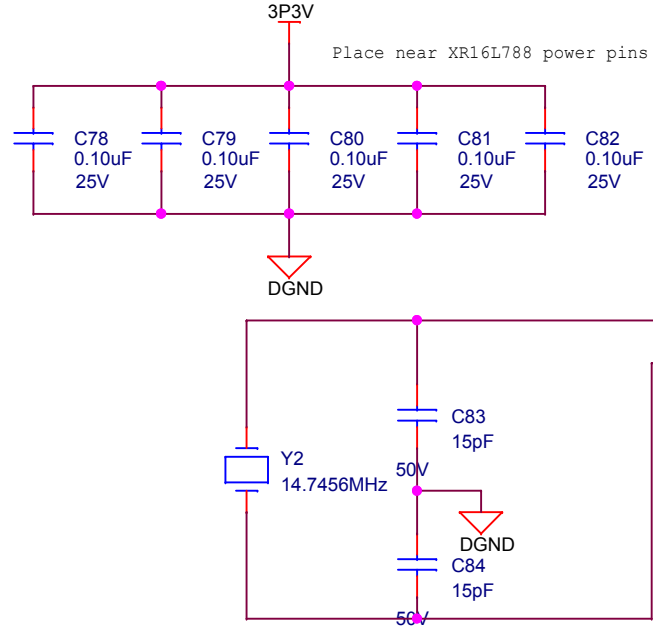
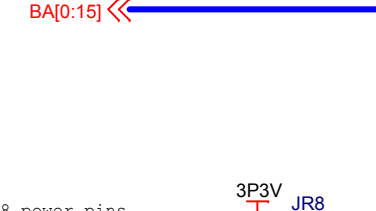
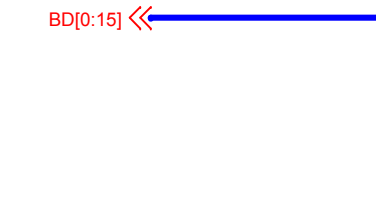
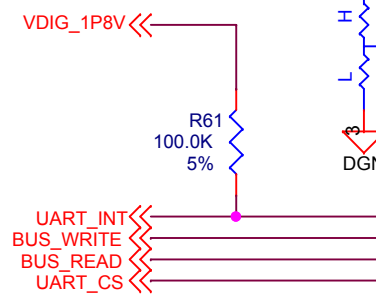
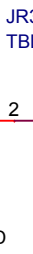
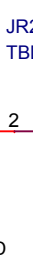
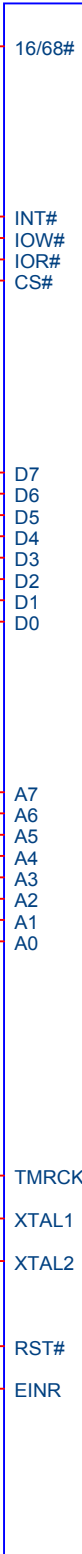
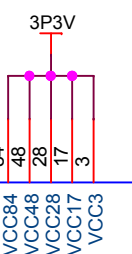
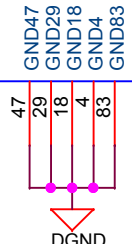
31 BUFFER8_RX

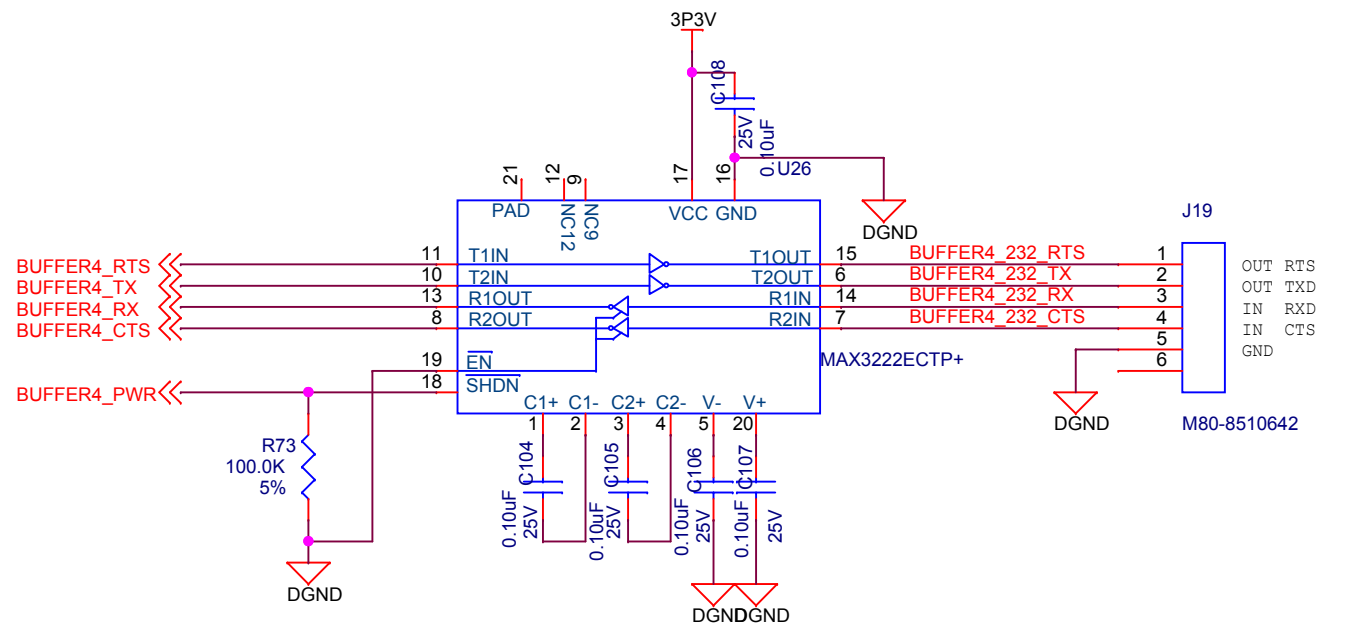
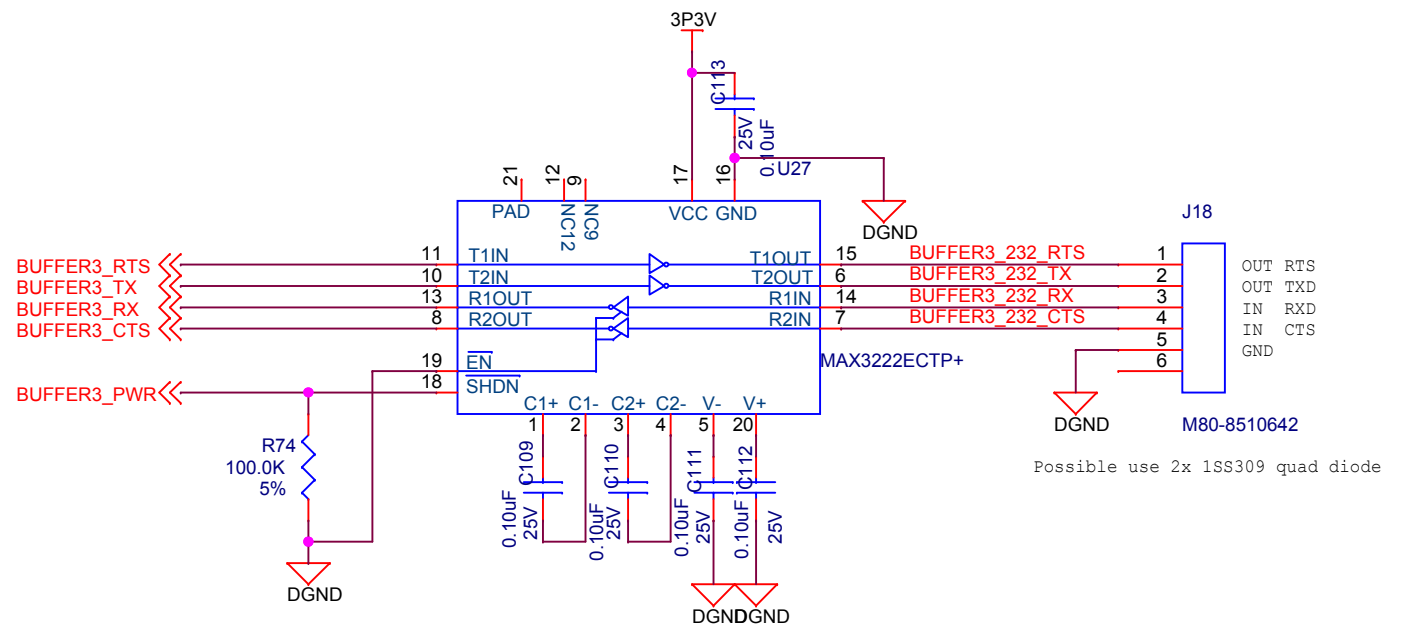
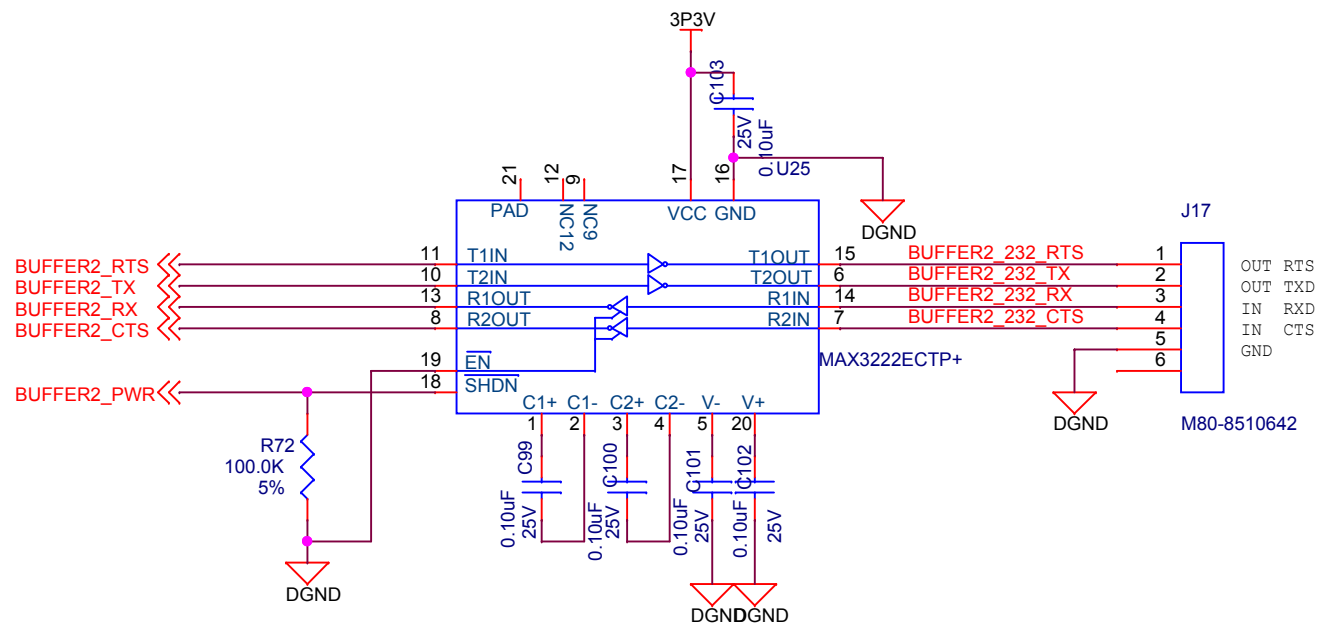
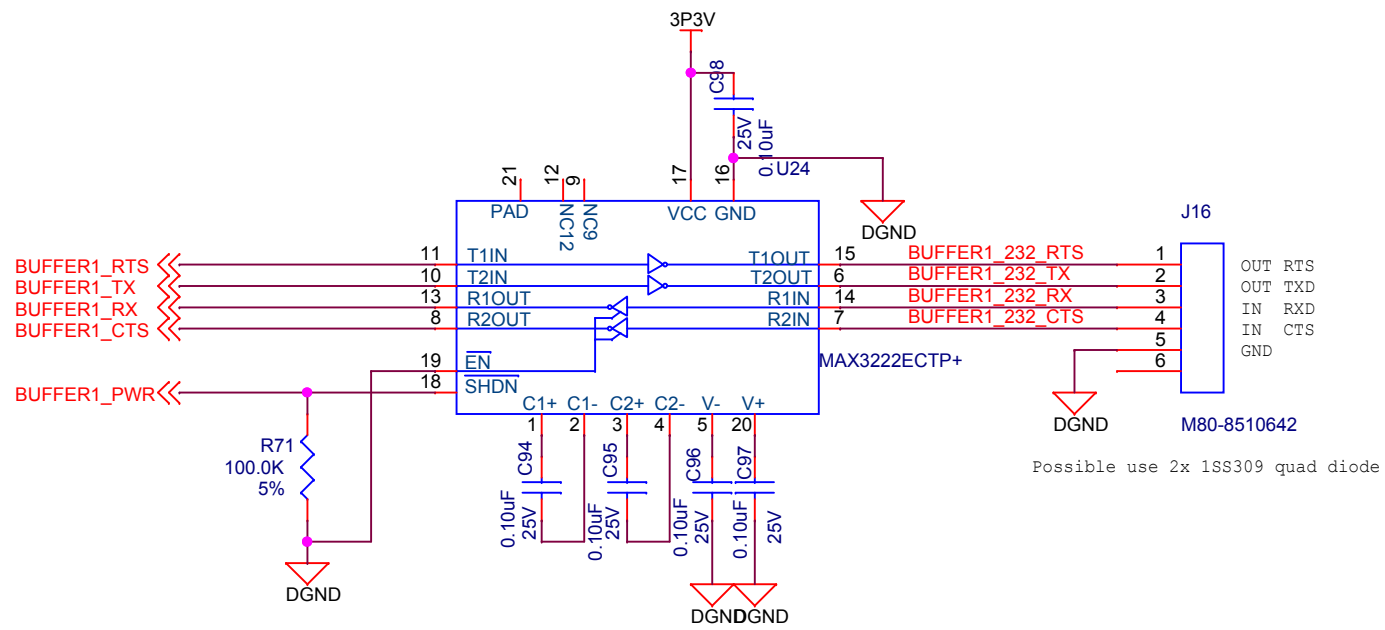
33 BUFFER8_CTS

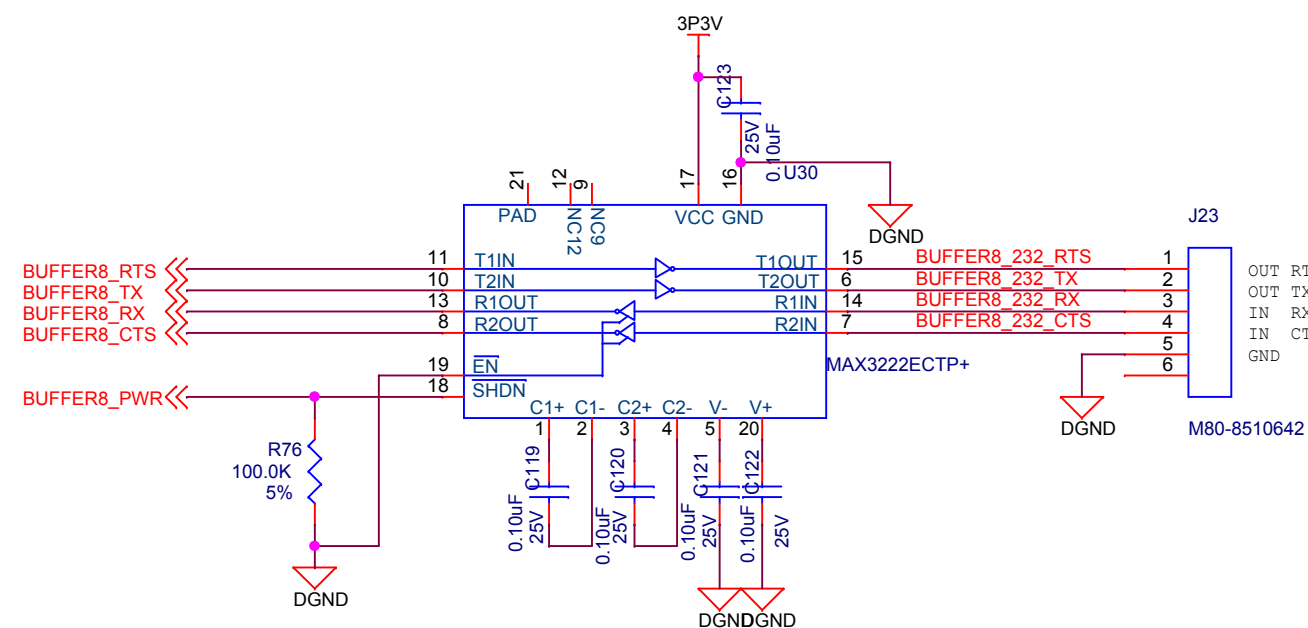
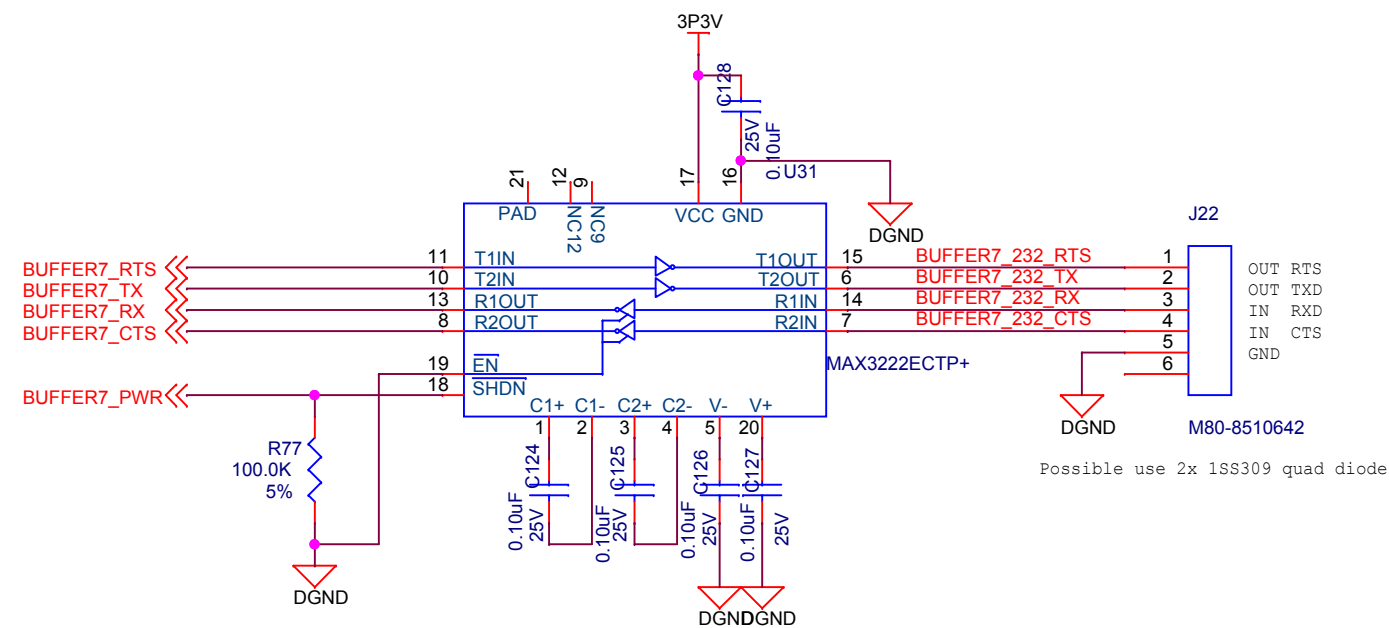
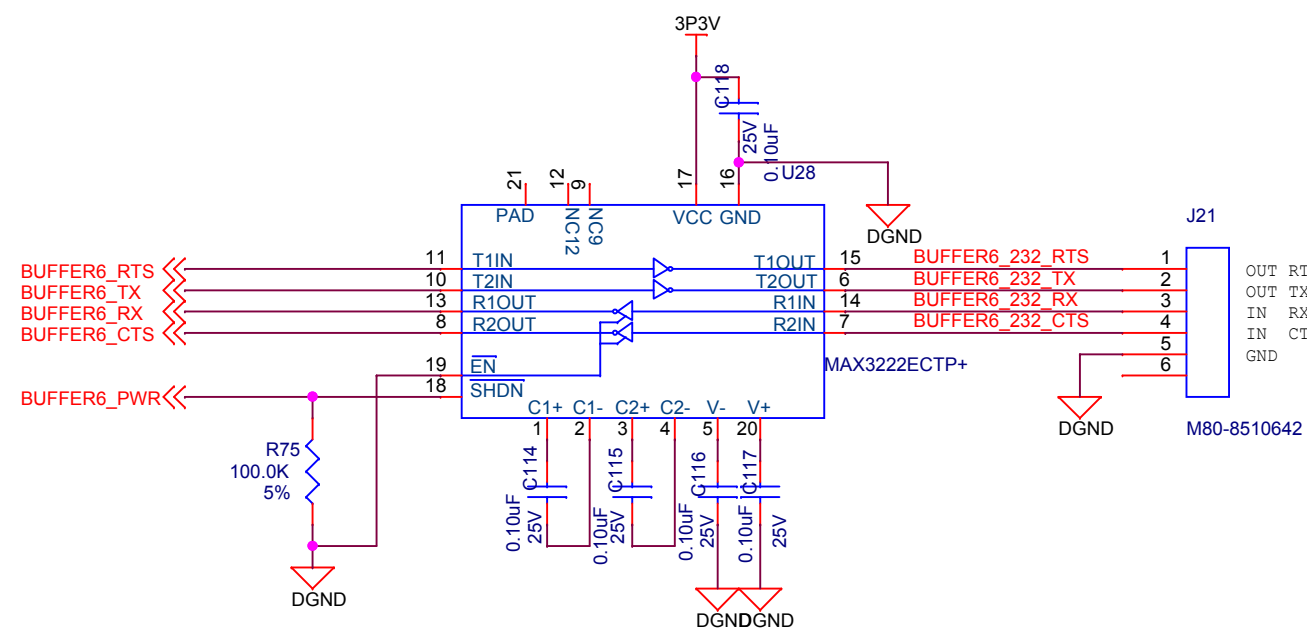
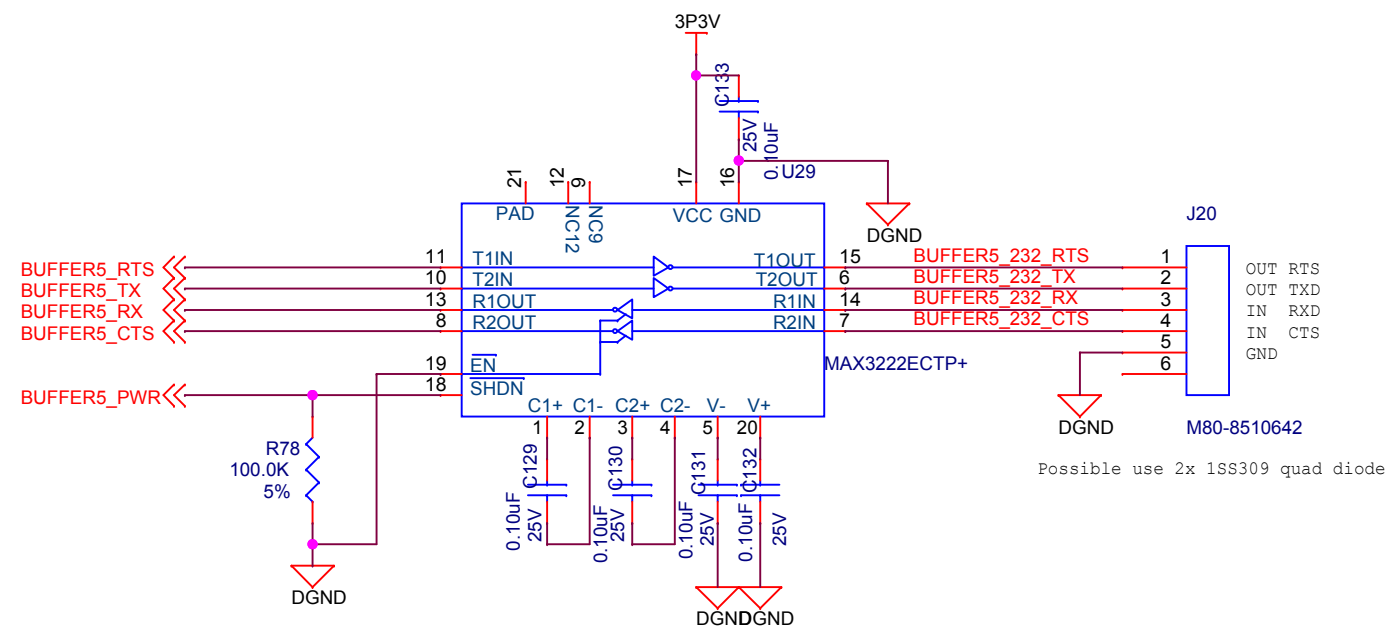
37 BUFFER8_RTS

38 BUFFER8_TX

U23
XR16L788CQ-F







MONTEREY BAY AQUARIUM RESEARCH INSTITUTE
7700 SANDHOLDT ROAD
MOSS LANDING, CA 95039
(831) 775-1700 www.mbari.org
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Title baseMicroProto RS-232 Buffers					Number <Doc>	
Size B	Engineer <Engineer>		Layout <Layout Eng.>		Filename <Filename>	
	Date: Monday, August 17, 2015		Sheet 9 of 9		Rev 0.0	