

11.3. Analog Inputs Port JP4

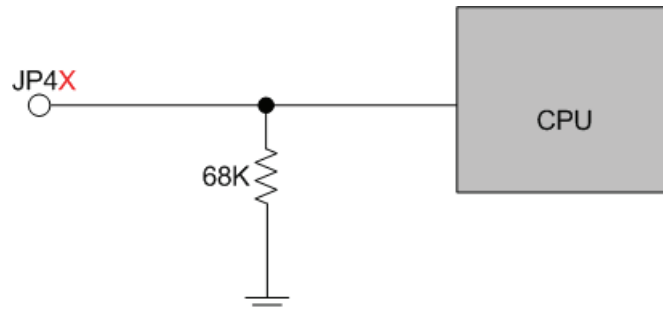


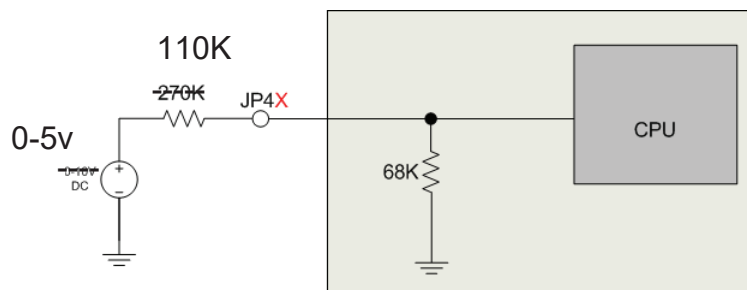
Figure 48. DAEnetIP4 Analog Inputs port JP4

- There is pull-down resistor of 68K connected to GND inside **DAEnetIP4** for every JP4 input pin;
- Maximum input voltage on any JP4 input pin should not be more than **2.048V DC**;

 The Analog Inputs port is not protected and it is connected directly to the CPU! Over-voltage or inverse polarity voltage will damage the CPU.

 It is recommended to use always resistor in sequence to the Analog Input.

Bellow is shown example how to extend the JP4 Analog Inputs port to measure voltage 0-10V DC. The 270K resistor also acts as protection.



DAEnetIP4

Figure 49. How to extend DAEnetIP4 Analog Inputs port JP4 to 0-10V