

J5 J9 J7:
USE PHOENIX
HEADER 1881529
CONNECTOR 1881406

P5
1881406

J9
400V CB trip8
48V CB trip3
48V CB trip5

J7
48V CB trip2
400V CB trip2
400V CB trip5
400V CB trip3

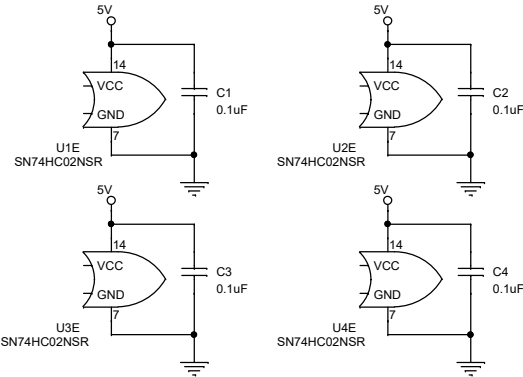
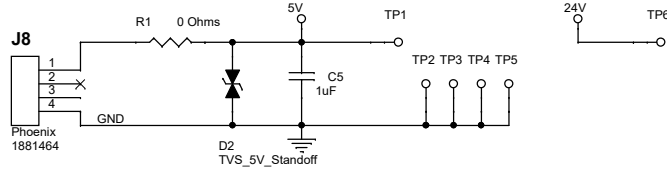
P9
1881406

P7
1881406

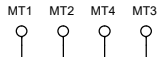
J8:
USE PHOENIX
HEADER 1881464
CONNECTOR 1881341

5 VDC IN
P8
1881341

TEST POINTS



MOUNTING HOLES # 6 WITH WASHER



UNUSED
RN2D
5k
4

A	B	Out
0	0	1
0	1	0
1	0	0
1	1	0

Rev C:
Connector change
Deletion of extra wire ways
Rev D:
?
Rev E:
Added U4 SR latch
Added input protection resistors
Rev F:
Changed drivers from source to sink.
Changed current sense input to 500 mV/A.
Added 8 more output channels.

Scaling is 0.5 V / A

Board dimensions to be the same as Relay Board VEN2016-003. With the same mounting holes, these boards will stack together.

Drawn by: P McGill, C Dawe, D Thompson	
Title	
TTL-24Vdc Level Shift LCB	
Size	Document Number
C	100221371
Date:	Thursday, September 05, 2019
Sheet	1 of 7
Rev	H