

J5 J9 J7:
USE PHOENIX
HEADER 1881529
CONNECTOR 1881406

P5
1881406

J9
GFD 350v (2-N)
GFD 350COM (2-N)
GFD TEST (N)

Rev J 2/2022
CONNECT
J7 Pin 6 to J9 Pin 8
J5 Pin 6 to J9 PIN 1
J5 Pin 7 to J9 Pin 2
J5 Pin 8 to J9 Pin 3

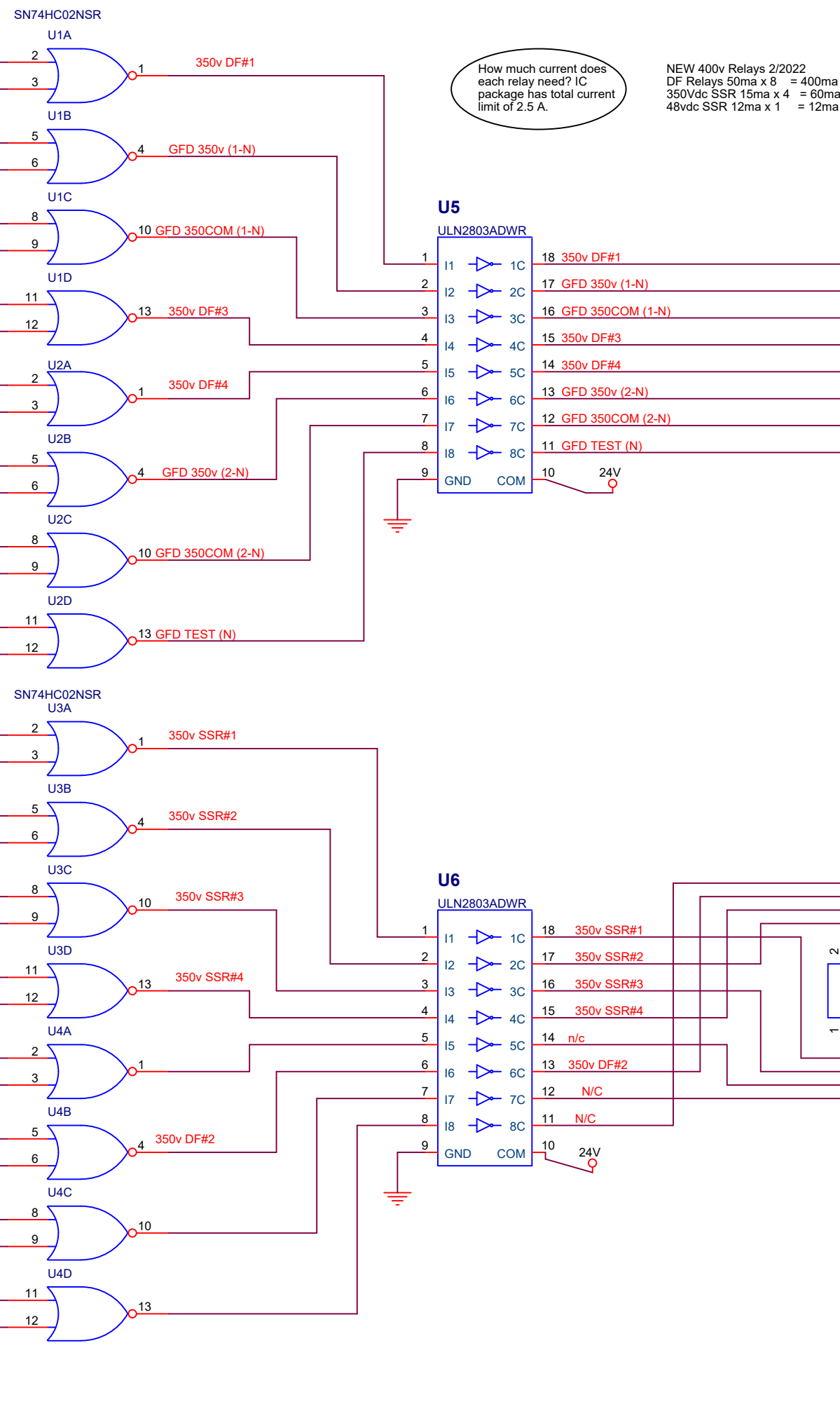
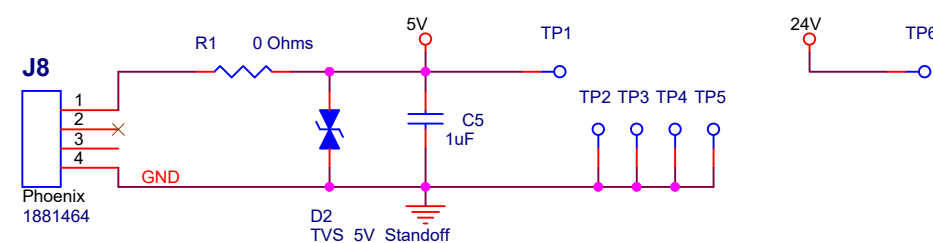
J7
1 350v SSR#1
2 350v SSR#2
3 350v SSR#3
4 350v SSR#4
5 350v DF#2
6 350v DF#2
7 24V
8 24V
9 24V
10 24V

P9
1881406
P7
1881406

J8:
USE PHOENIX
HEADER 1881464
CONNECTOR 1881341

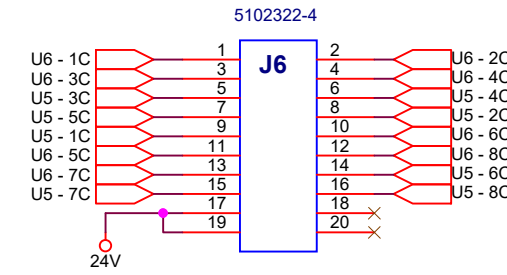
5 VDC IN
P8
1881341

TEST POINTS



How much current does
each relay need? IC
package has total current
limit of 2.5 A.

NEW 400v Relays 2/2022
DF Relays 50ma x 8 = 400ma
350vdc SSR 15ma x 4 = 60ma
48vdc SSR 12ma x 1 = 12ma



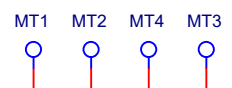
CONNECT
J6 Pin 1 350v SSR#1
J6 Pin 2 350v SSR#2
J6 Pin 3 350v SSR#3
J6 Pin 4 350v SSR#4
J6 Pin 5 GFD 350COM (1-N)
J6 Pin 6 350v DF#3
J6 Pin 7 350v DF#4
J6 Pin 8 GFD 350v (1-N)
J6 Pin 9 350v DF#1
J6 Pin 10 350v DF#2
J6 Pin 11 N/C
J6 Pin 12 N/C
J6 Pin 13 N/C
J6 Pin 14 GFD 350v (2-N)
J6 Pin 15 GFD 350COM (2-N)
J6 Pin 16 GFD TEST (N)
J6 Pin 17 24v
J6 Pin 18 N/C
J6 Pin 19 24v

Pinout of new outputs?

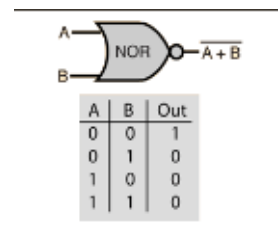
J6
TE CONNECTIVITY
5102322-4
20-pin ribbon cable
to relay card

P6
1658821-4
TE CONNECTIVITY

MOUNTING HOLES # 6 WITH WASHER



UNUSED



Rev C:
Connector change
Deletion of extra wire ways
Rev D:
?
Rev E:
Added U4 SR latch
Added input protection resistors
Rev F:
Changed drivers from source to sink
Changed current sense input to 500 mV/A.
Added 8 more output channels.
Rev J:
CONNECT
J7 Pin 6 to J9 Pin 8
J5 Pin 6 to J9 PIN 1
J5 Pin 7 to J9 Pin 2
J5 Pin 8 to J9 Pin 3
Rev J 2/2022
Board used for Mini-Node2

Scaling is 0.5 V / A

Board dimensions to be the
same as Relay Board
VEN2016-003. With the same
mounting holes

Drawn by: P McGill, C Dawe, D Thompson and D French		
Title		
TTL-24Vdc Level Shift LCB		
Size	Document Number	Rev
C	100221371	J
Date:	Thursday, February 24, 2022	Sheet 1 of 7