

IGLOO (AGL) Modular Board

"Icicle"

Rev 2.0

Revision History

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01	TITLE BLOCK
02	POWER SUPPLIES
03	AGL125 DEVICES - 100VQFP & 132QFN
04	INTERFACE CIRCUITS
05	USB TO UART

Version	Date	Description	Originator
1.0	08/07/2007	Initial Creation	
1.1	08/10/2007	RESET Ckt Part updated as per part availability. P/N: DS1818R-10+T&R	
1.2	08/22/2007	Power Section(U14) Updated for LC JTAG Automatic programming	
1.3	08/28/2007	Current sense circuit and (U14)SHDN pin connections Updated.	
1.4	08/29/2007	VLDO Linear Regulator(U14)connection Updated and a capacitor C65 added in the Current sense circuit.	
2.0	09/18/2007	Net name updated at SW5 and J8.1 was connected to VUSB.	
2.1	09/25/2007	OLED Display Changed to Dual Supply Mode.(LT1615) provide for external 13V supply .Battery P/N Changed(LIR2450)	

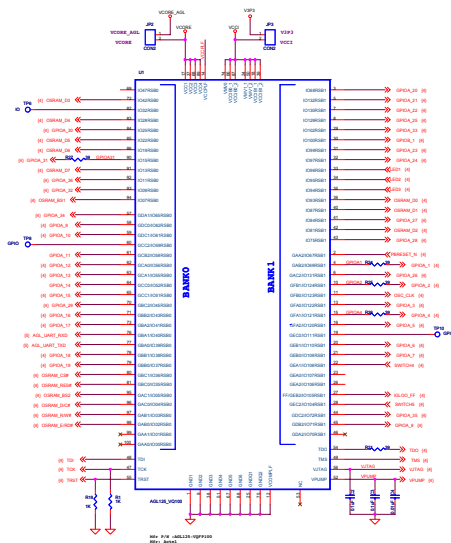


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JUNIOR OPTION	ACTION
1 & 2	Power supply will be from Regulator L50400
3 & 3	supply will be from Solar cell Resistor J90



AGL125 - 100VQFP

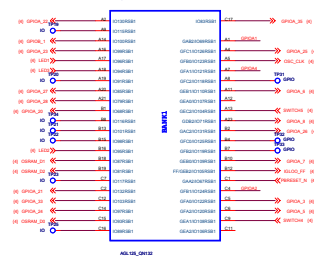


AGL125 - 132 QFN

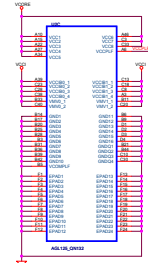
BANK 0 I/O SIGNALS



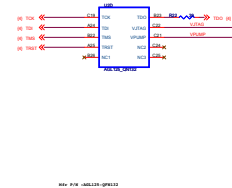
BANK 1 I/O SIGNALS



POWER & GND

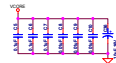


JTAG SIGNALS

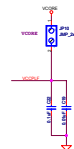
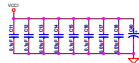


Notes:
All Test points can be SMT Pad on the board

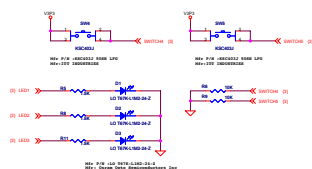
DECAPS FOR VCC0E



DECAPS FOR I/O BANK0 & BANK1

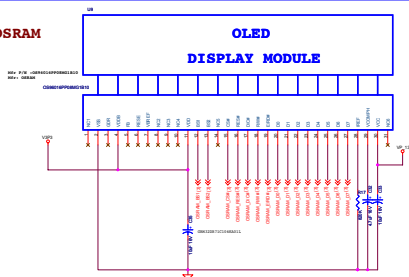


TEST LEADS & TEST SWITCHES

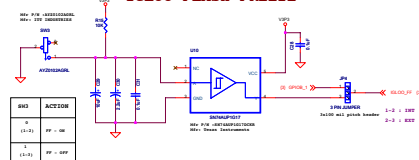


OSRAM

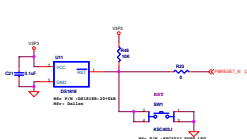
OLED DISPLAY MODULE



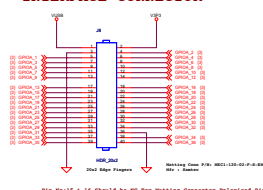
IGLOO FLASH FREEZE



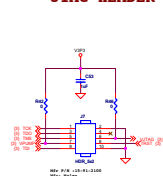
PUSH BUTTON SYSTEM RESET



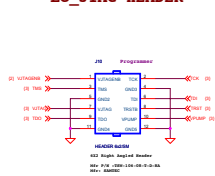
INTERFACE CONNECTOR



JTAG HEADER



LC_JTAG HEADER



CLK OSCILLATOR CIRCUIT

