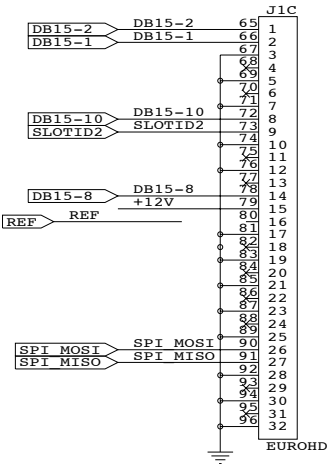
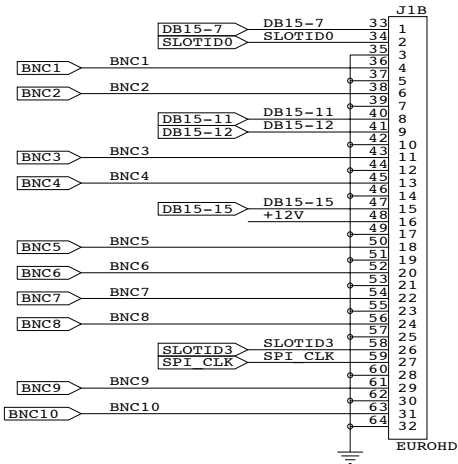
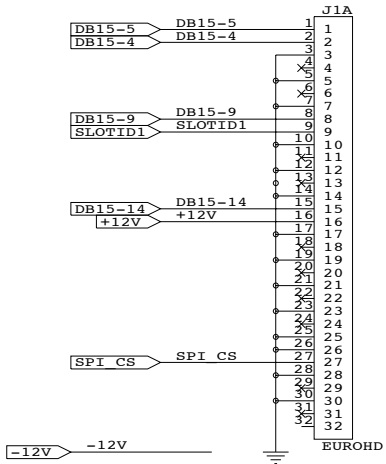


This schematic printed on 12 Feb 07
for A1 Bringup.



PIN1 IS AT THE BOTTOM OF THE MODULE



STANDARD SCHEMATIC CIRCUIT
DO NOT EDIT

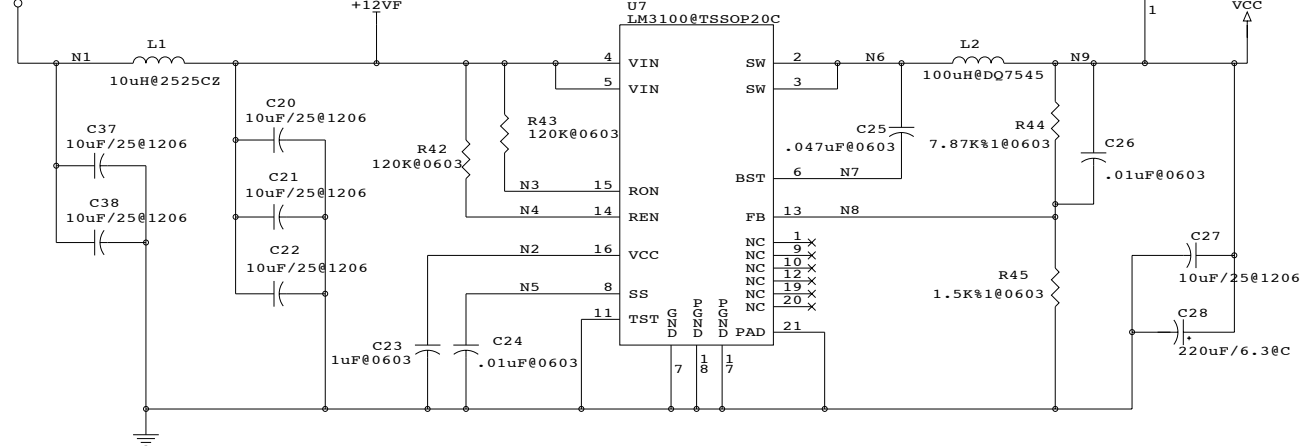
MOTHERBOARD INTERFACE

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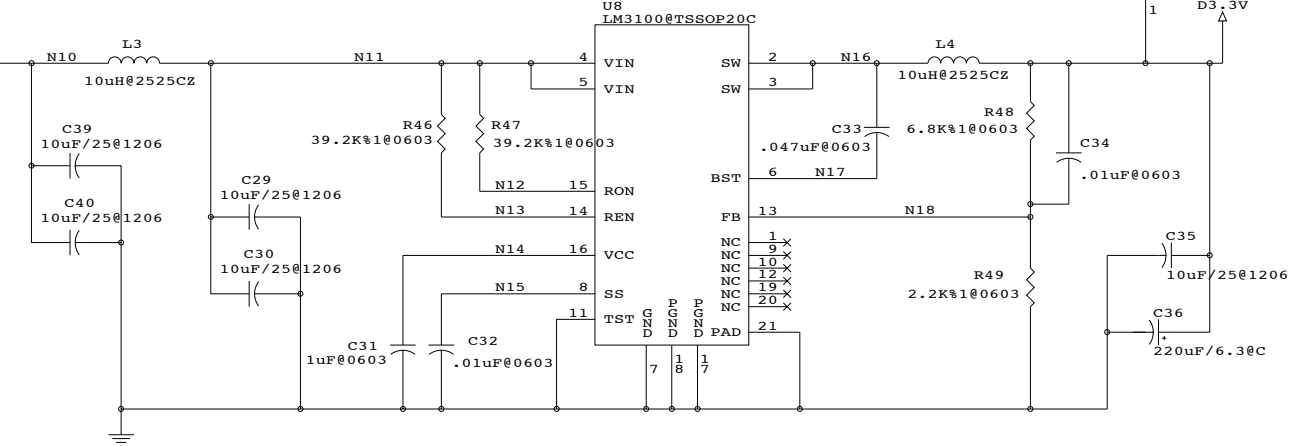
PTC DEVICE
TRIPS @1.5A

+12V +12V

5v VCC Main

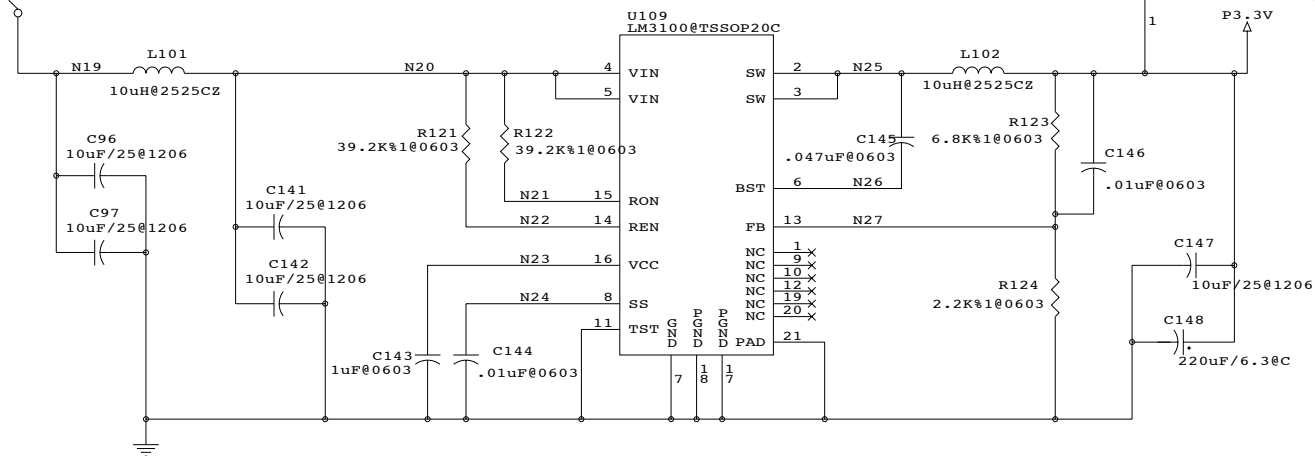


Driver 3.3v

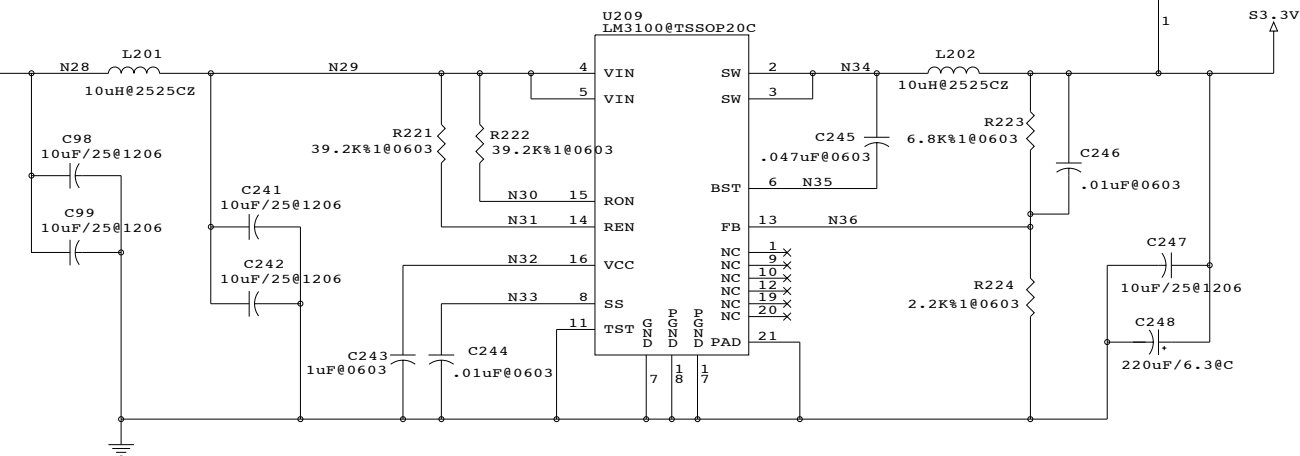


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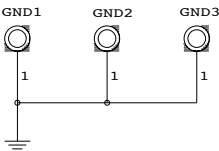
Primary 3.3v



Secondary 3.3v

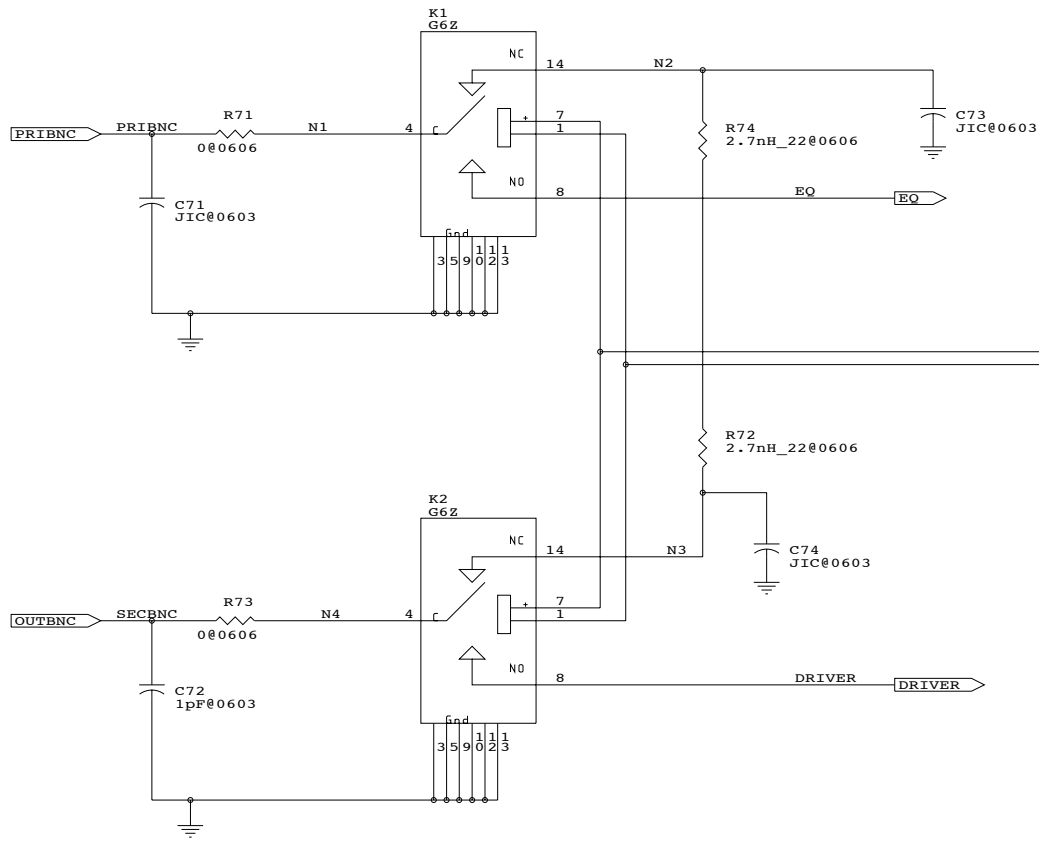


REGULATORS



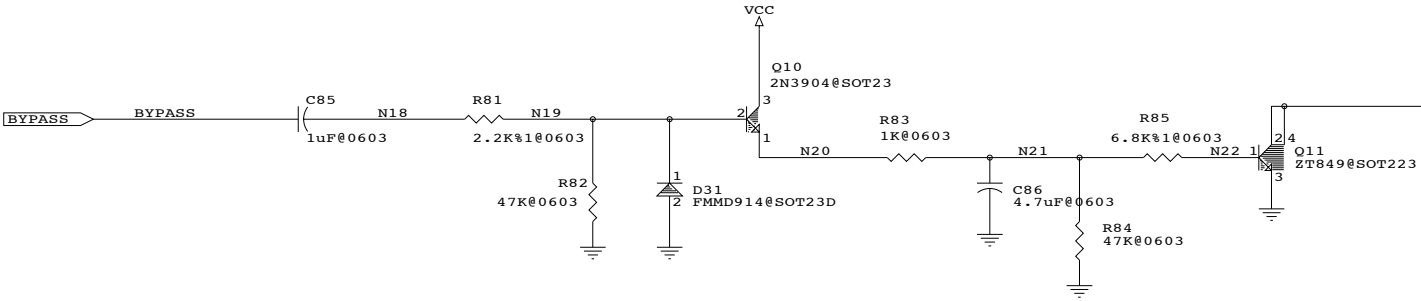
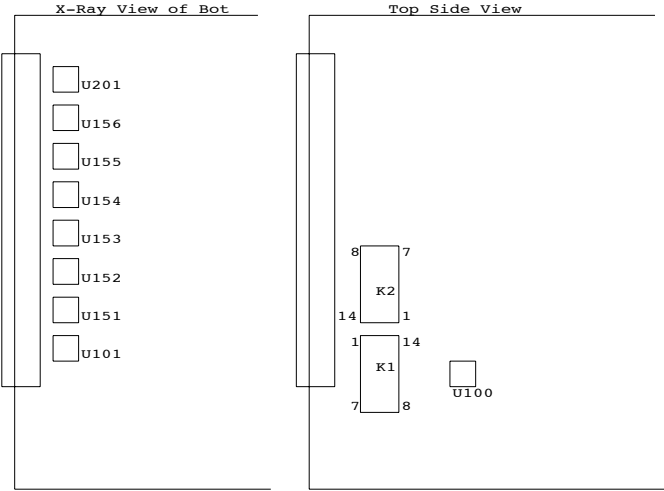
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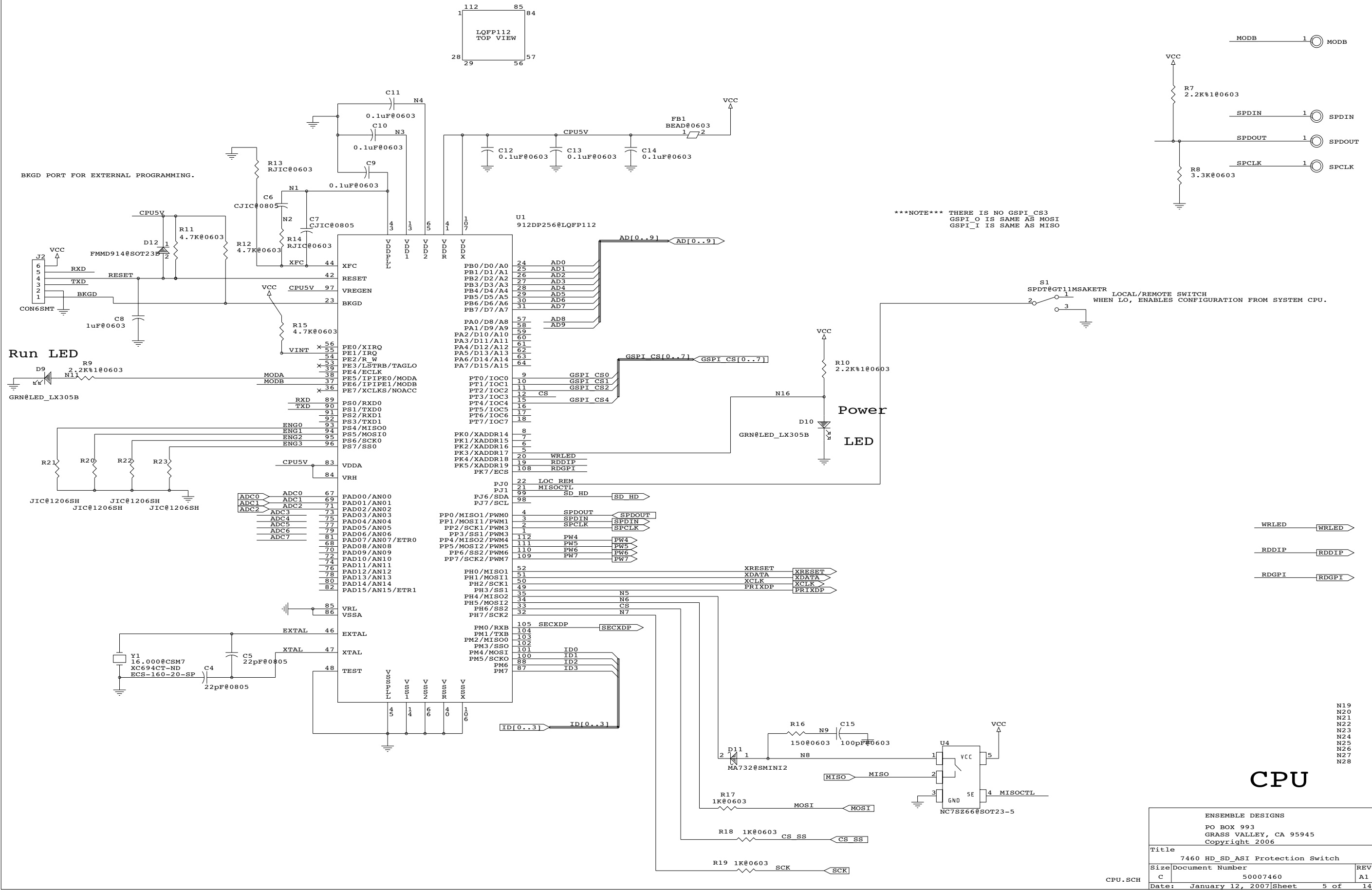
NOTE: Just in case components are for use in achieving desired return loss and transmission characteristics.

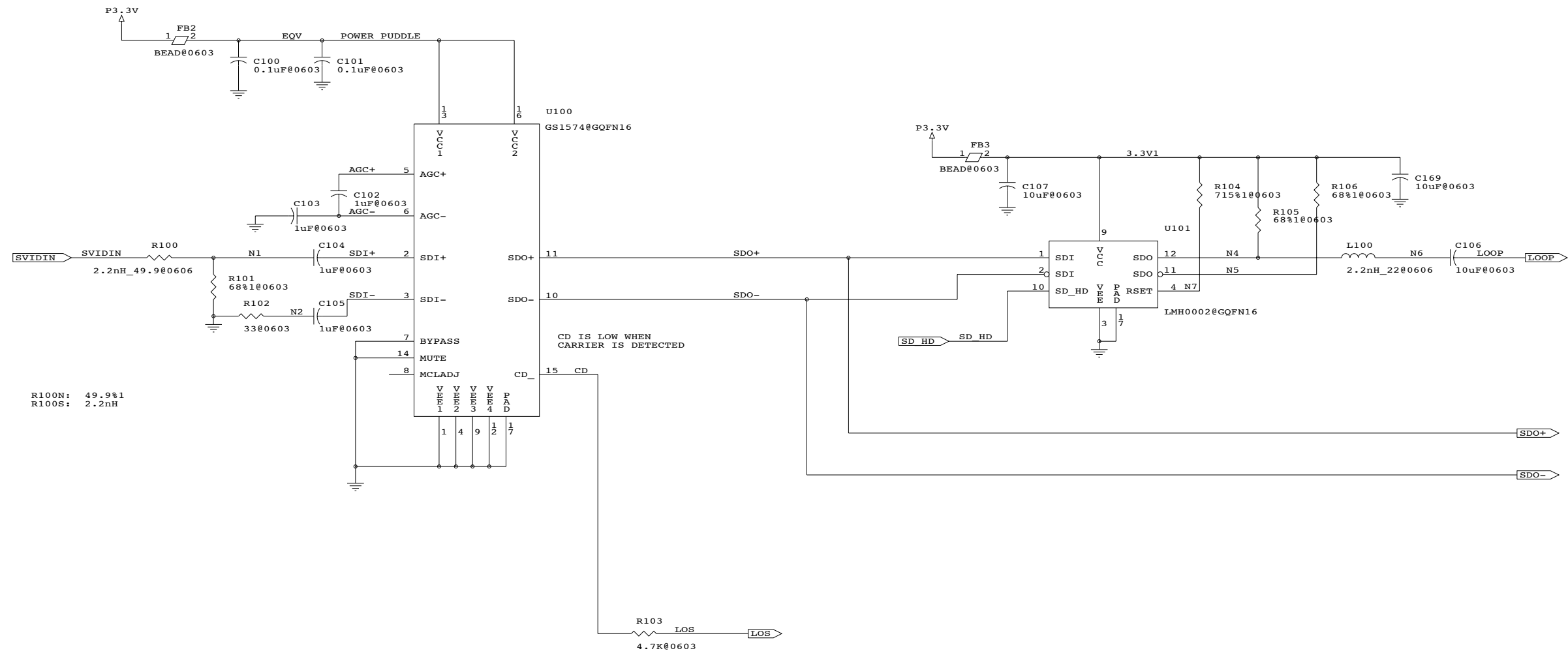


Bypass

BYPASS.SCH

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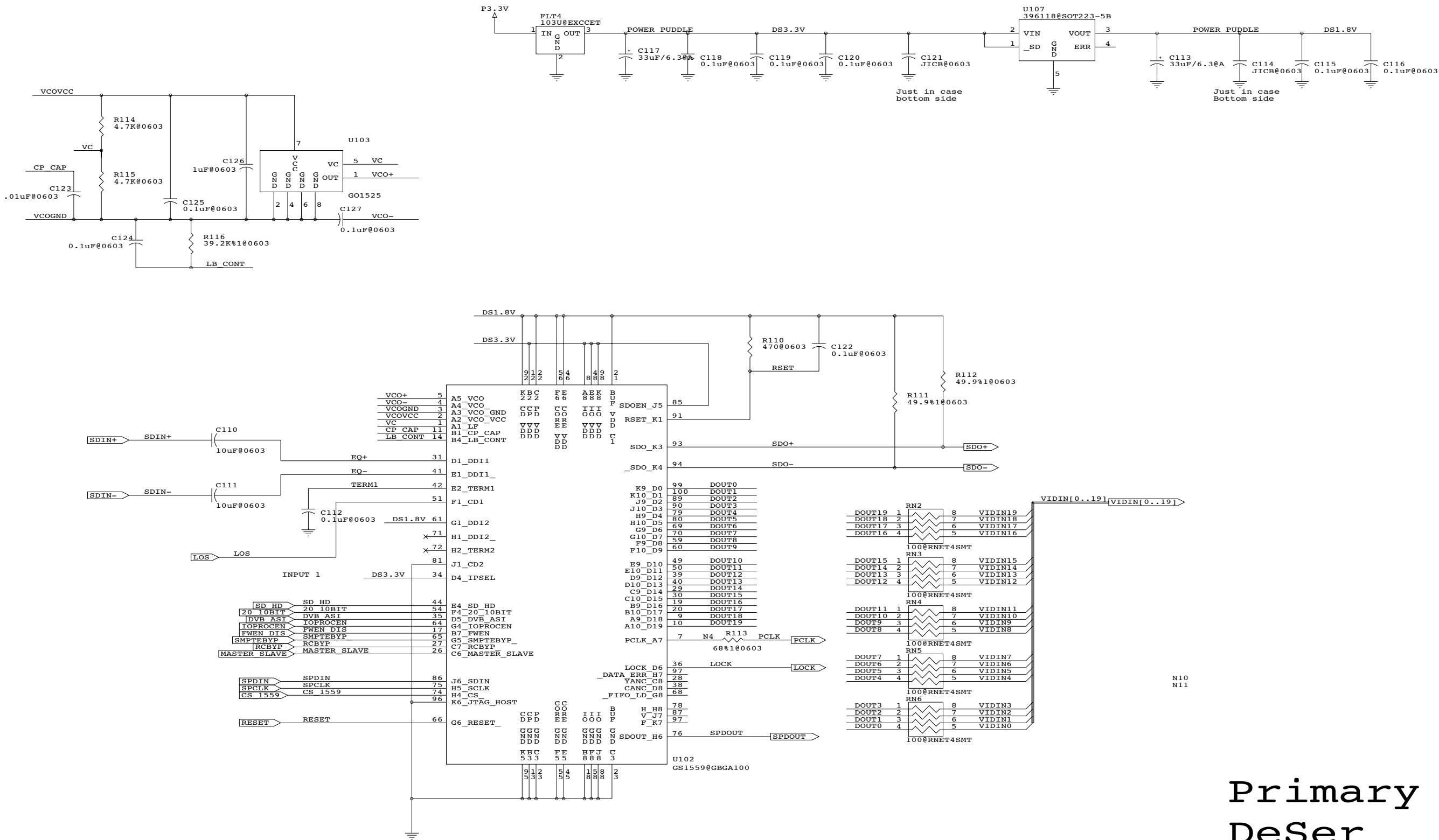




Primary EQ

PRIEQ.SCH

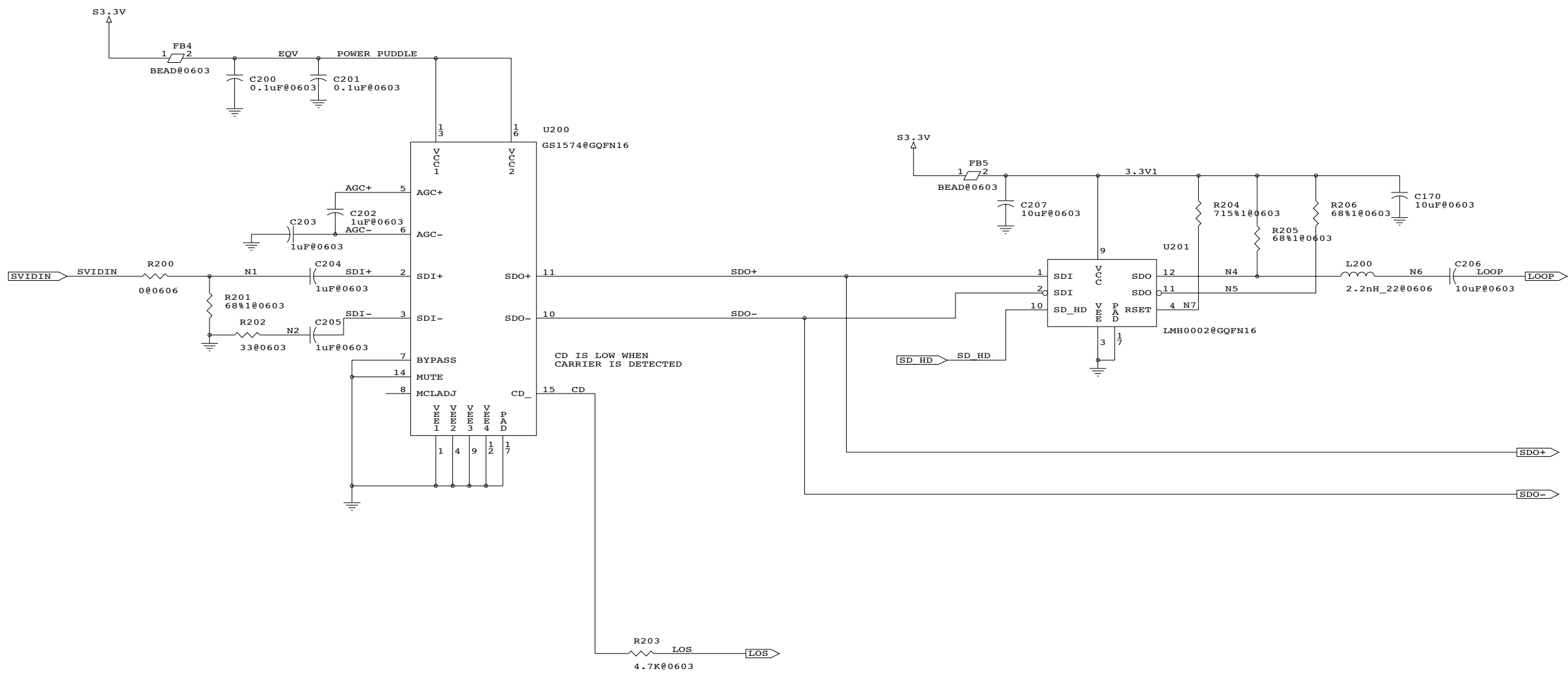
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Primary DeSer

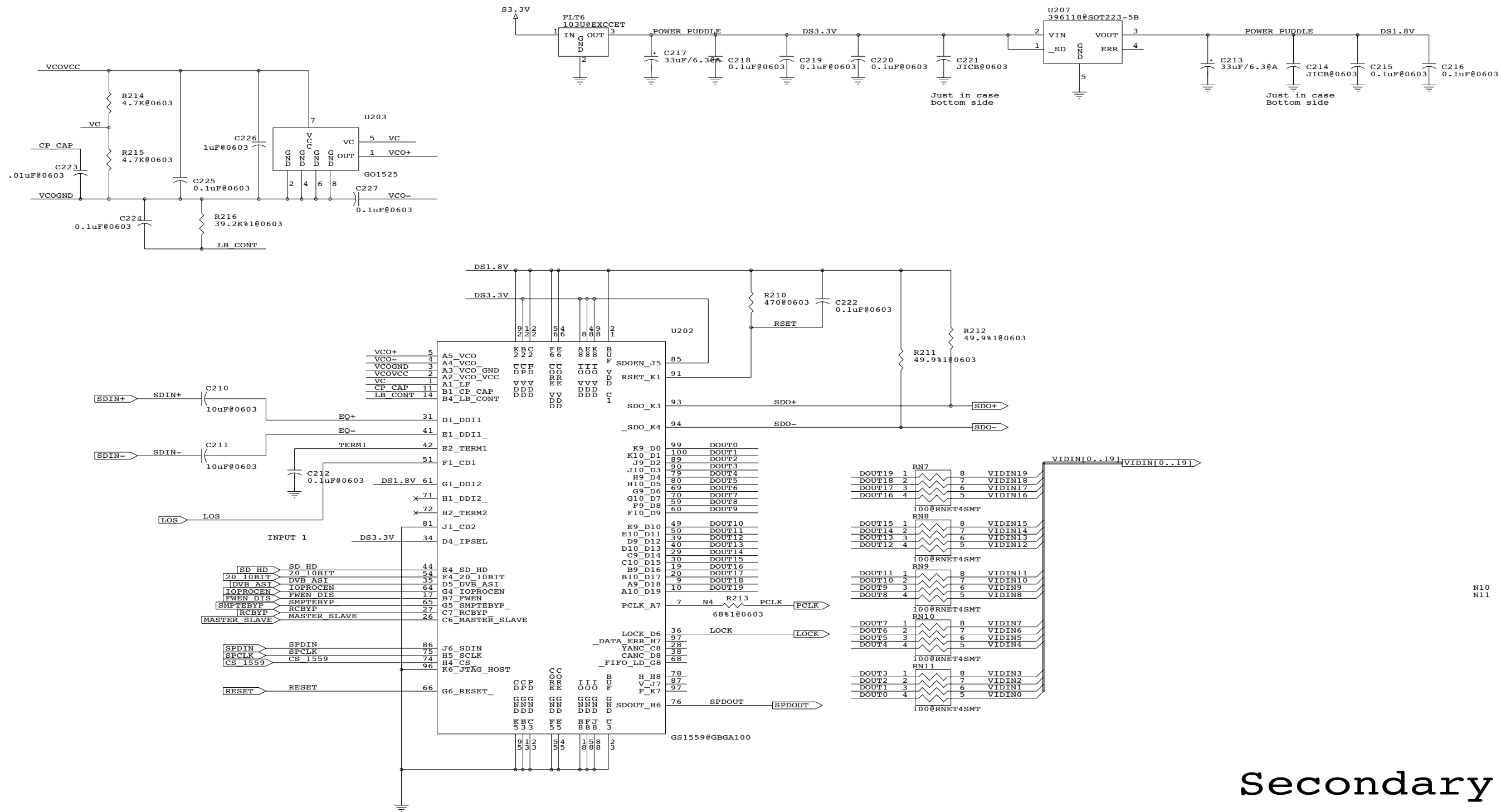
ENSEMBLE DESIGNS PO BOX 993 GRASS VALLEY, CA 95945 COPYRIGHT 2006		
Title 7460 HD_SD_ASI Protection Switch		
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PRIRCVR.SCH

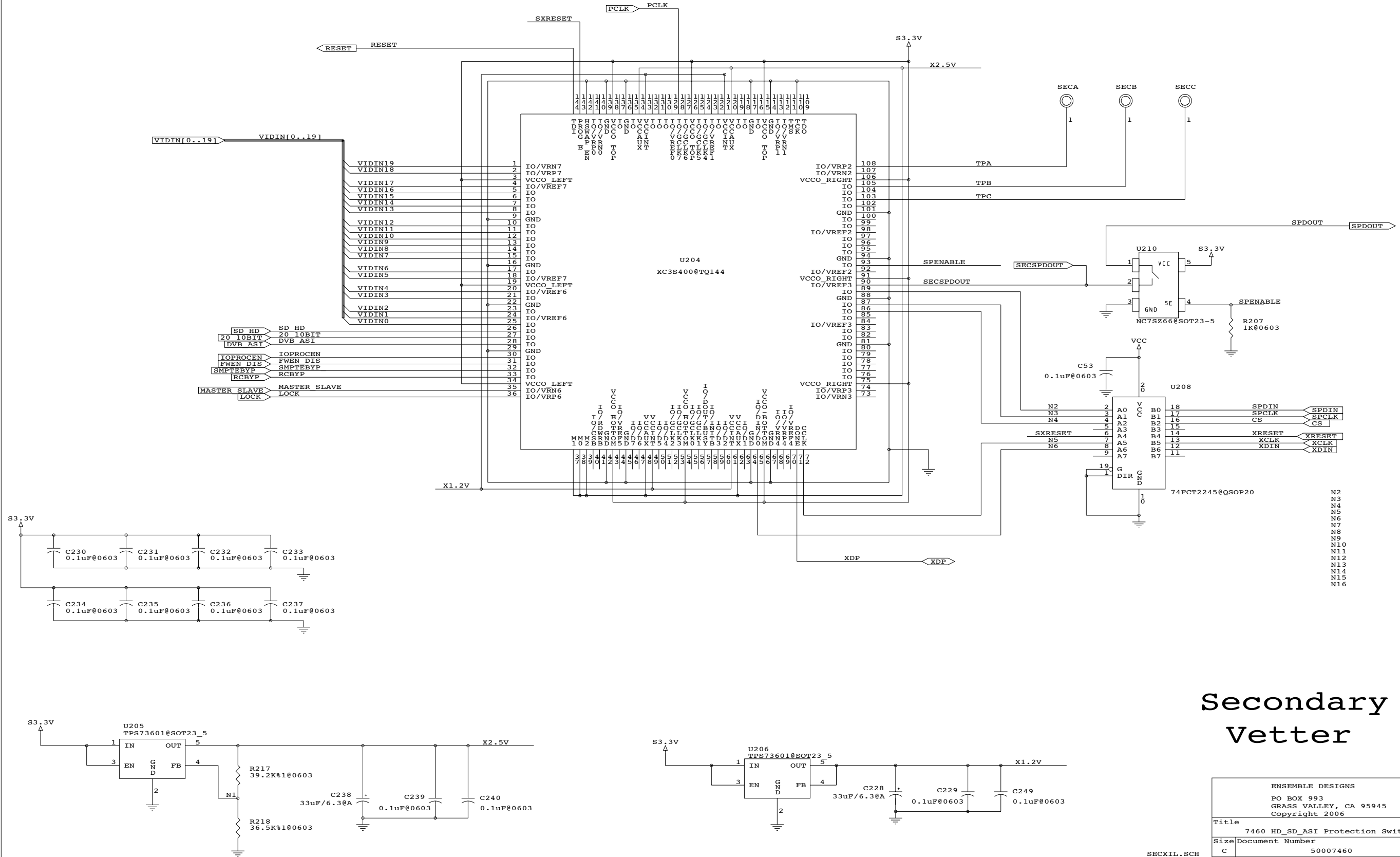


Secondary EQ

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Secondary DeSer

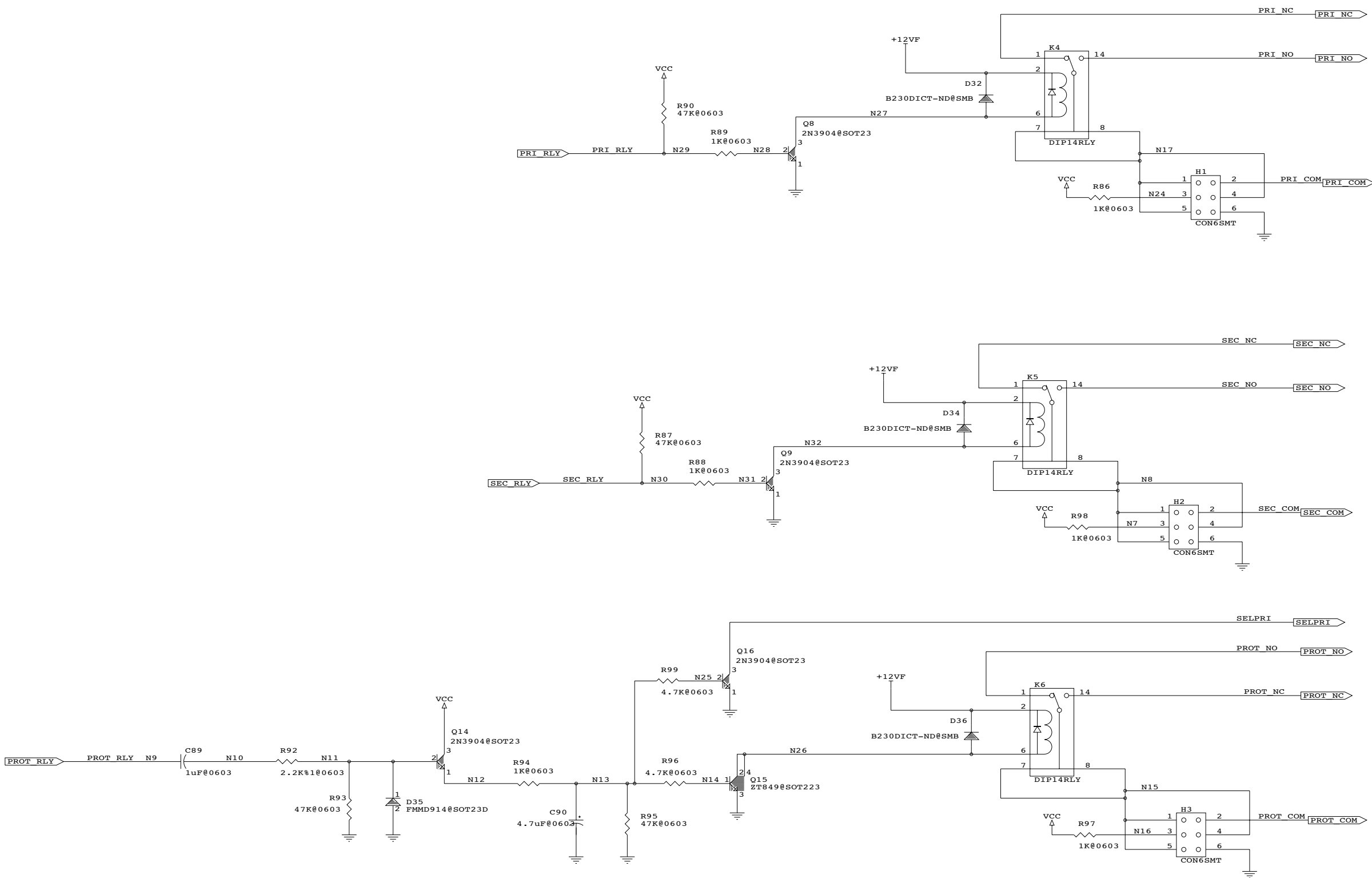


Secondary Vetter

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SECXIL.SCH



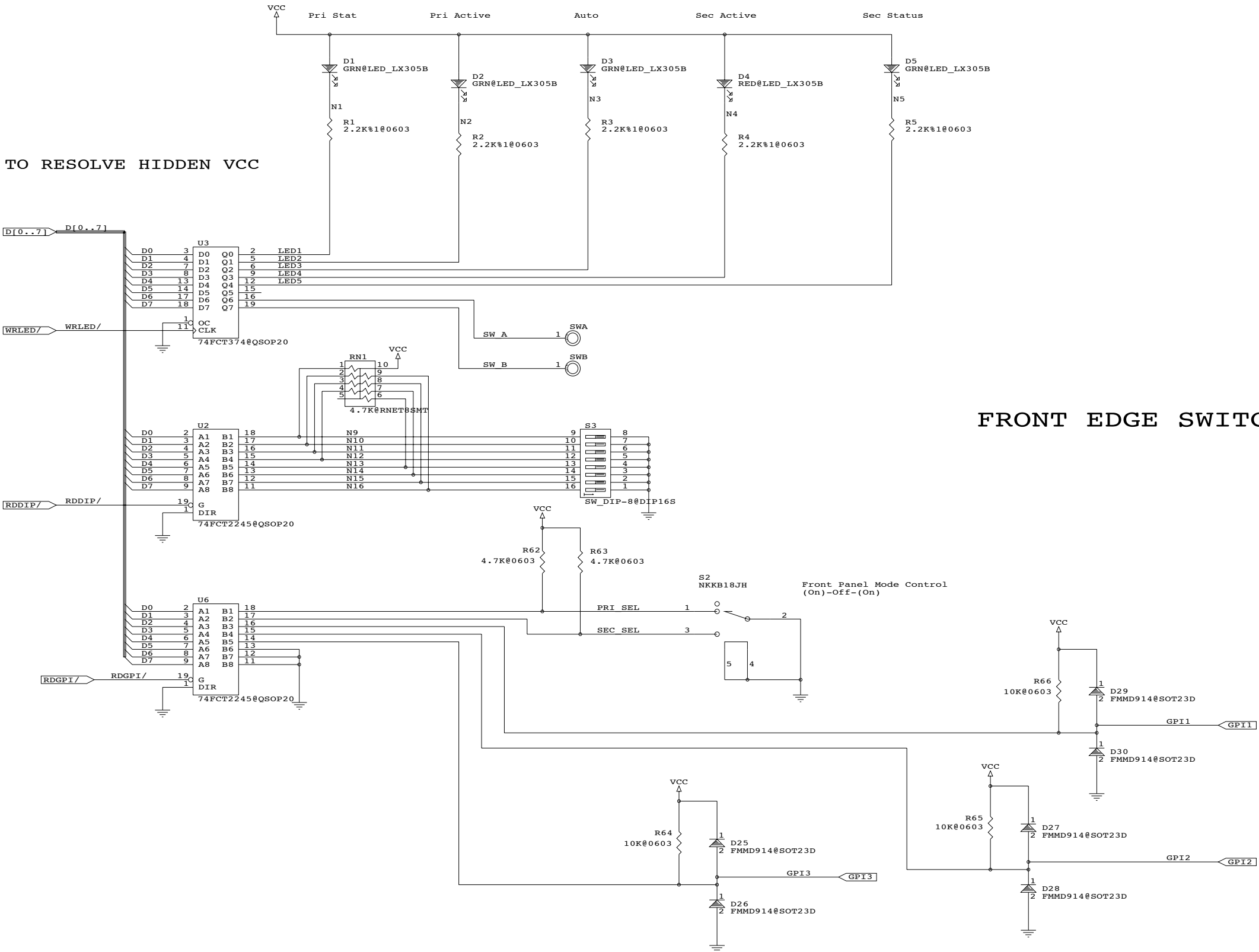


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Status Output Relays

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NEED TO RESOLVE HIDDEN VCC



FRONT EDGE SWITCHES AND INDICATORS

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N20

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