



Sierra Circuits, Inc.

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Quotation

Quote Number: Q113697-6

Quote Date: Aug 26th, 2014

Quote Valid Till: Sep 26th, 2014

Customer MBARI Payment Terms: Net Credit Approved	Address: 7700 Sandholdt Rd Moss Landing California 95039 United States	Buyer: Jose Rosal Phone: 8317751796 Email: jrosal@mbari.org Fax:	Account Manager : Jeff Strozza Phone: 1-408-735-7137x9838 Email: jeffs@protoexpress.com Fax: 1-408-735-1408
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PART NUMBER: 10020568 REV: 1

Quote Line Item	Quantity	Turn Time	Price	NRE	Testing	Other	Line Total
Q113697-6B	12	5 Days	\$106.25	\$0.00	\$0.00	\$0.00	\$1275.00
PCB	Board(s)		Piece	(Tooling waived)	(Testing will be done, charges waived)		
Fabrication			Price				
Notes:							

Layers	: 4	Indv Brd Dimensions	: 5 in x 2 in	Array	: [4 Boards] 4.9 in x 10.9 in
PCB Fabrication Spec (IPC Spec)	: IPC-600G Class 2	Arrays need to be created by Sierra	: No	Min Trace (Inner layer)	: 17 mils
Material	: Nan Ya NP-175	Individual Route	: Yes	Min Space (Inner layer)	: 10 mils
Finished Thickness	: 0.062 inches	Route and Retain	: No	Solder Mask Sides	: Top and Bottom
Thickness Tolerance	: Plus or Minus 8%	Scoring	: No	Solder Mask Color	: Green
Surface Finish	: HASL	UL Required	: No	Solder Mask Type	: LPI
Gold Fingers	: No	ROHS compliant	: No	Solder Mask Finish	: Standard (Semi-Gloss)
Outer Layer Finish Copper	: 2 Oz	Electrical Test	: Yes	Solder Mask Via Opens	: Covered on both sides
Inner Copper	: 2 oz Inners	Controlled Impedance	: None	SM Plugged Vias	: No
Gerbers provided?	: Yes	Controlled Dielectric	: No	Silk Screen Sides	: Both
Layer order provided?	: Yes	# of Holes Per Board	: 361	Silk Screen Color	: White
Board outline provided?	: Yes	Minimum Hole Size	: 10 mils	Logo Allowed	: In copper or silk screen
Drill file provided?	: Yes	Internal Slots	: None	Photo plots	: No
Fab drawing provided?	: Yes	Internal Cutouts	: None	Solderpaste Files for Customer	: No
IPC Netlist Provided?	: Yes	Min Trace (Outer layer)	: 8 mils	Cross Section certificate	: No
Assembly BOM Available ?	: No	Min Space (Outer layer)	: 5 mils	Micro Section Coupon	: No

Not sure due to missing information ## Not sure due to discrepancy

Generic Terms & Conditions:

1. All orders are FOB Sunnyvale, California, USA unless otherwise specified within the Quote. For deliveries outside USA, all Duties and Customs are the responsibility of the customer, unless otherwise agreed upon in writing.
2. Prices are quoted in US Dollars; Taxes and Shipping Charges are extra as applicable.
3. This quote is only valid for 30 days unless otherwise agreed upon in writing by our representatives.
4. Payment terms are mentioned at the top of this Quote.
5. Prices shown on this document are based on information provided by customer's Gerber files and associated instructional documents at date of quotation/order. To facilitate quick-turns, customers sometime request us to release orders with a pending PO. Any changes requested to PCB manufacture/assembly processes after placing order, or any new details that emerge in a pending PO, may result in additional charges.
6. The Turn Time shown is calculated using business days. Weekends and business holidays are excluded in any turn time calculations. Additional turn days may be added to orders that are placed "On Hold," waiting for customer response for resolution of missing information or discrepancies.

PCB Fabrication Terms & Conditions:

1. Sale subject to limited warranty. Warranty extends only to bare PCBs and does not apply after components are added by you or others. Our liability shall not exceed price paid by you for the ordered PCBs. No change or addition to these terms, or any additional or different terms on your purchase order, shall be binding on us unless executed in writing by one of our officers.
2. If Sierra Circuits is assembling the boards then we will also warranty the Assembly Labor workmanship.
3. If IPC Netlist is not provided in 356 format, Sierra Circuits will create a net-list from the incoming customer Gerber files to generate an electrical test program. In such cases, Sierra cannot guarantee, nor can it be liable for the electrical performance of the boards, since the test program itself is derived from the files submitted.
4. If you are ordering our "Web PCBs" product, the selection you made online, and the product you chose online, takes priority over your fab drawing or readme notes. Example: Say you chose material FR4 online and your fab drawing calls for Rogers material, we will simply process your order using FR4 material. The price quoted online is based on the specifications and product you chose online.
5. Manufacturing Optimizations: As with most other PCB manufacturers, we may make simple optimizations to your design to ensure boards are manufactured correctly. Such changes do not affect the electrical functionality of your design. These may include:
 - A) Teardrops may be added to pad/trace interfaces to ensure better connectivity.

- B) Slivers less than .005" may be filled in to prevent resist slivers on the panels.
- C) Soldermask dams that are less than .004" wide between SMD pads which have a .003" minimum clearance may be gang relieved.
- D) Silkscreen may be clipped to ensure it does not get on top of pads/copper features.
- E) Non-functional pads may be removed from inner layers.

Please contact me if you have any questions about this quote.

Best Regards,
Jeff Strozza
Account Manager
Sierra Circuits, Inc.

Email: jeffs@protoexpress.com
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www.ProtoExpress.com

End of Quote Q113697-6 from Sierra Circuits, Inc.

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