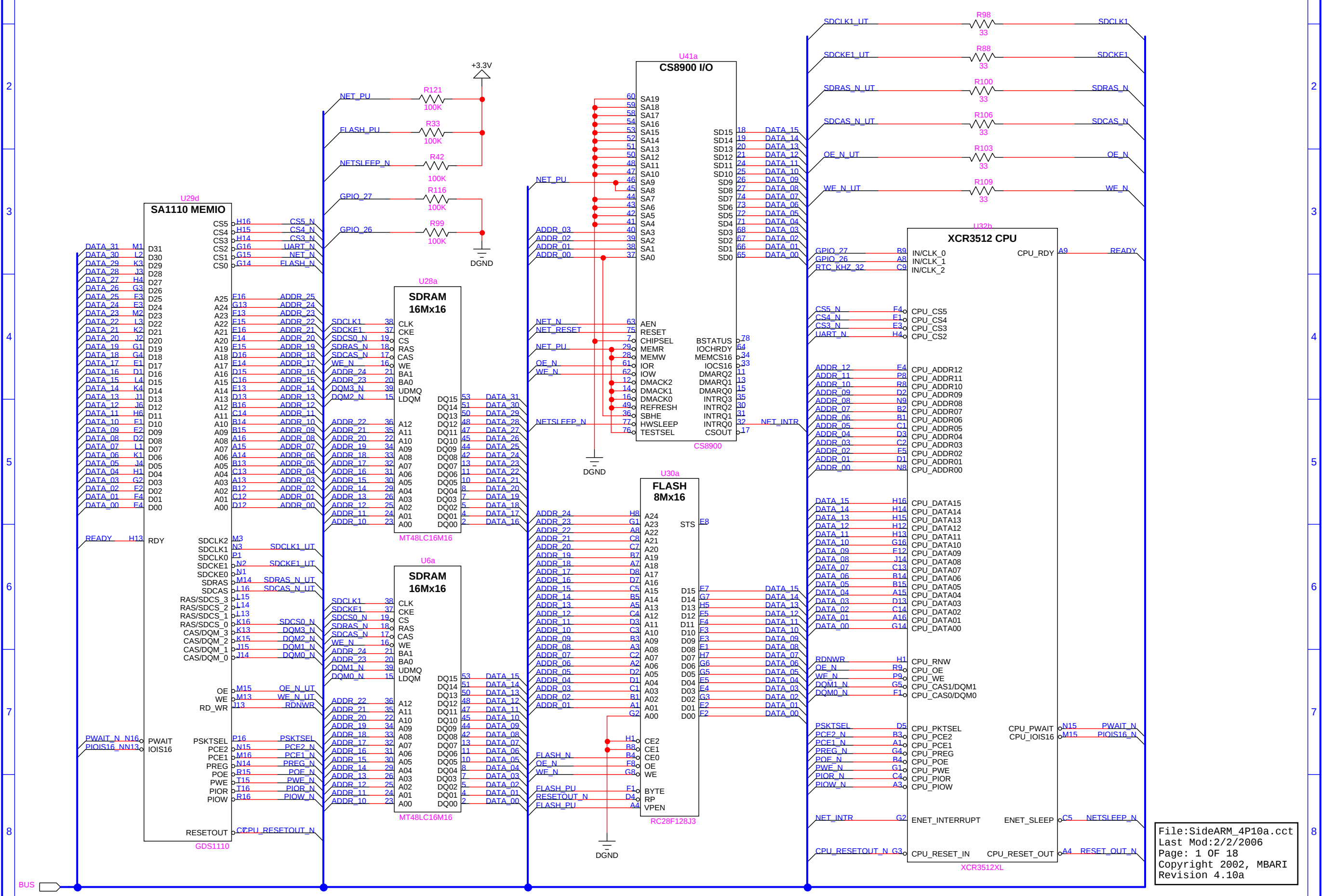
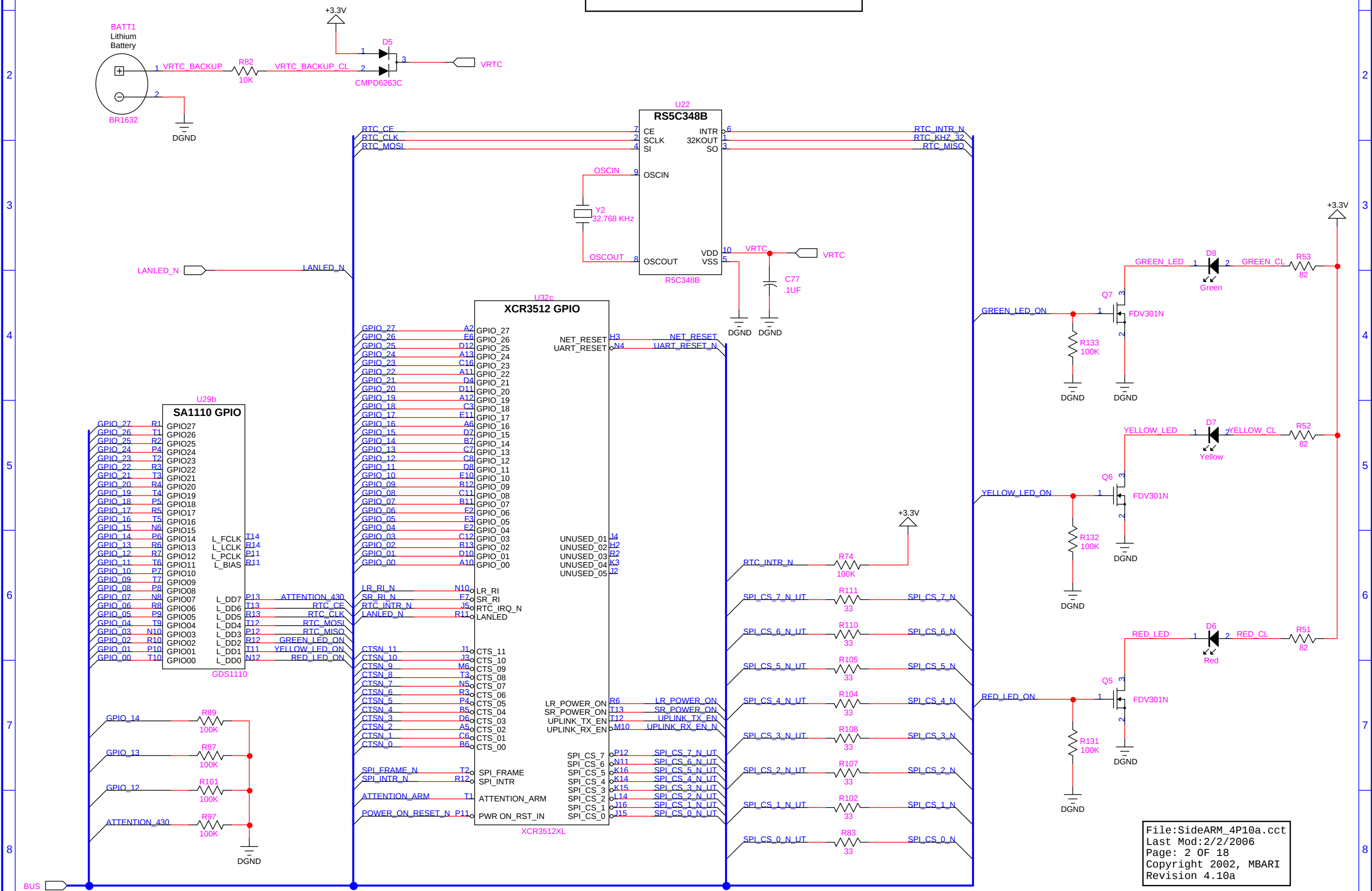


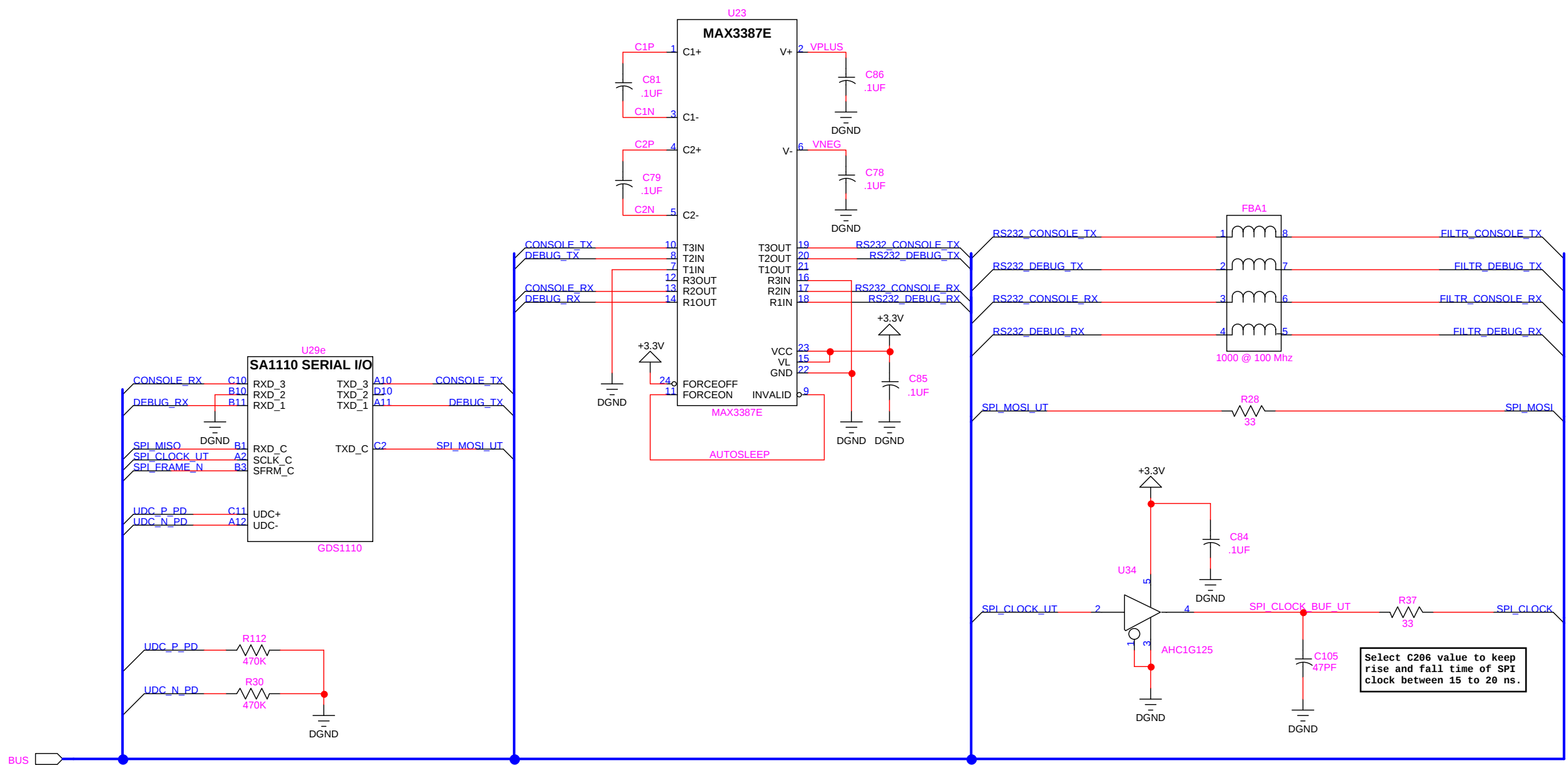
# CPU & STORAGE



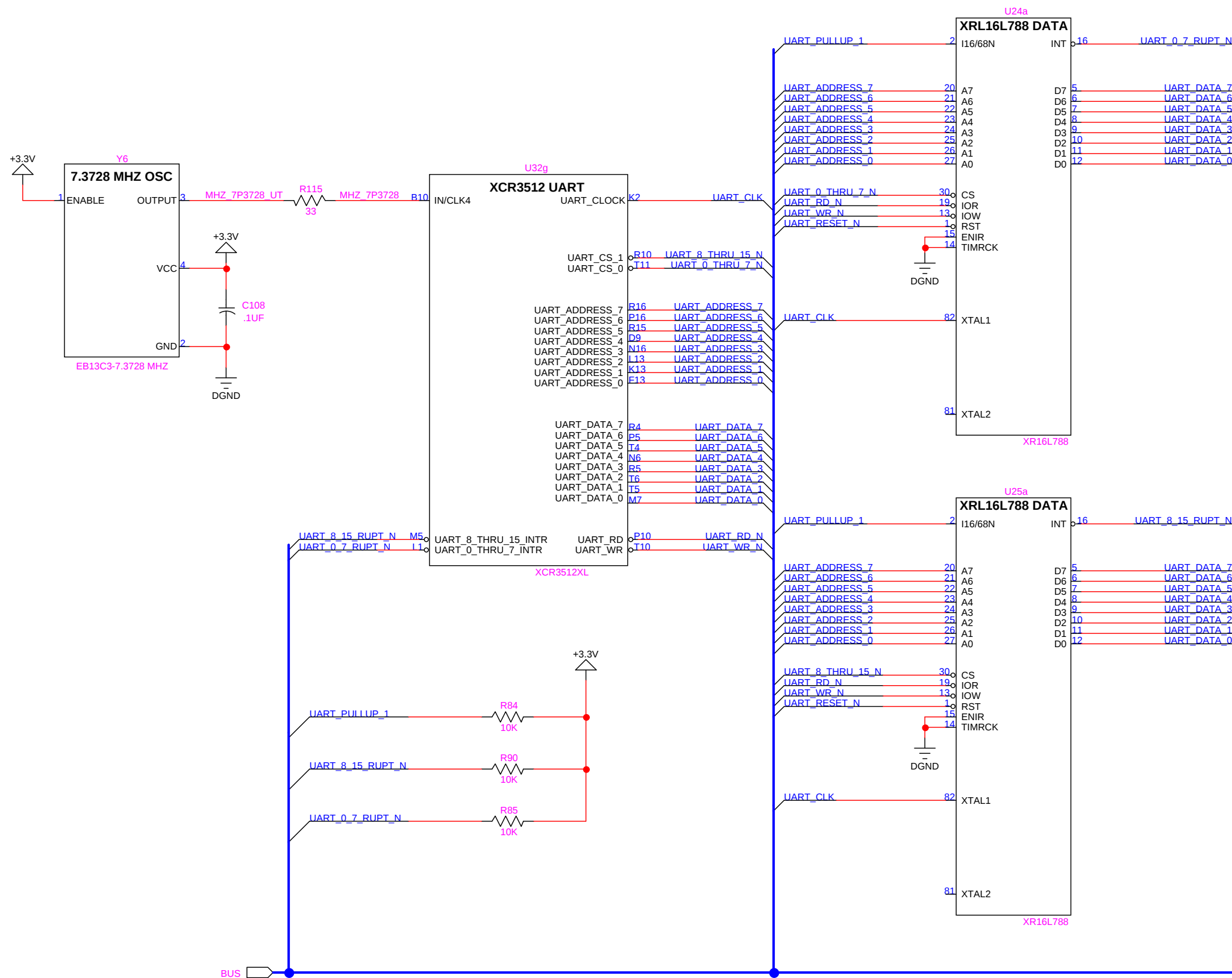
# STRONGARM GPIO



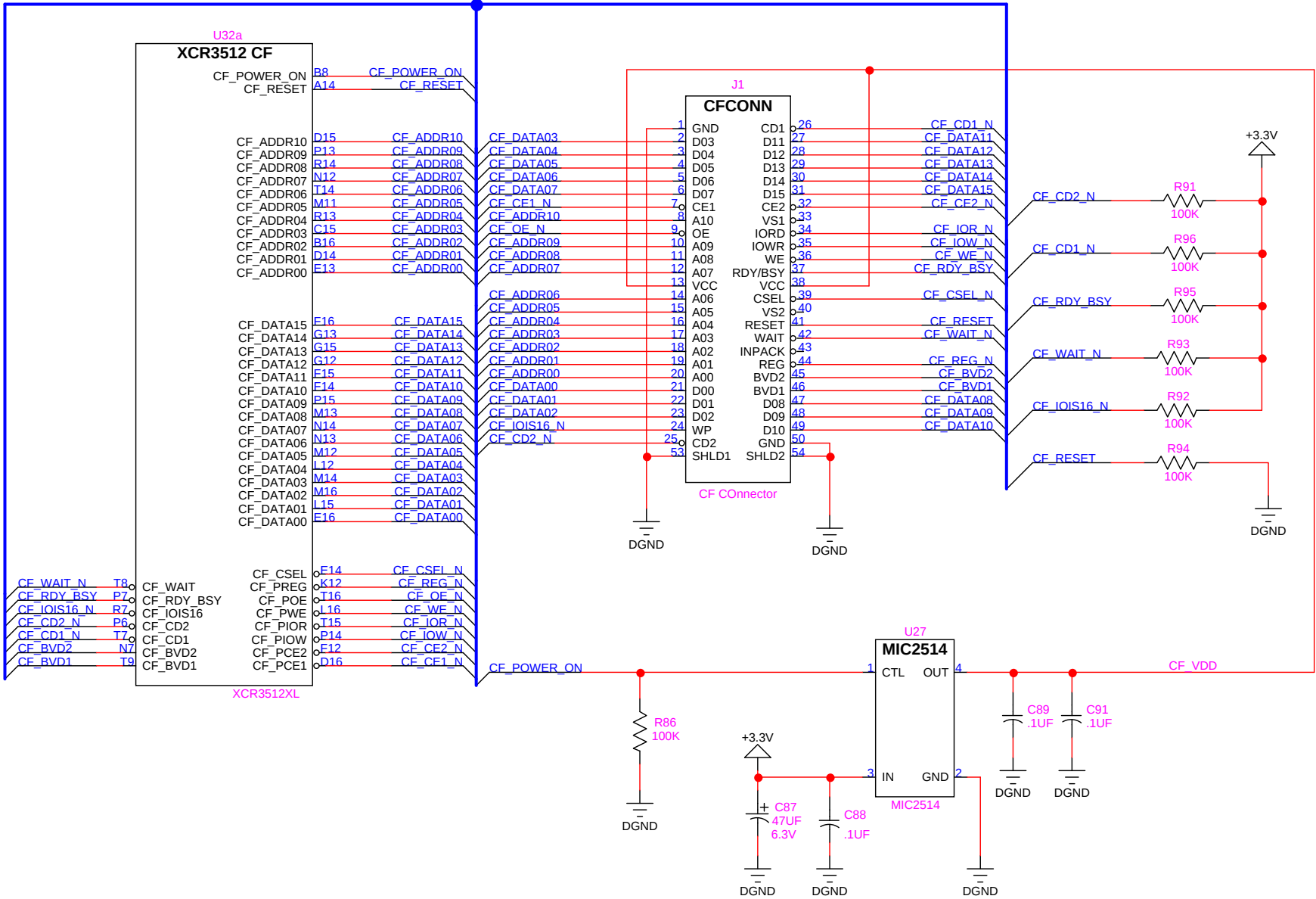
# STRONGARM SERIAL I/O



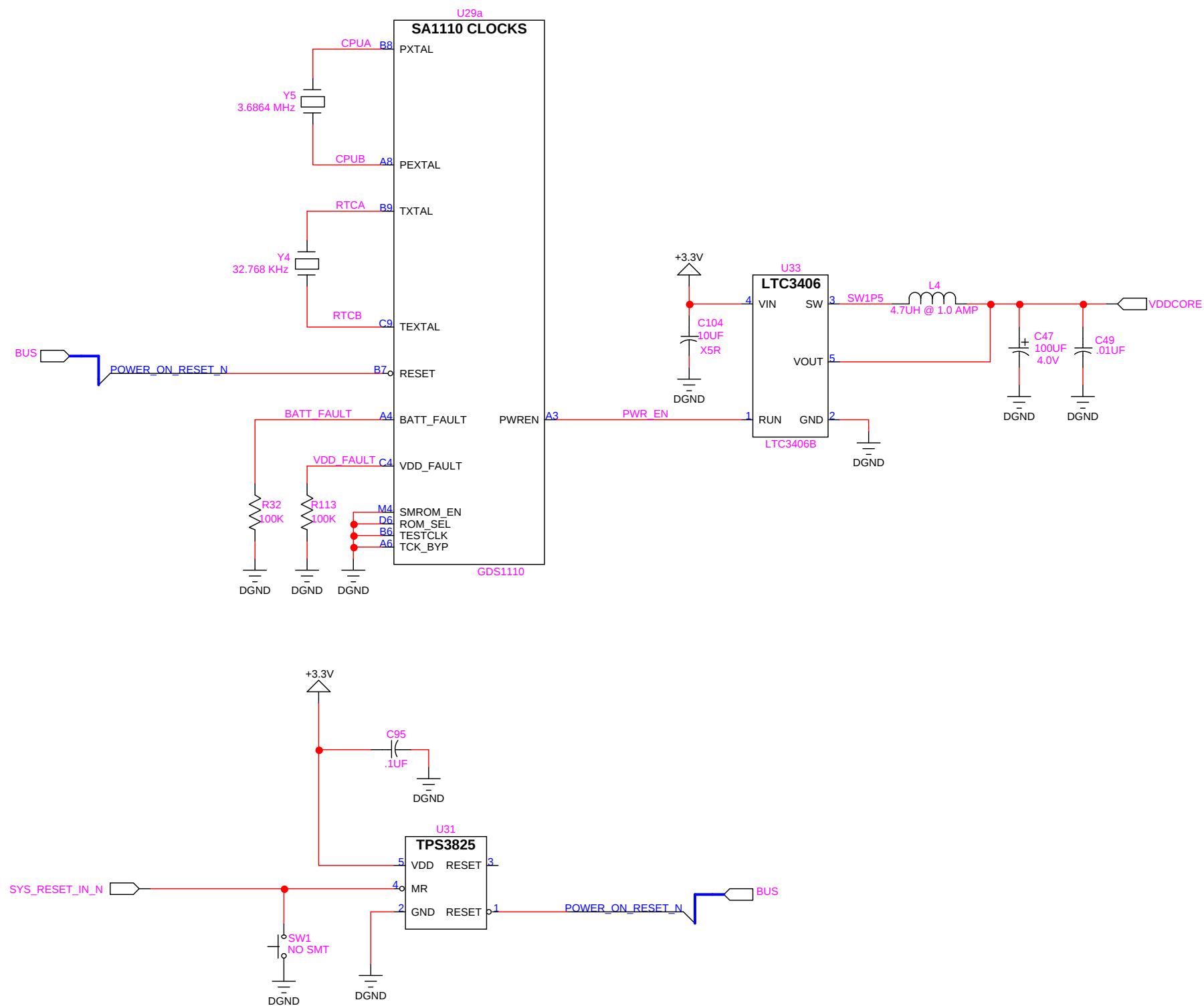
## UART BUS INTERFACE



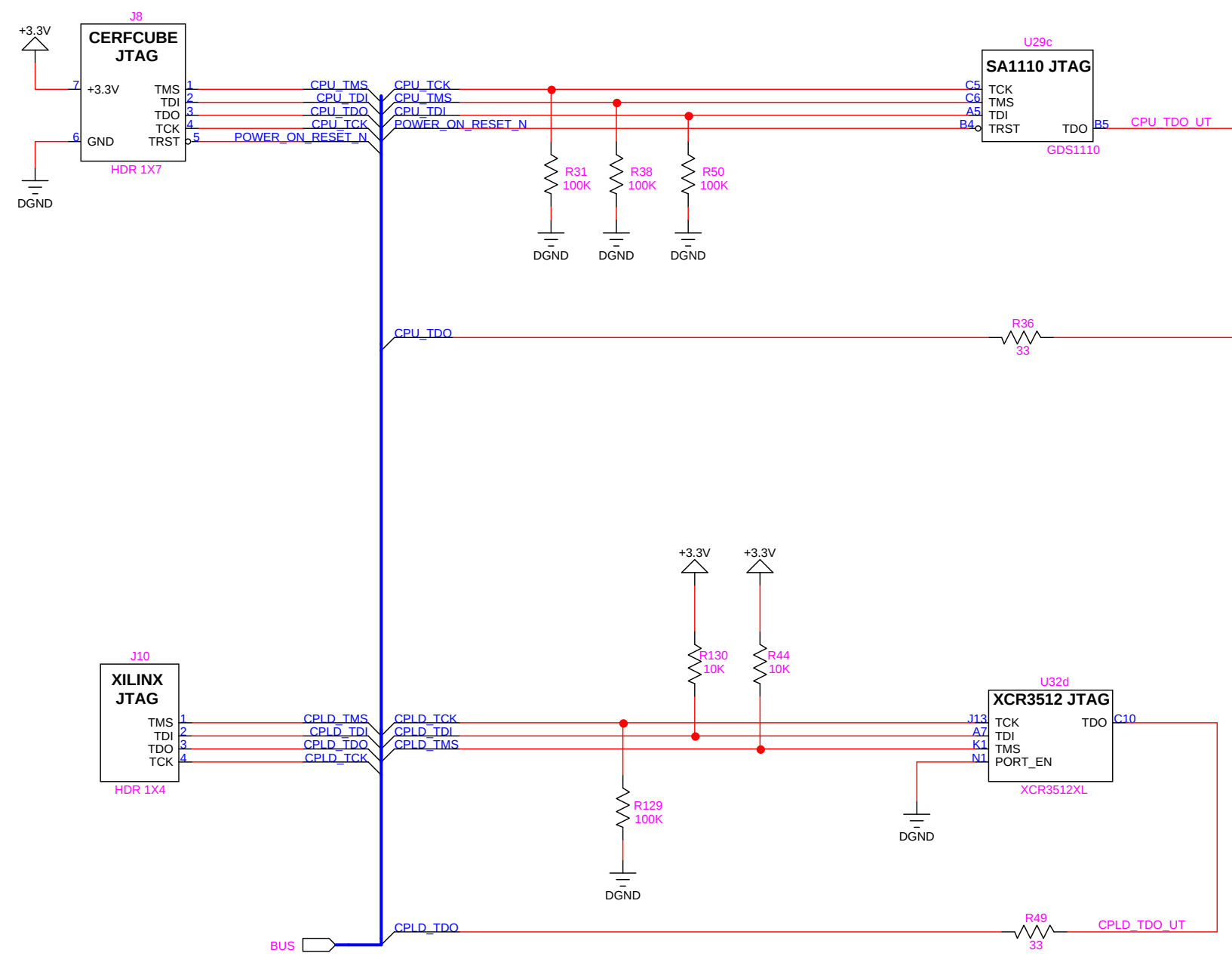
COMPACT FLASH



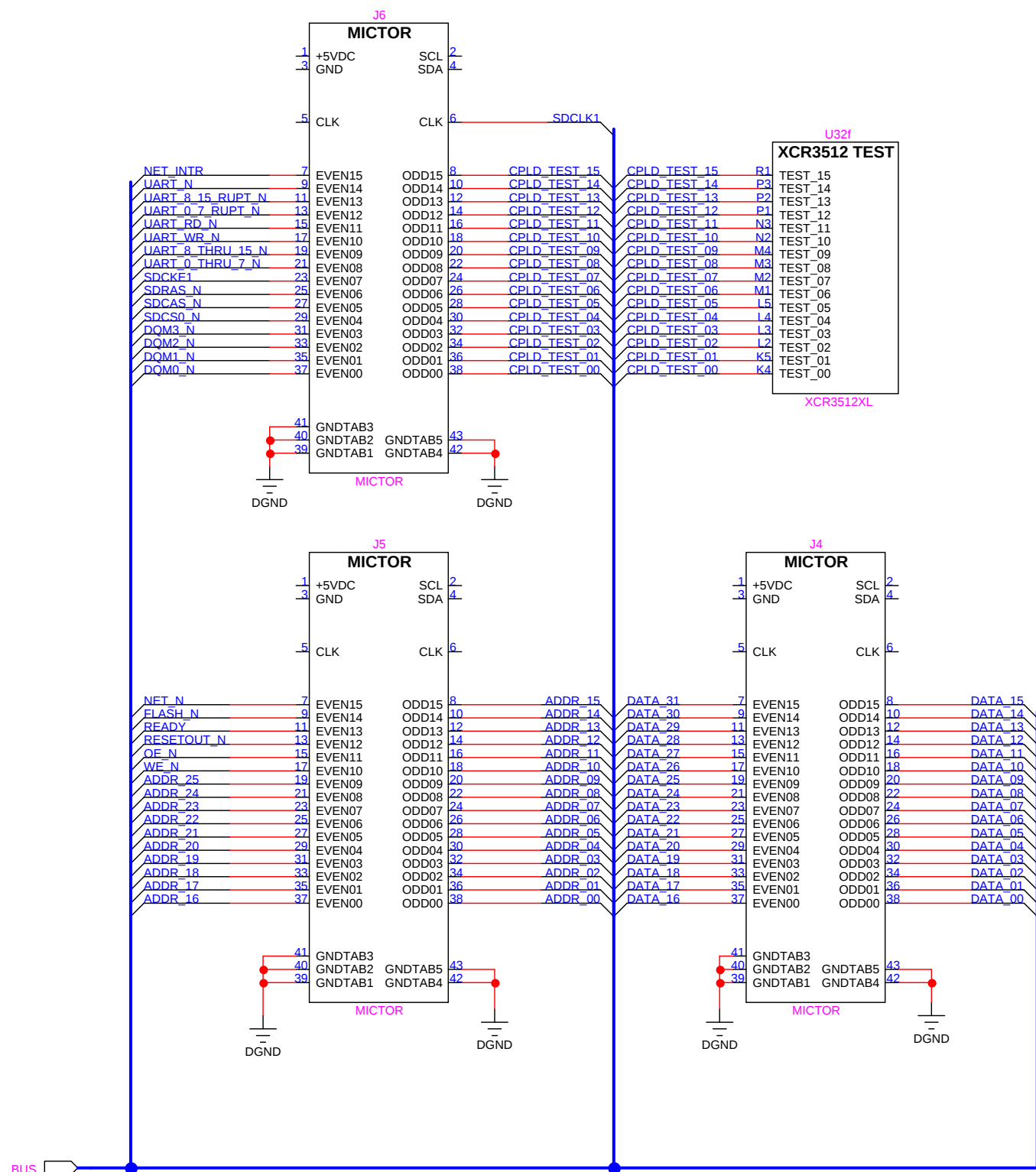
# CLOCKS, RESET & CORE POWER



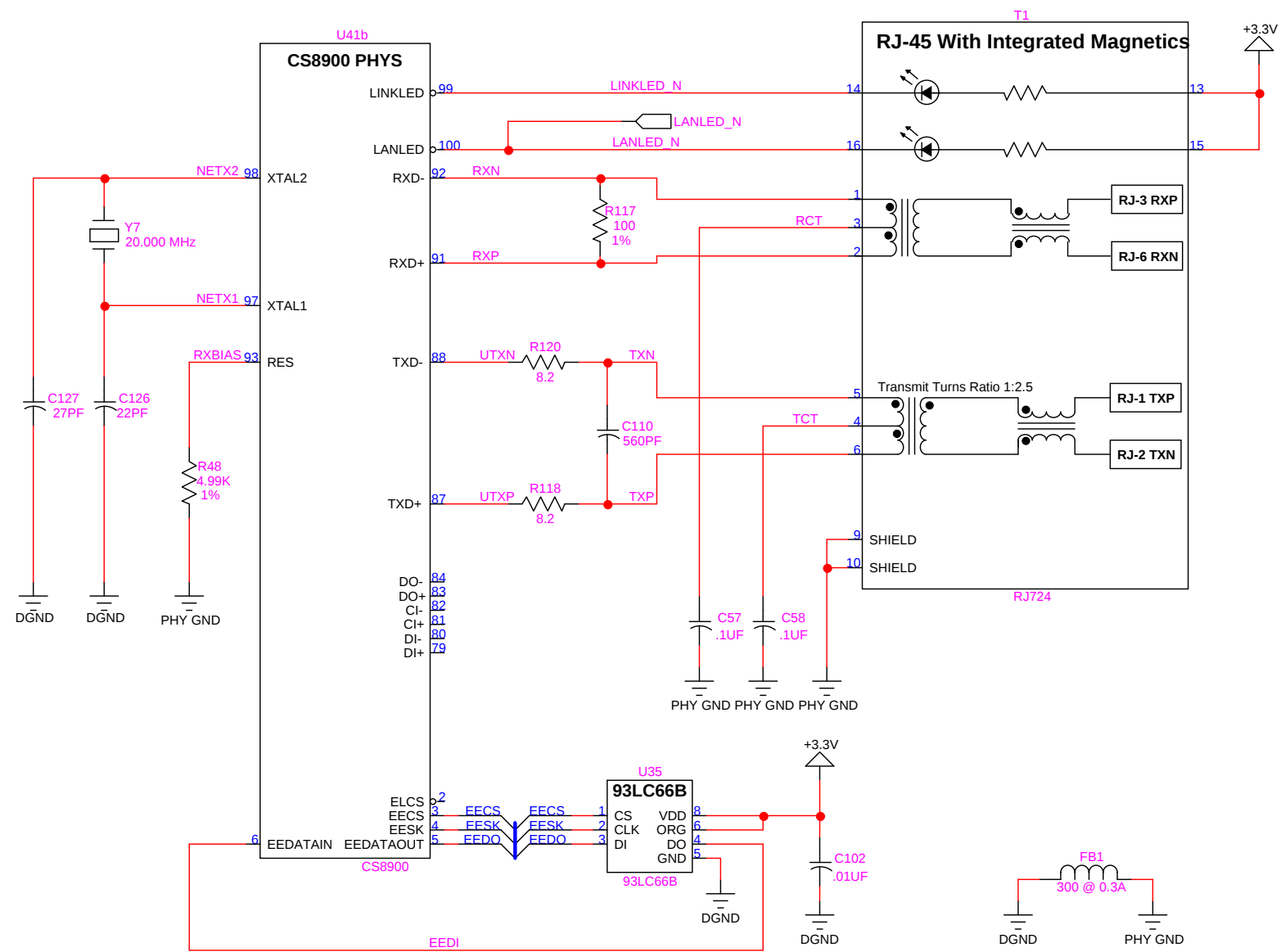
# JTAG INTERFACE



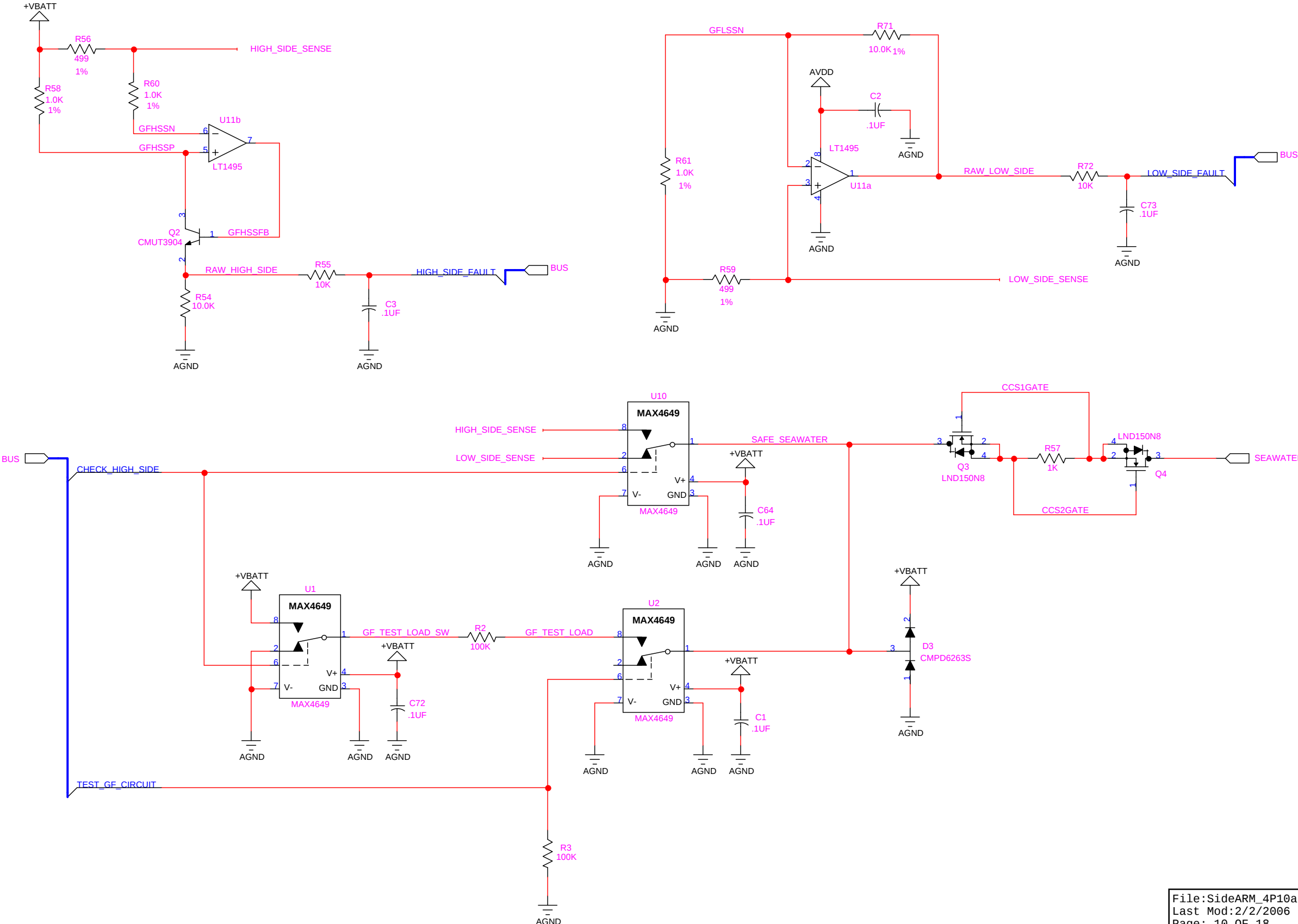
# LOGIC ANALYZER TEST POINTS



# ETHERNET PHYSICAL INTERFACE

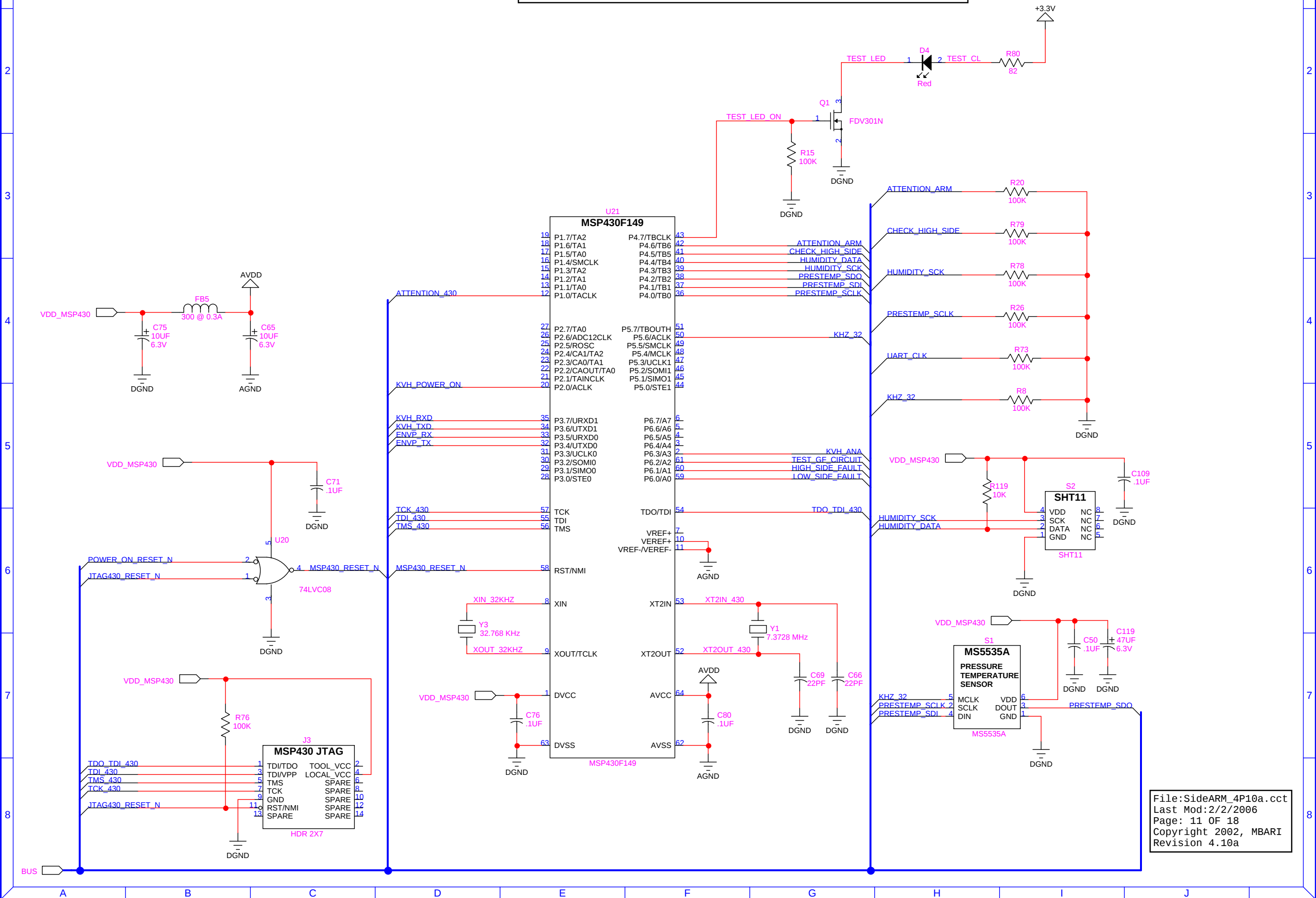


## GROUND FAULT CURRENT MEASUREMENT

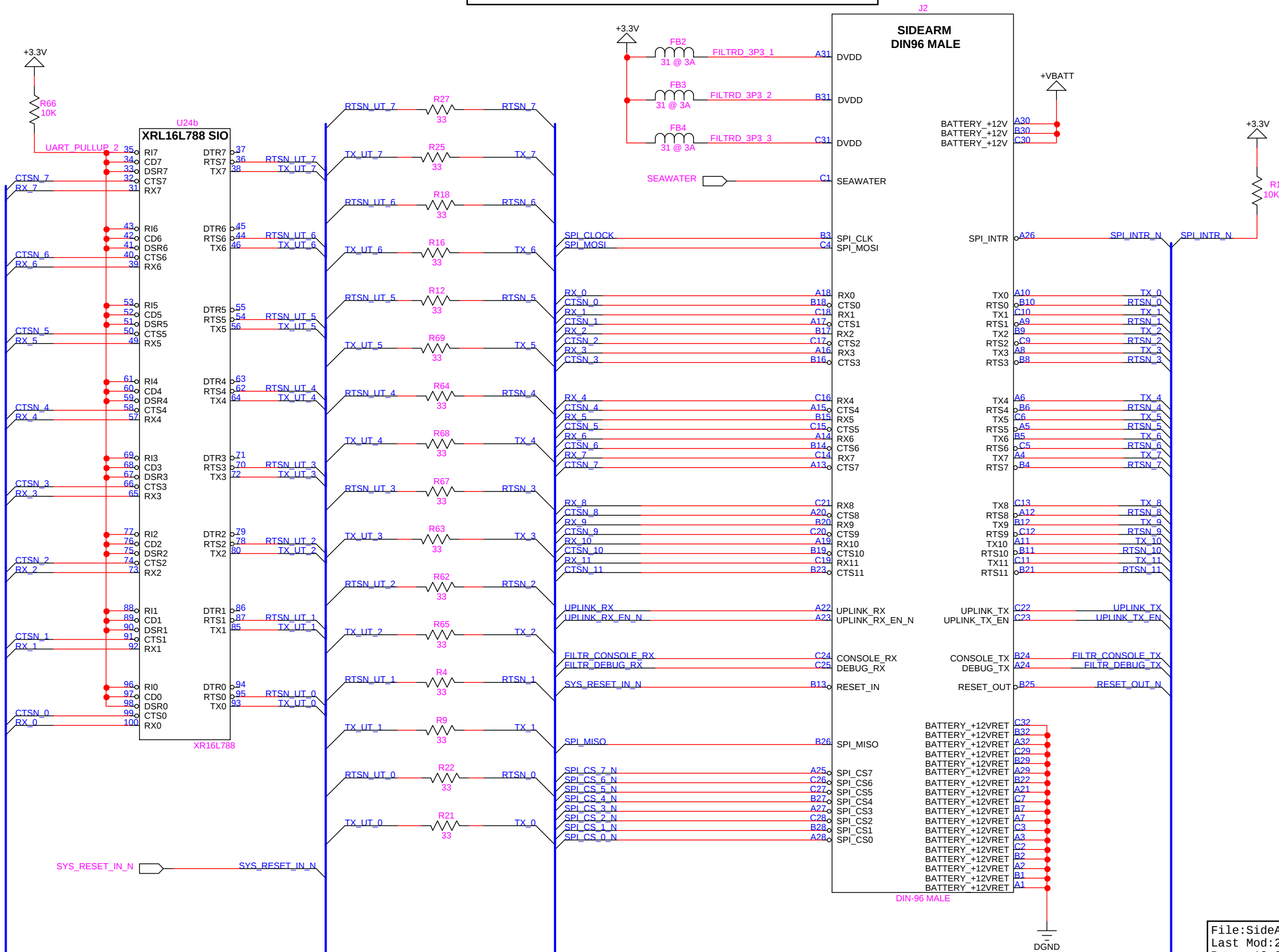


File:SideARM\_4P10a.cct  
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Revision 4.10a

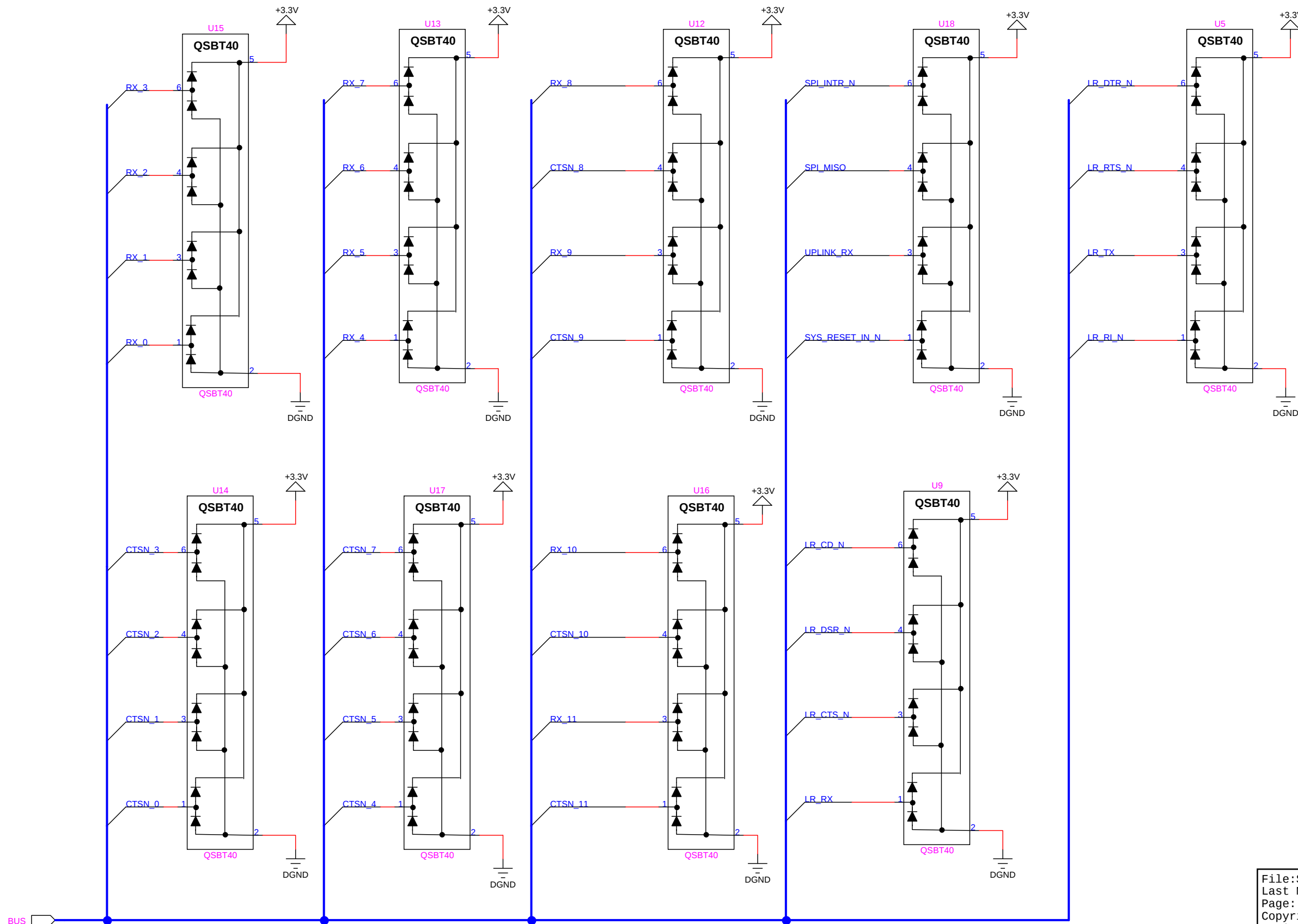
# ENVIRONMENTAL MONITORING



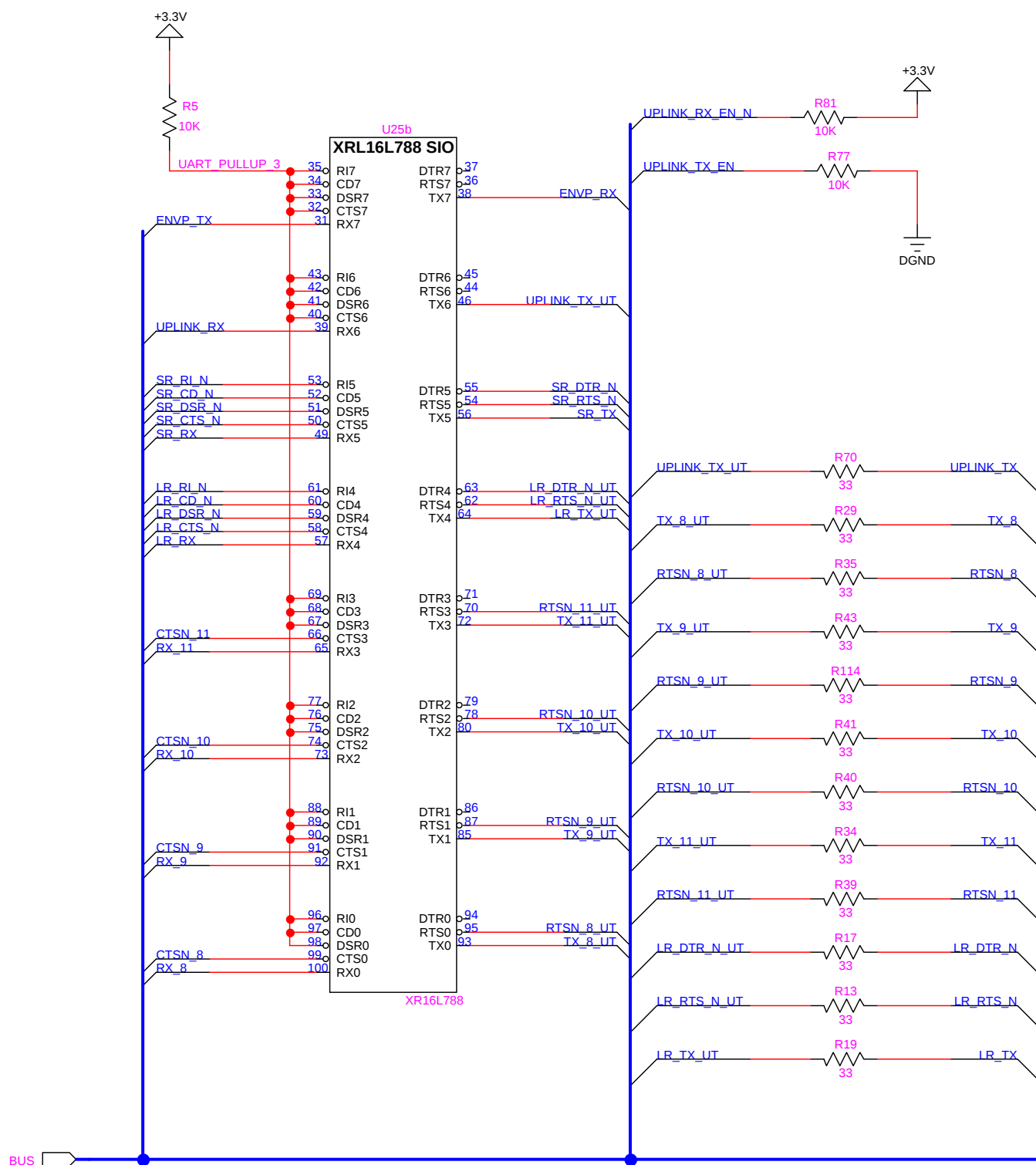
# BACKPLANE CONNECTOR



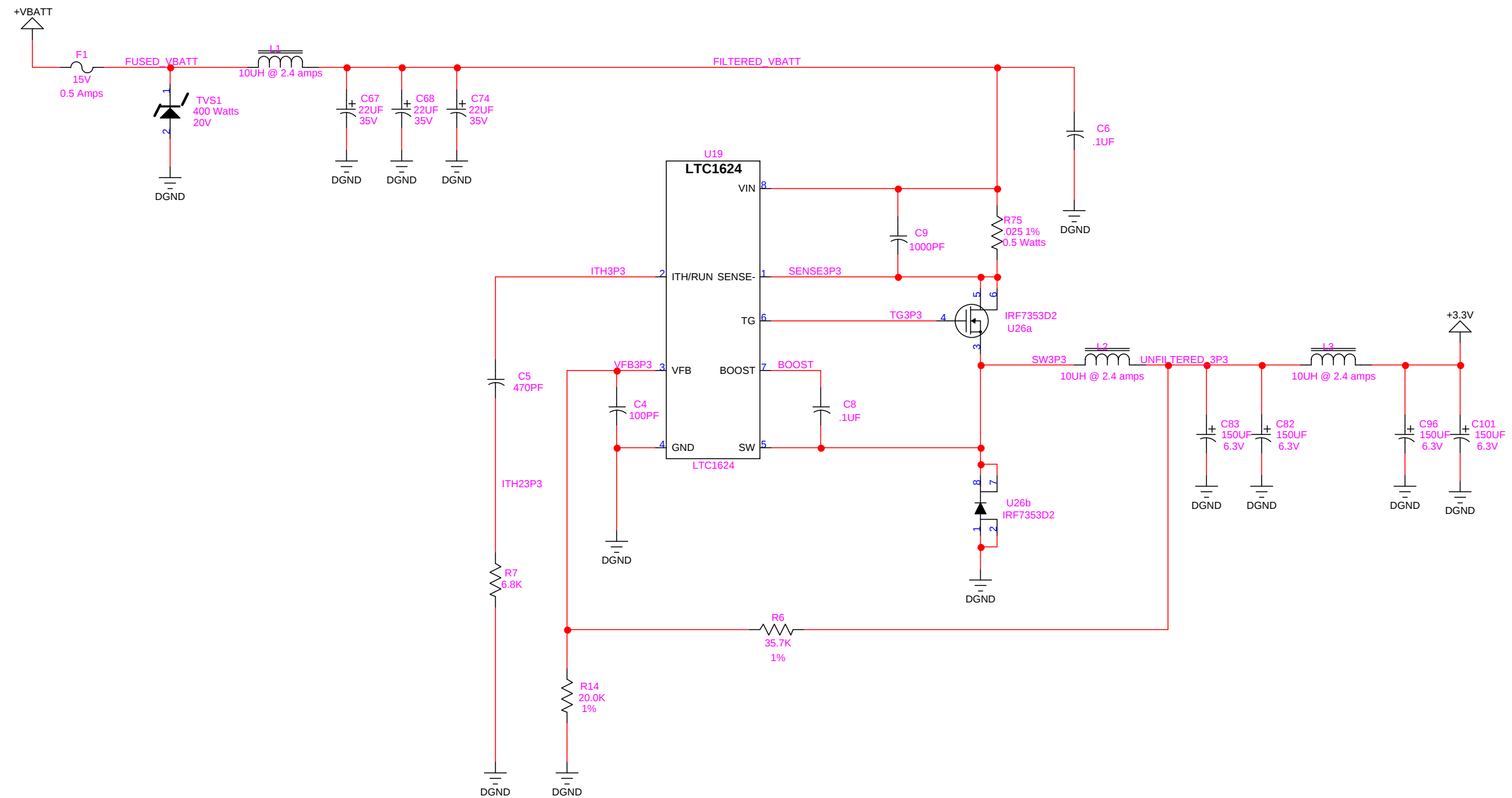
# BACKPLANE TERMINATIONS



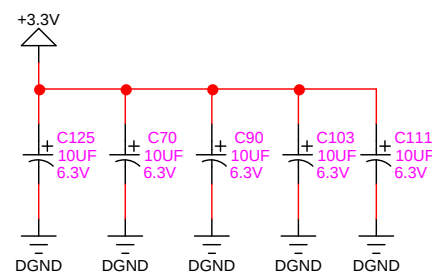
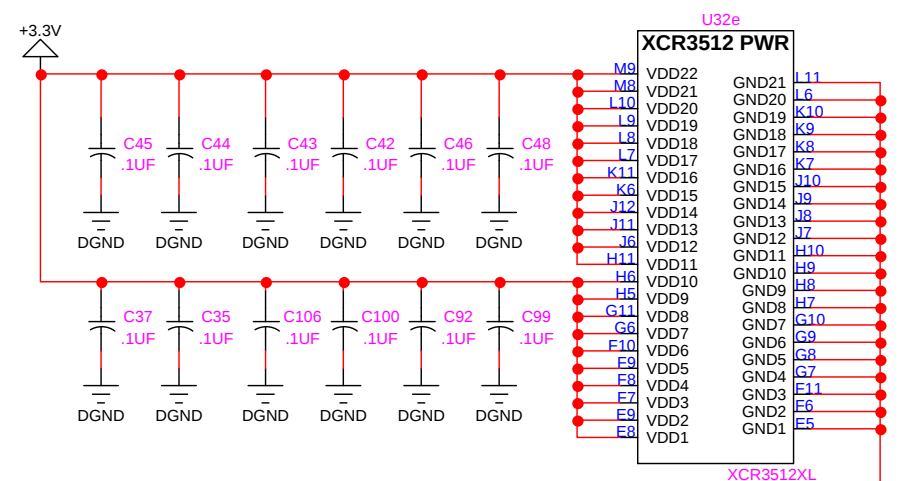
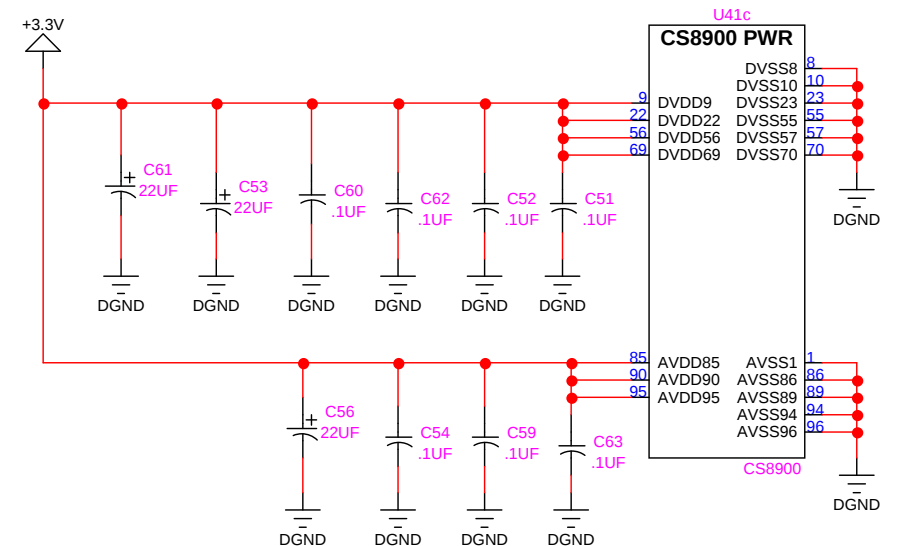
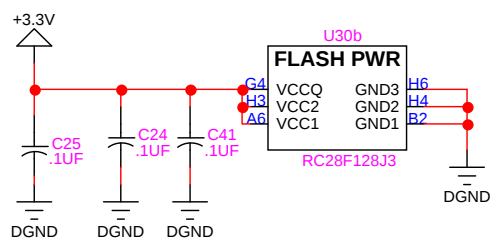
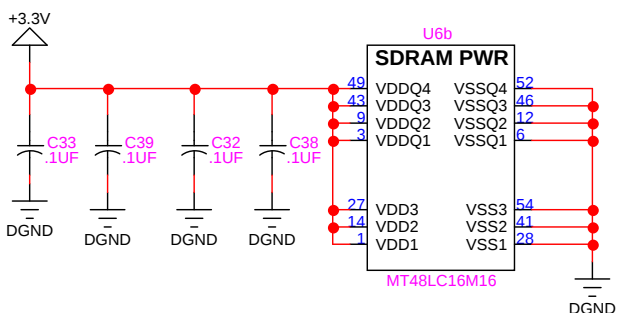
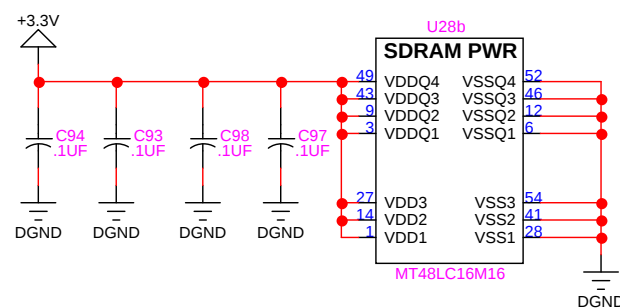
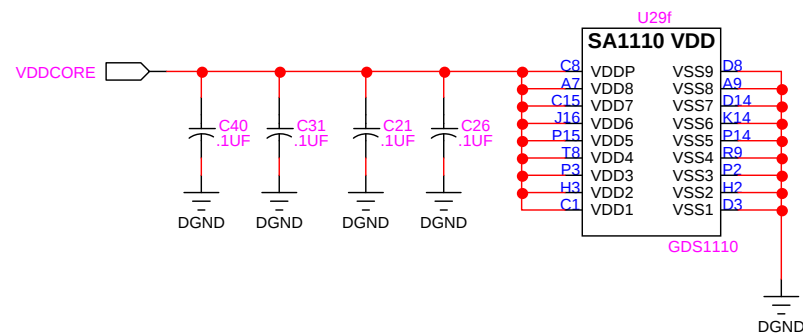
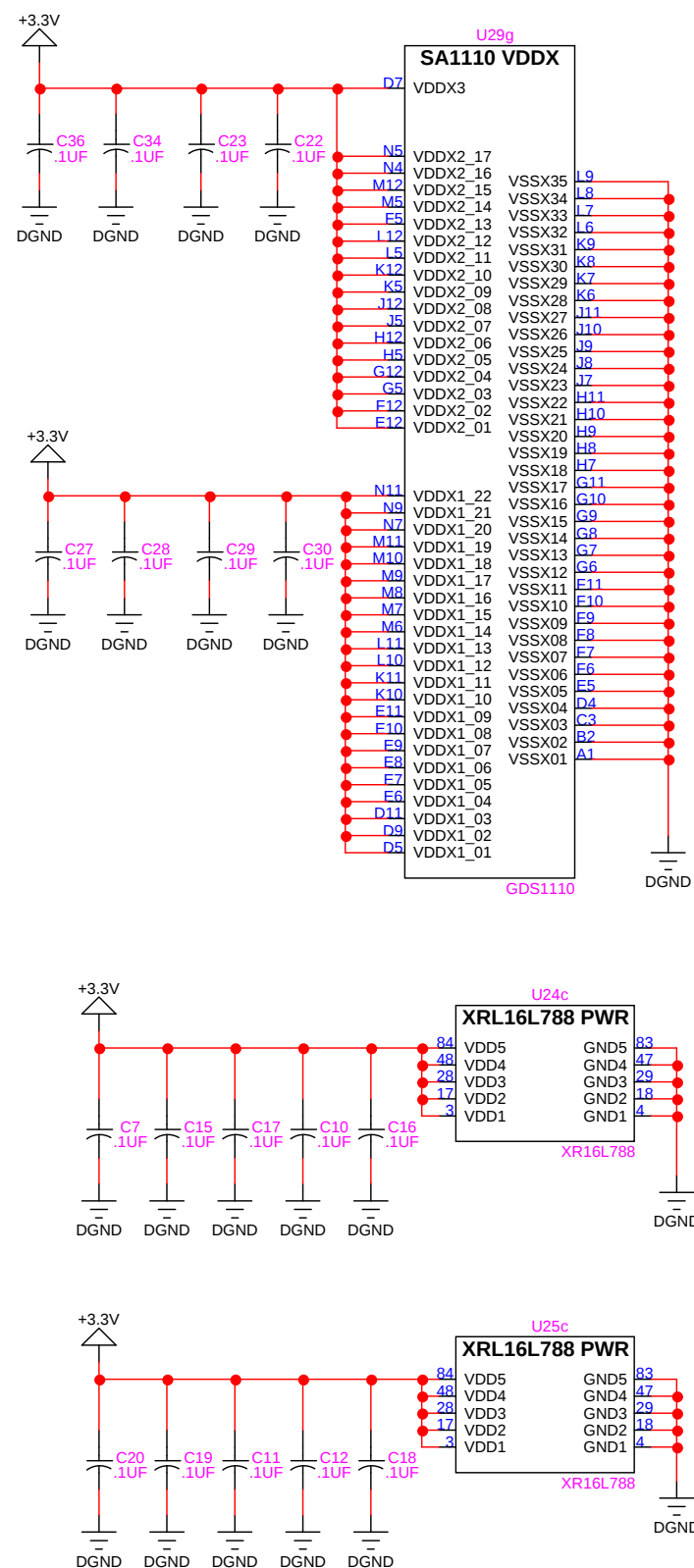
# UART AND UPLINK



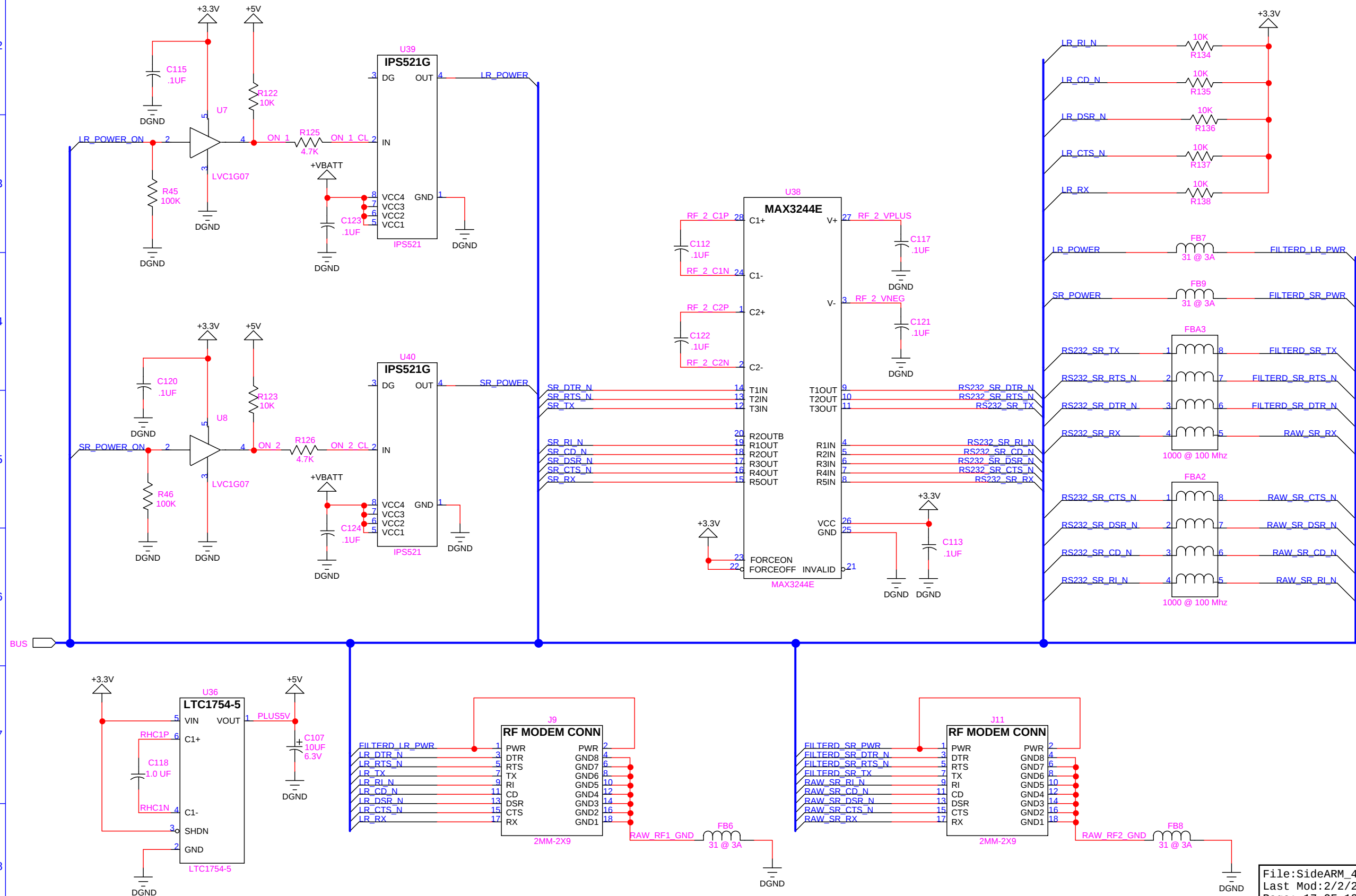
# POWER SUPPLY



# POWER DISTRIBUTION



# RF MODEM INTERFACE



# KVH COMPASS INTERFACE

