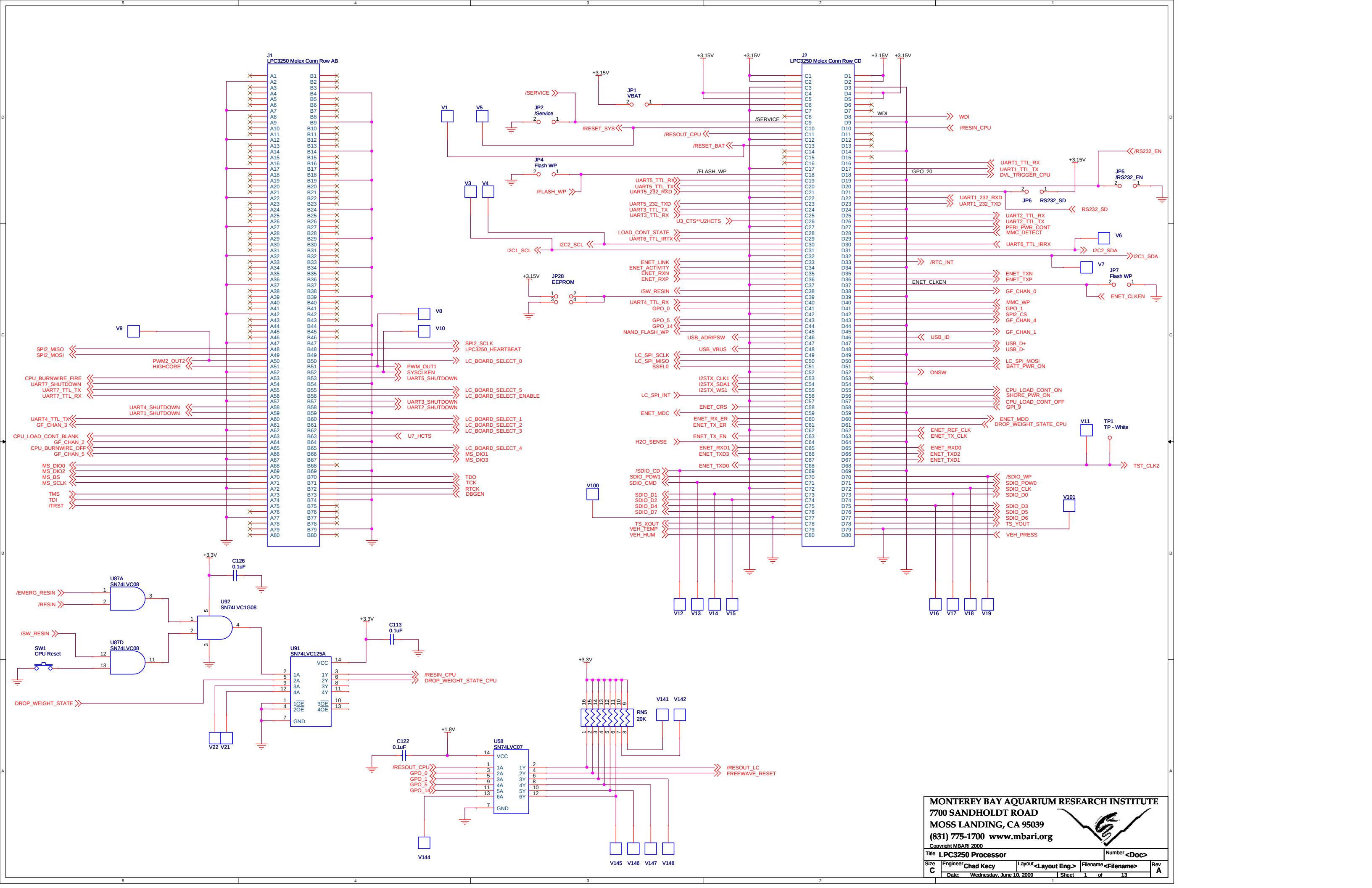
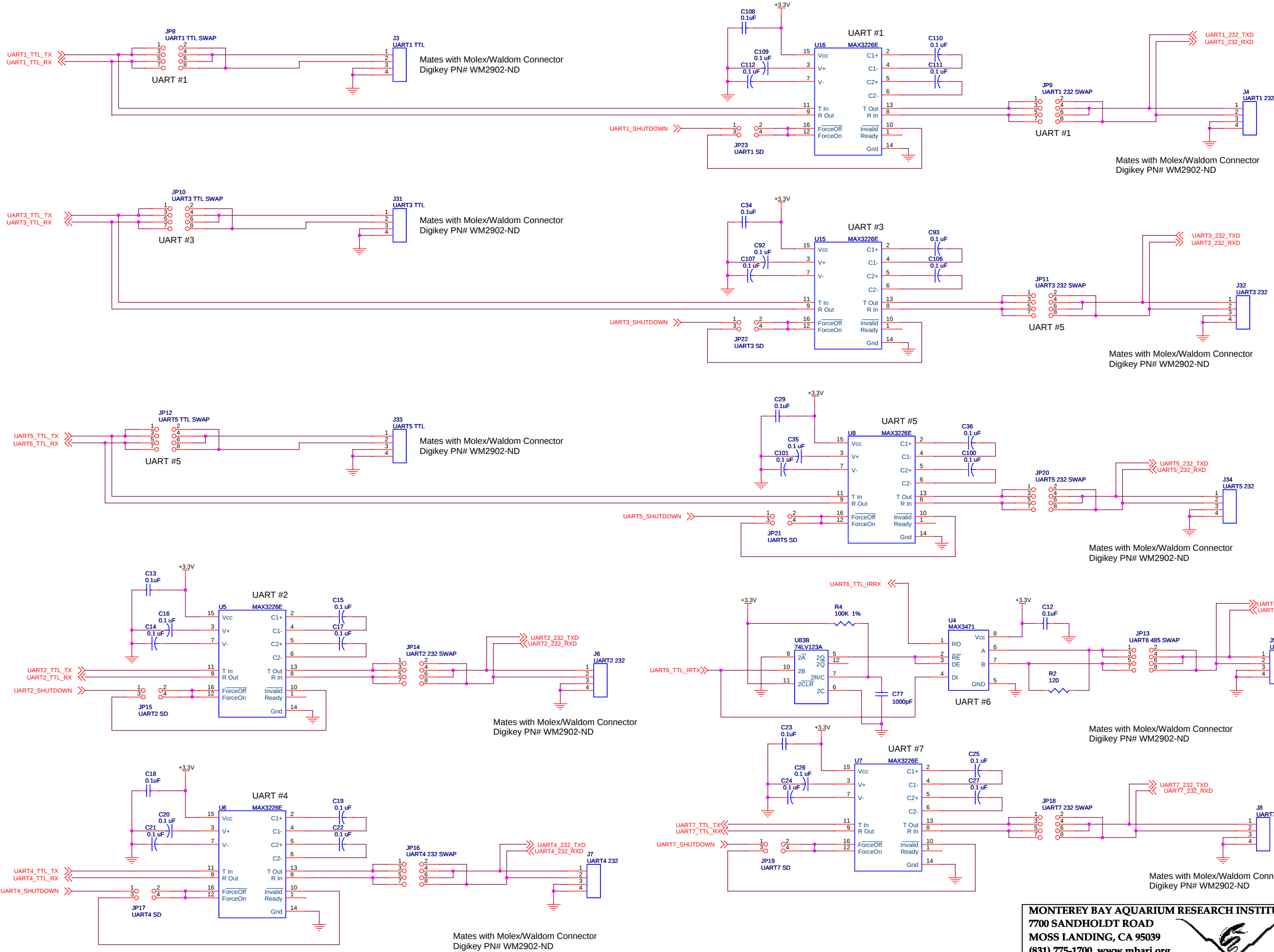




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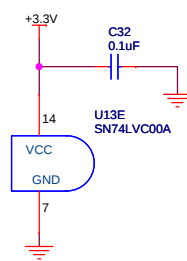
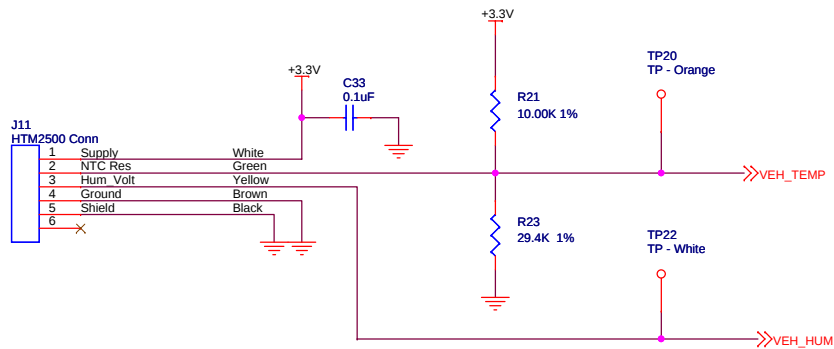
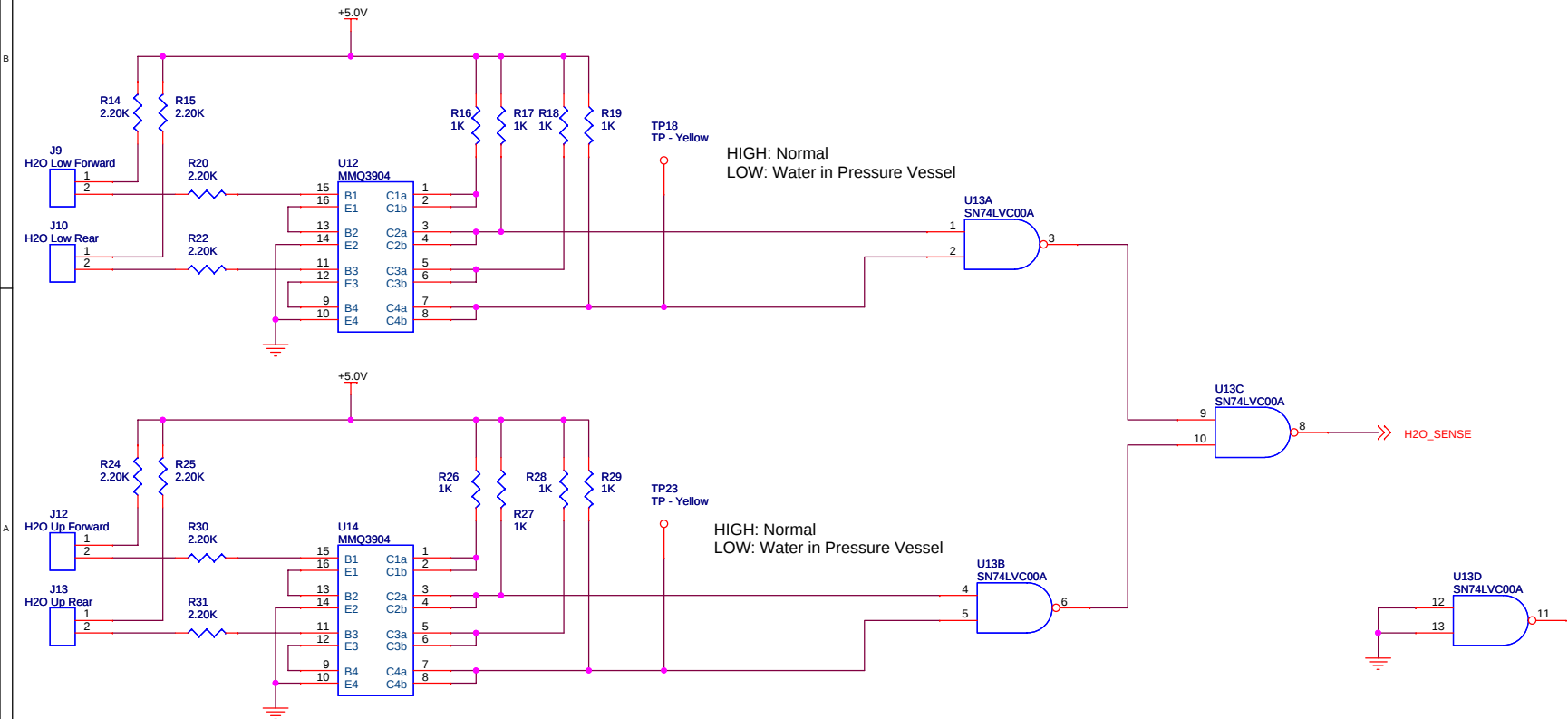
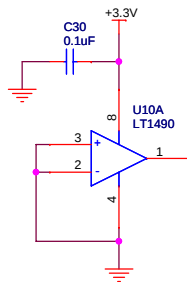
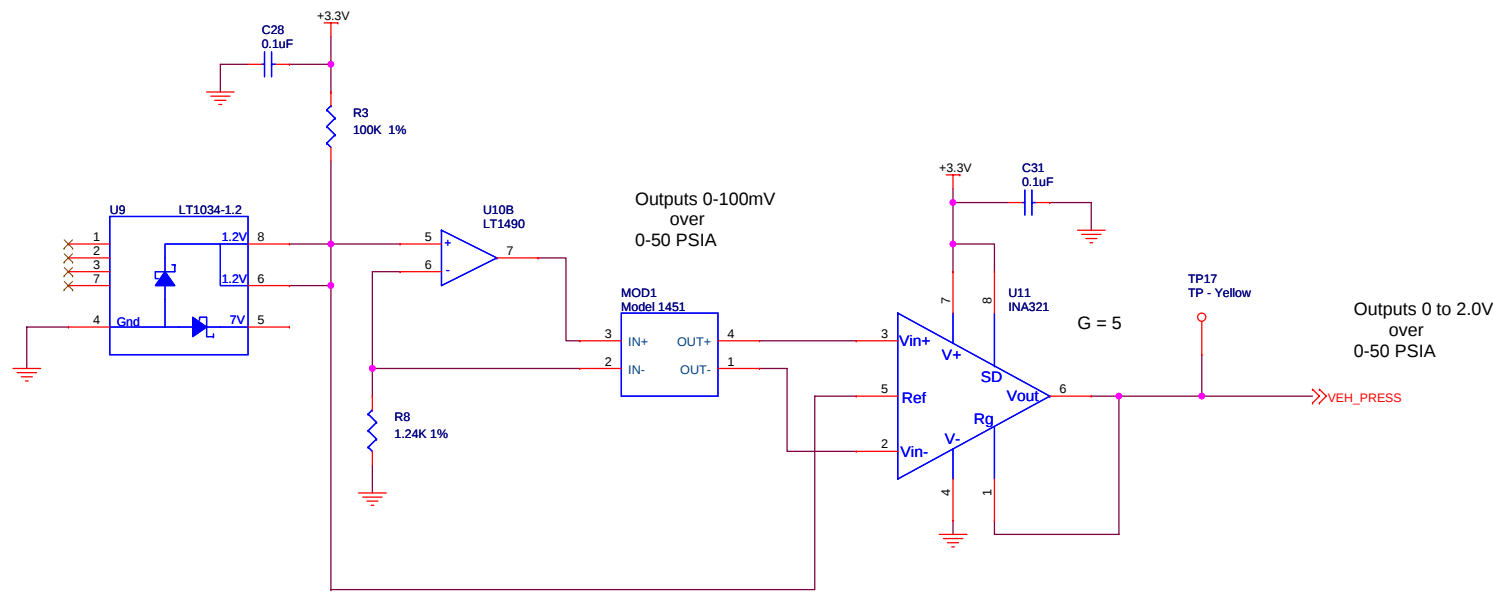
Title LPC3250 Processor			Number <Doc>
Size C	Engineer Chad Kacy	Layout <Layout Eng>	Filename <Filename>
Date: Wednesday, June 10, 2009			Rev A
Sheet 1 of 13			



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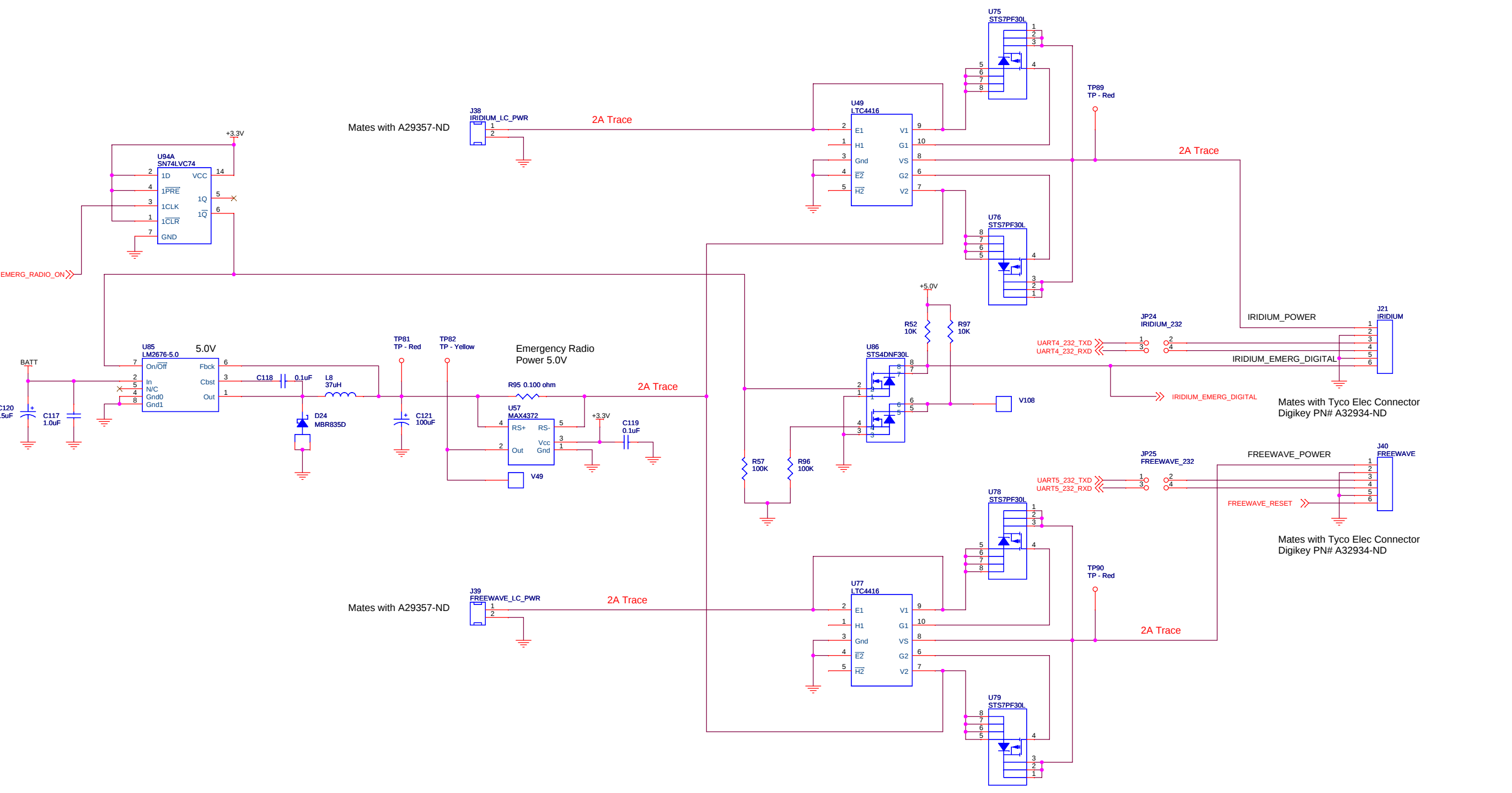


Title Tethys Serial Circuits					Number <Doc>		
Size C	Engineer Chad Key		Layout <Layout Eng.>		Filename <Filename>		Rev
	Date: Wednesday, June 10, 2009		Sheet 2 of 13				



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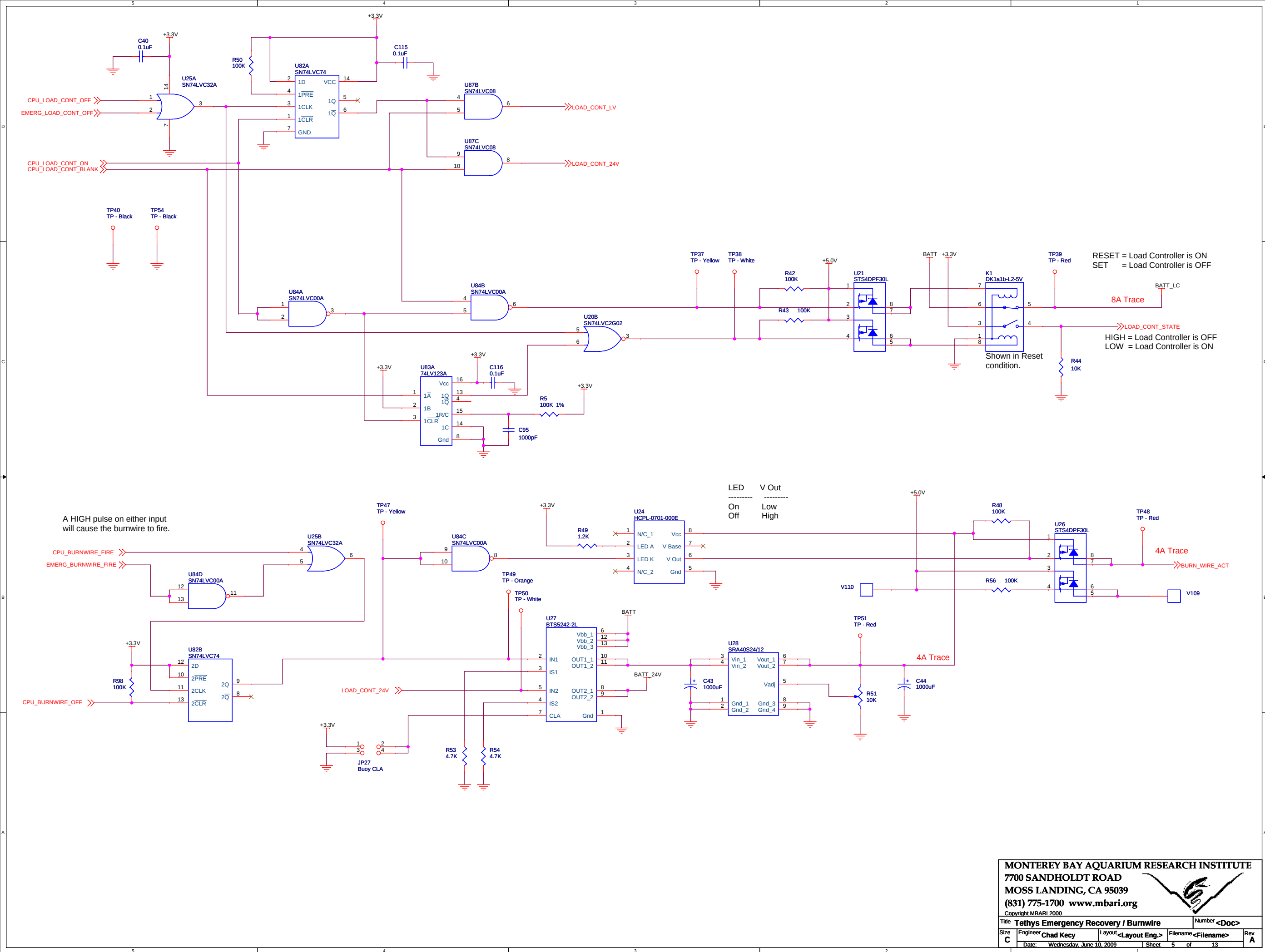
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Size C	Engineer Chad Kacy	Layout <Layout Eng.>	Filename <Filename>
Date: Monday, June 08, 2009			Rev A

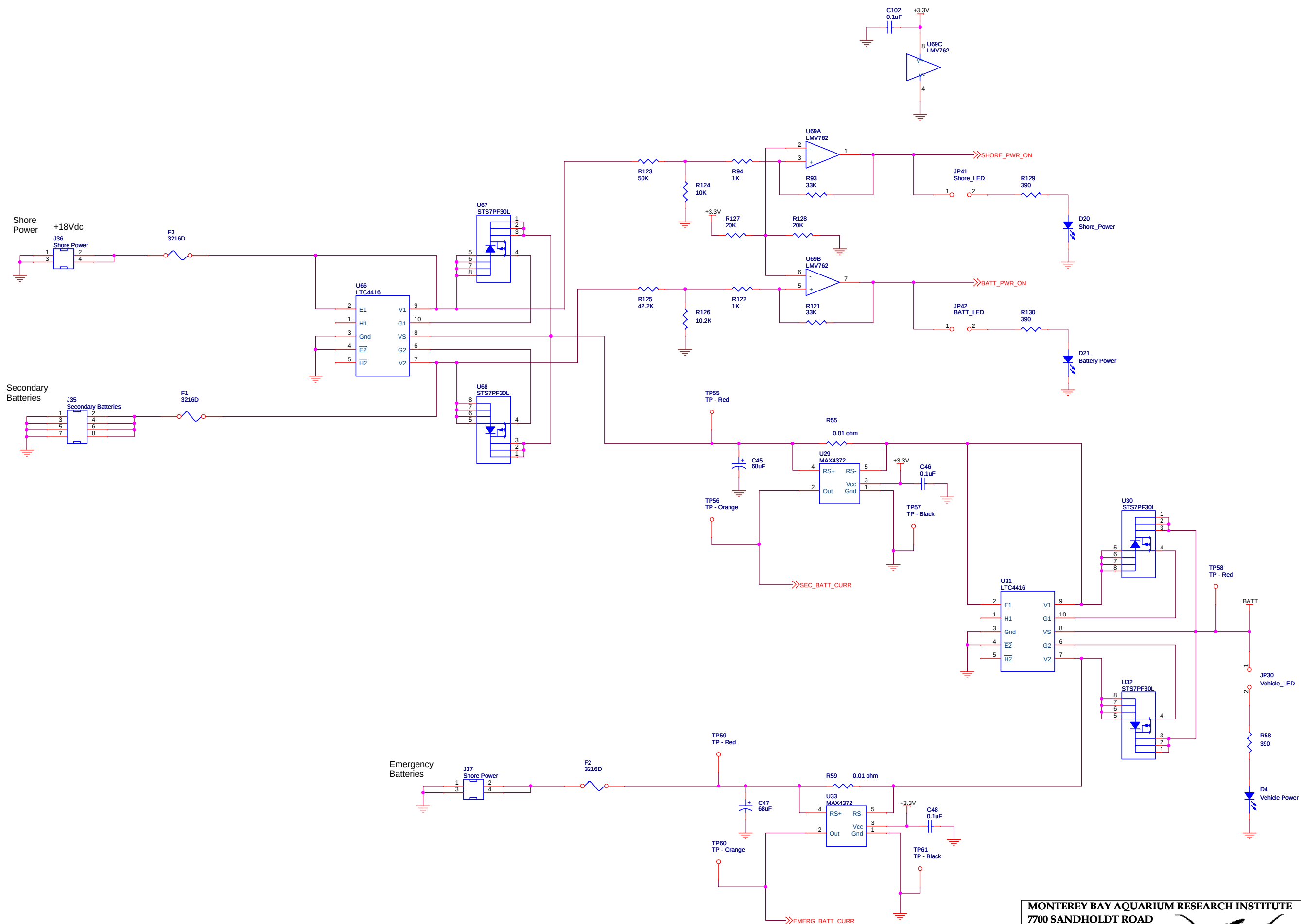


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Title	Tethys Buoyancy Engine Interface			Number	<Doc>
Size	Engineer	Layout	Filename	Rev	
C	Chad Kacy	<Layout Eng.>	<Filename>	A	
Date:	Wednesday, June 10, 2009	Sheet	4	of	13

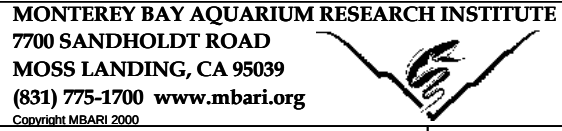




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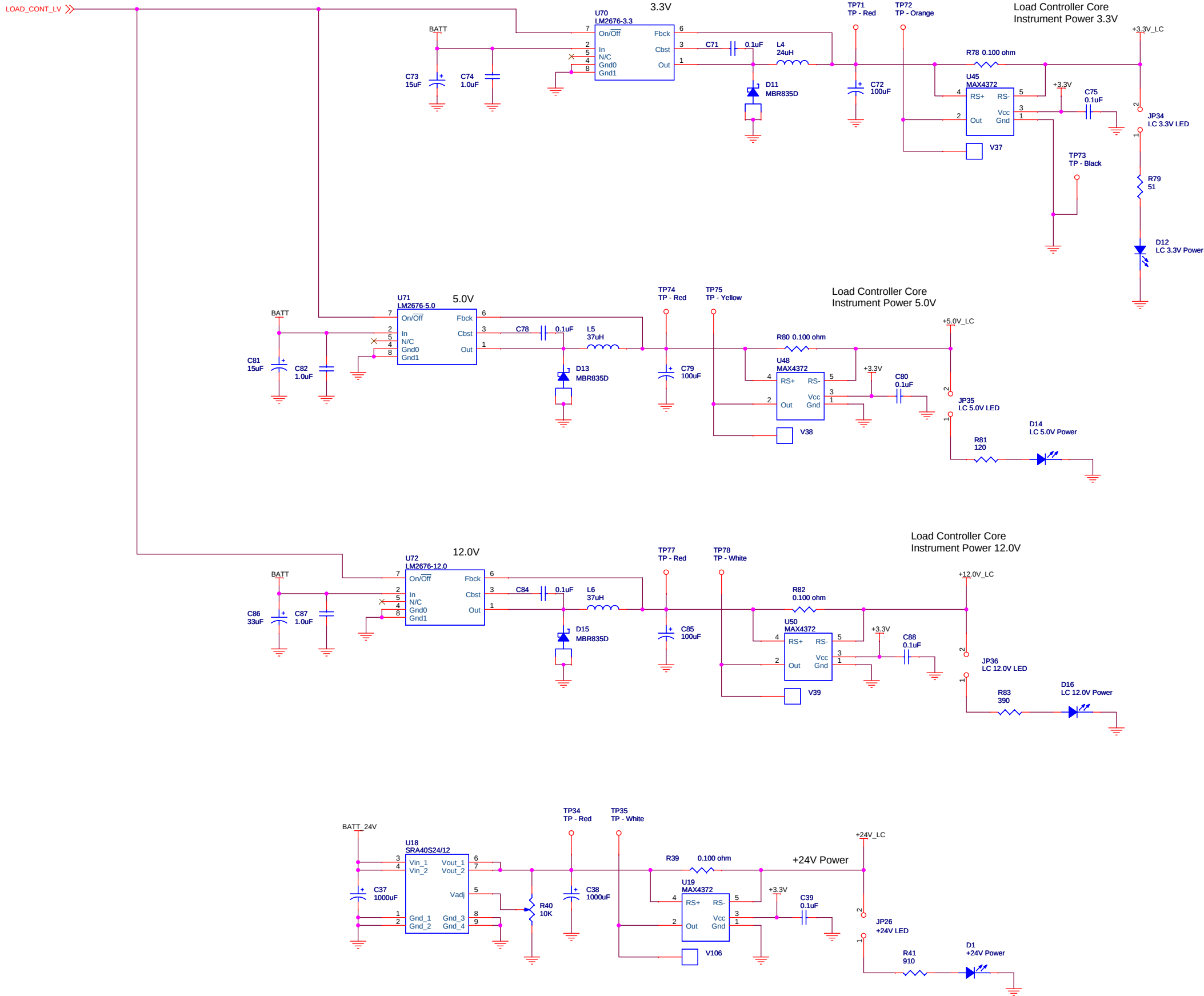
Title Tethys Battery Inputs		Number <Doc>	
Size C	Engineer Chad Key	Layout <Layout Eng.>	Filename <Filename>
Date: Thursday, June 04, 2009		Sheet 6 of 13	Rev A



Cap	Time
22nF	58 msec
33nF	87 msec
47nF	124 msec
68nF	180 msec
100nF	265 msec
150nF	398 msec
220nF	583 msec

ADM6820 Delay

Delay Time = 2.652E6 * Cset



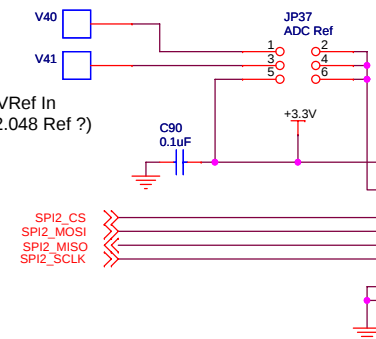
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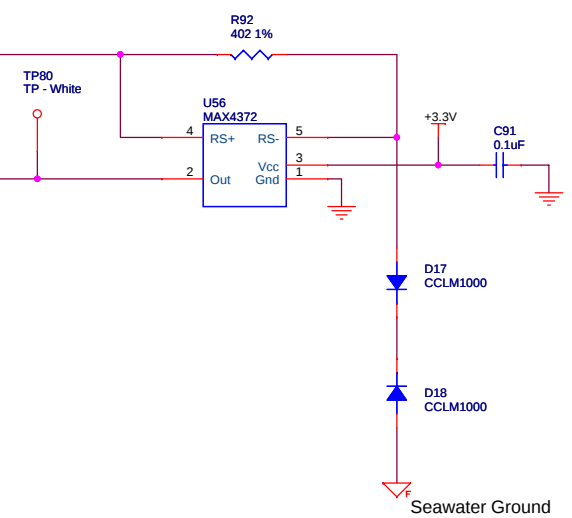
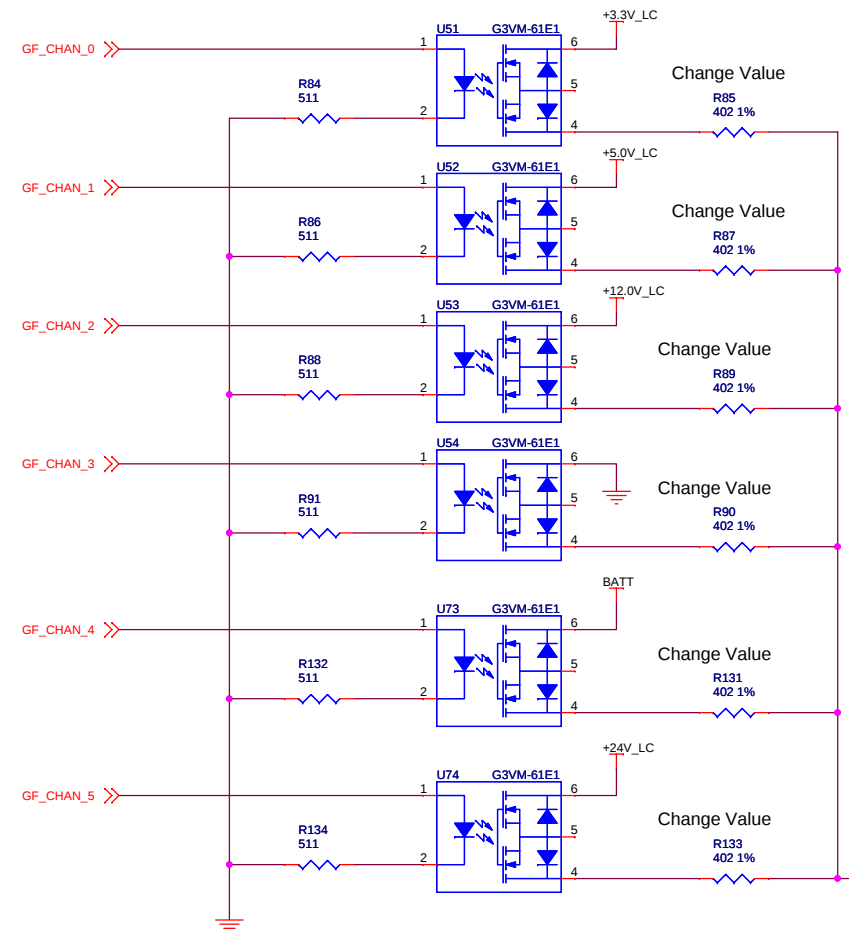
Title Tethys Load Controller Instrument Power		Number <Doc>	
Size C	Engineer Chad Key	Layout <Layout Eng.>	Filename <Filename>
Date: Monday, June 08, 2009		Sheet 8 of 13	Rev A

Optional VRef Out

Optional VRef In
(2.5V or 2.048 Ref ?)

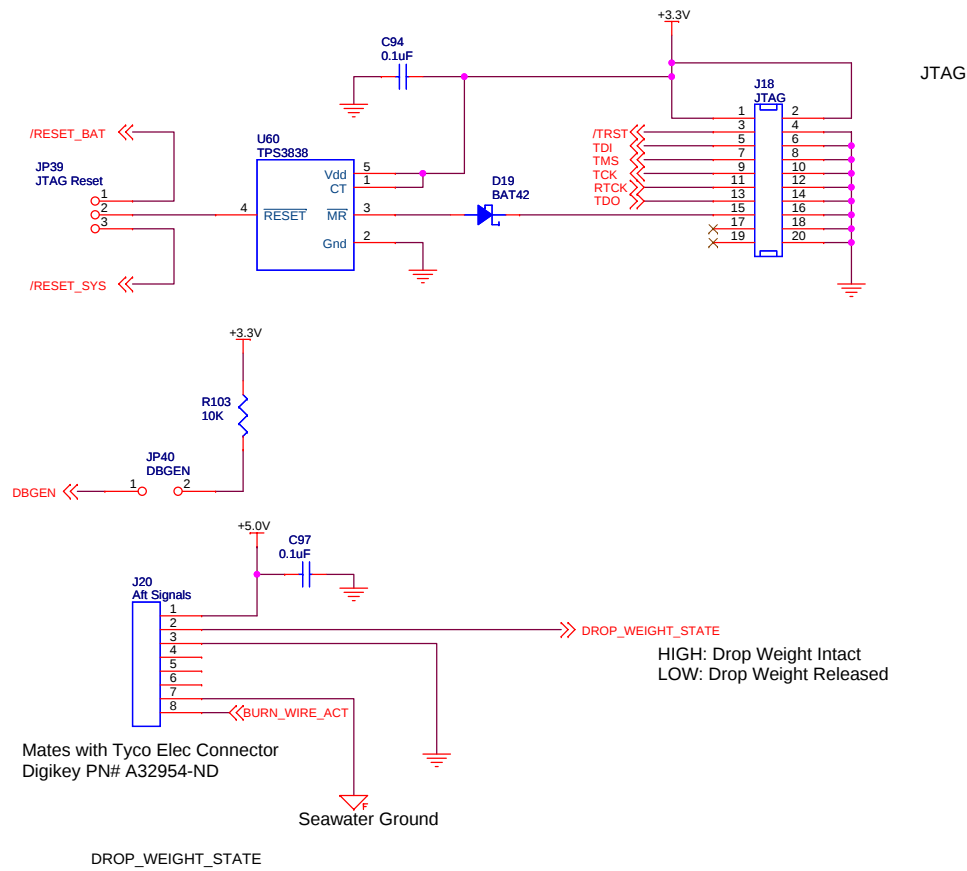


Digital Output
from CPU



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Title Tethys Ground Faulting Circuit		Number <Doc>	
Engineer Chad Key	Layout <Layout Eng.>	Filename <Filename>	Rev A
Date: Thursday, April 30, 2009	Sheet 9	of 13	



JTAG

DVL_TRIGGER_CPU

Mates with Molex/Waldom Connector
Digikey PN# WM2902-ND

HIGH: Peripheral Power Disabled
LOW: Peripheral Power Enabled

PERI_PWR_CONT

USB_D-

USB_D+

USB

USB

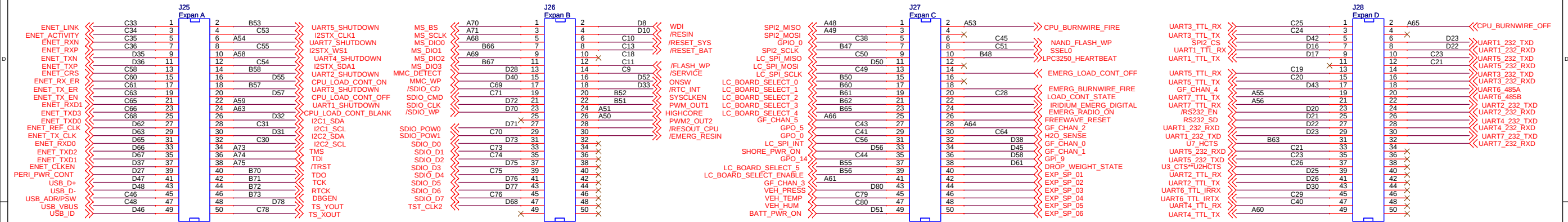
Ethernet

SD/MMC

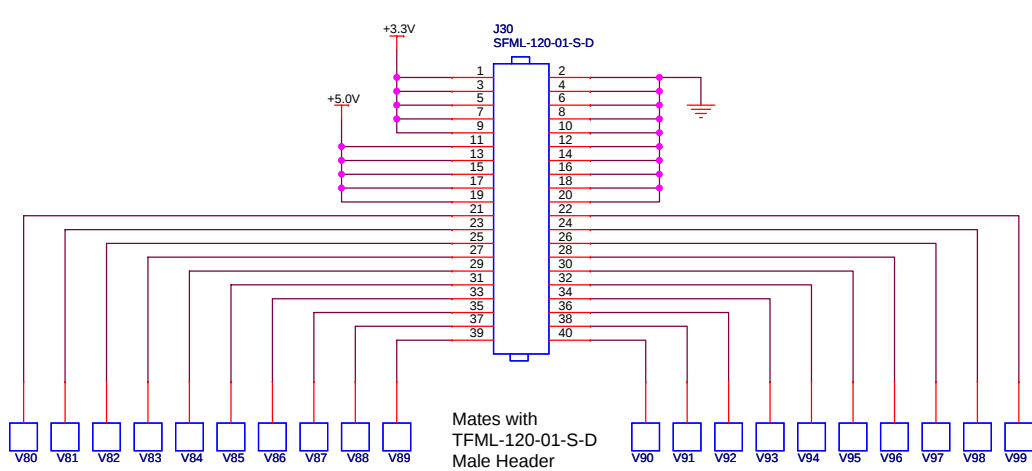
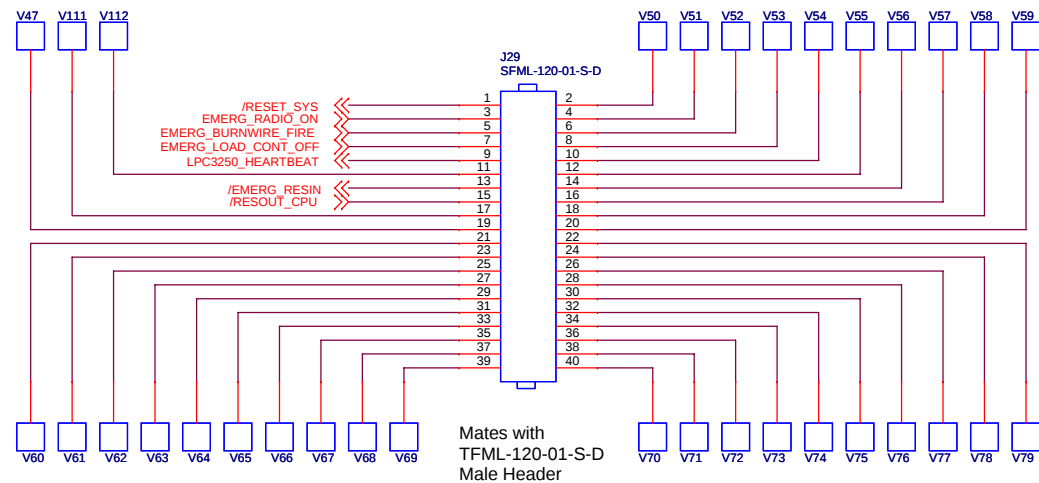
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Title: Tethys External Interfaces		Number: <Doc>	
Size: C	Engineer: Chad Kacy	Layout: <Layout Eng.>	Filename: <Filename>
Date: Monday, June 08, 2009	Sheet: 10	of: 13	Rev: A

To Expansion Test Connectors

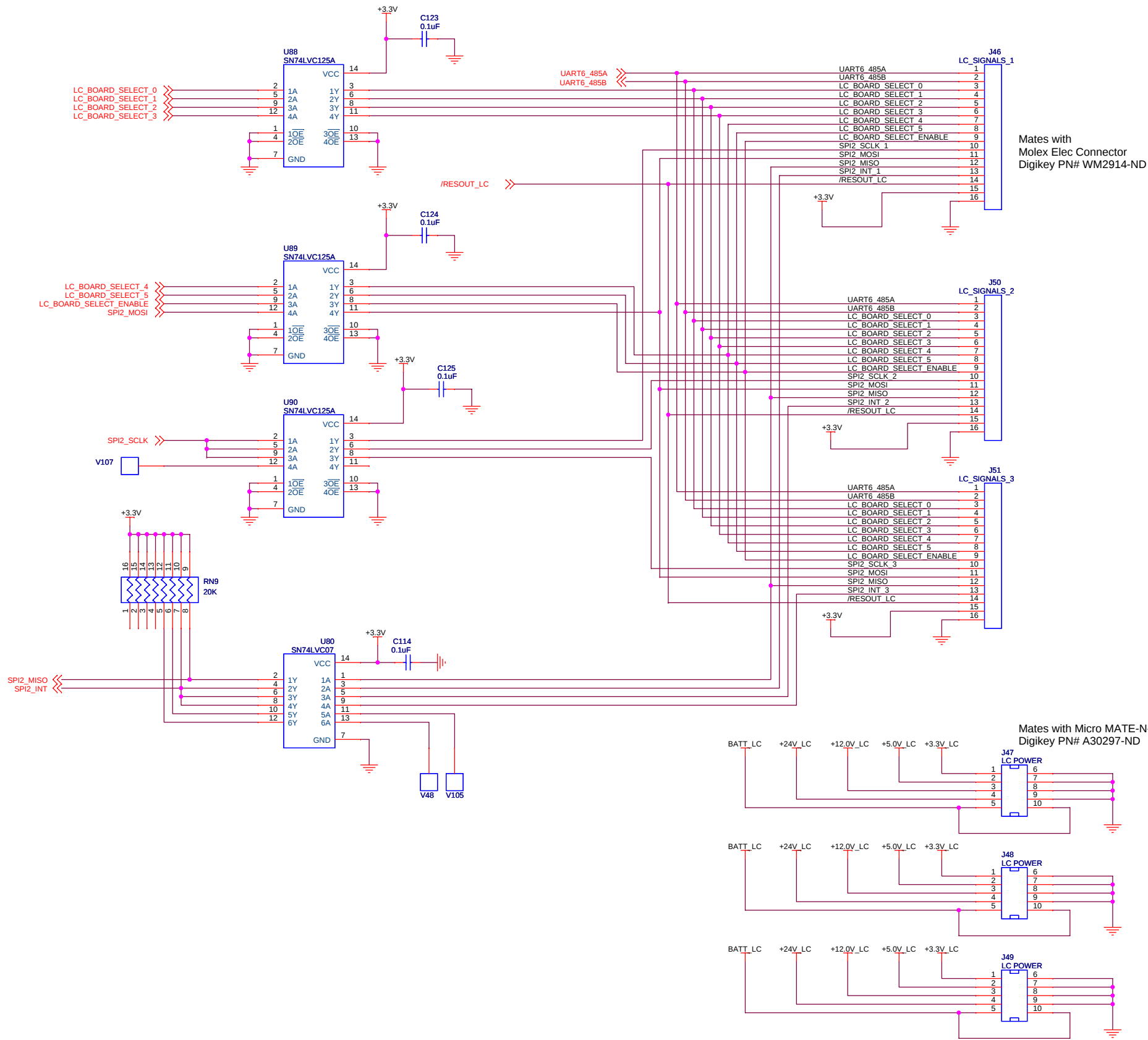


To Emergency Circuit Daughterboard



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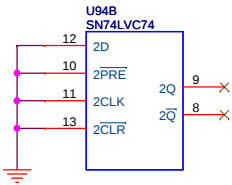
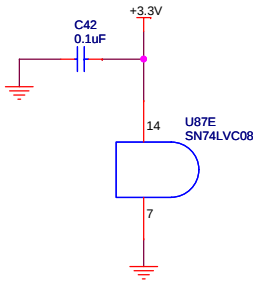
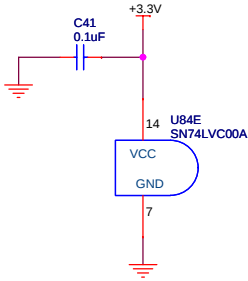
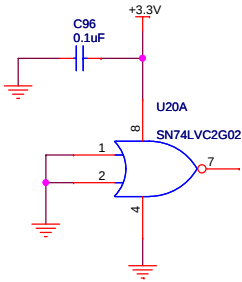
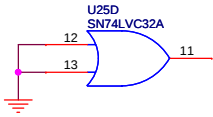
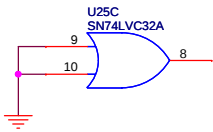


Mates with
Molex Elec Connector
Digikey PN# WM2914-ND

Mates with Micro MATE-N-LOK
Digikey PN# A30297-ND

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Title Tethys Load Controller Interface				Number <Doc>
Size C	Engineer Chad Key	Layout <Layout Eng.>	Filename <Filename>	Rev A
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Title	Tethys Spare Circuits			Number	<Doc>
Size	Engineer	Layout	Filename	Rev	
C	Chad Key	<Layout Eng.>	<Filename>	A	
Date: Wednesday, June 10, 2009		Sheet	13 of 13		