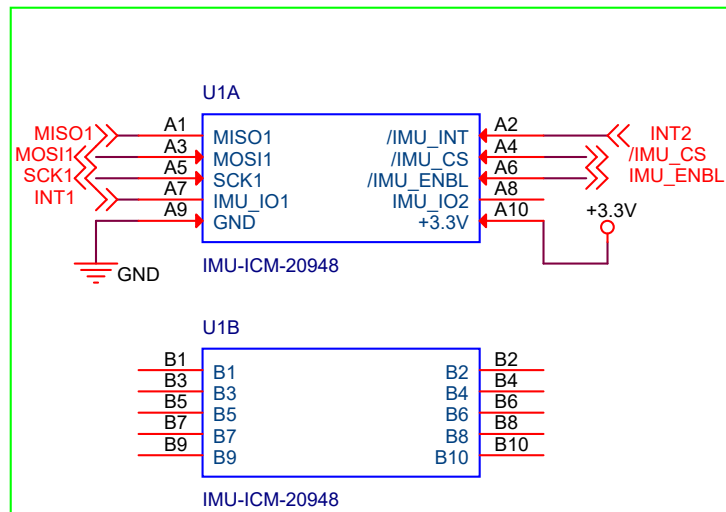
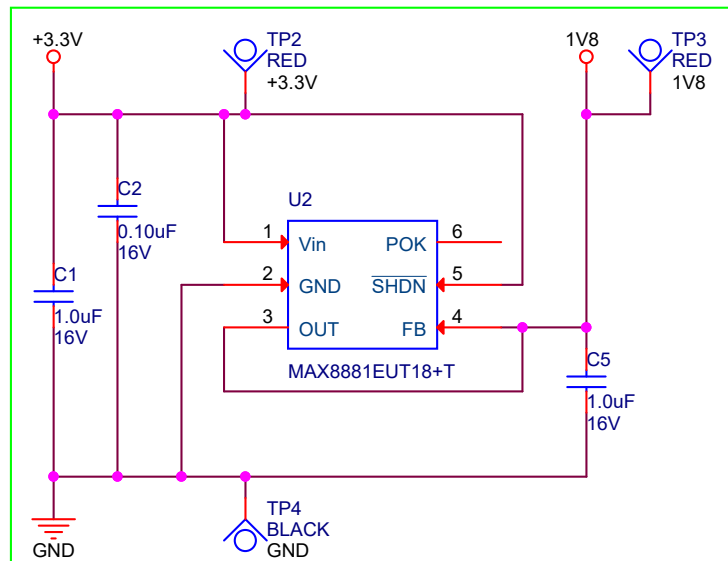
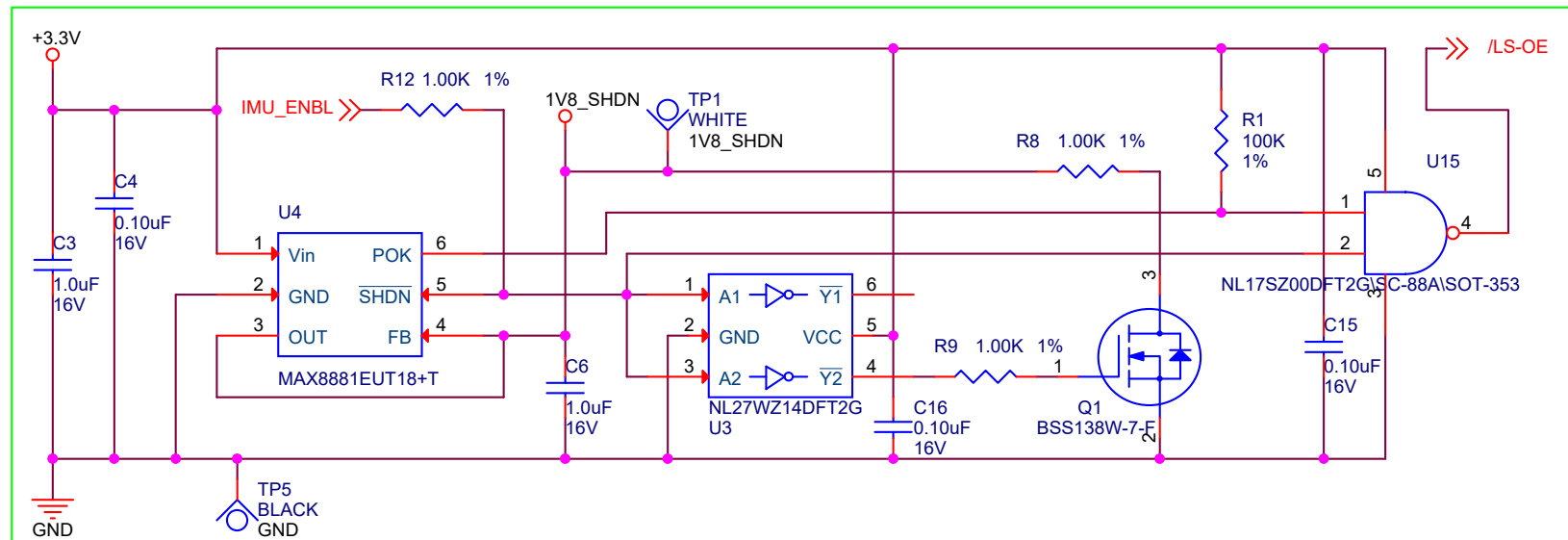




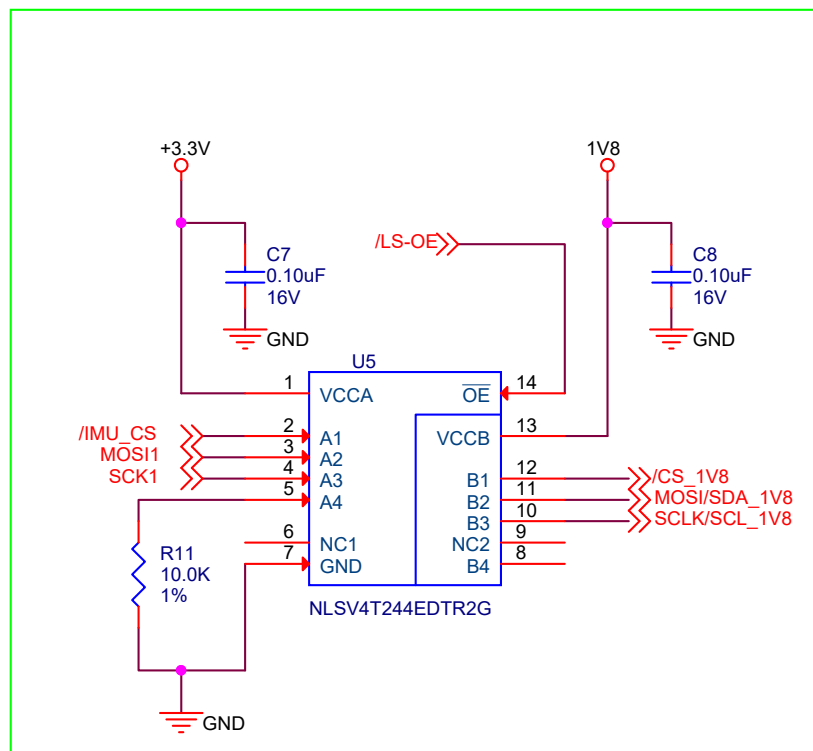
10020895-M0B3 interface connectors



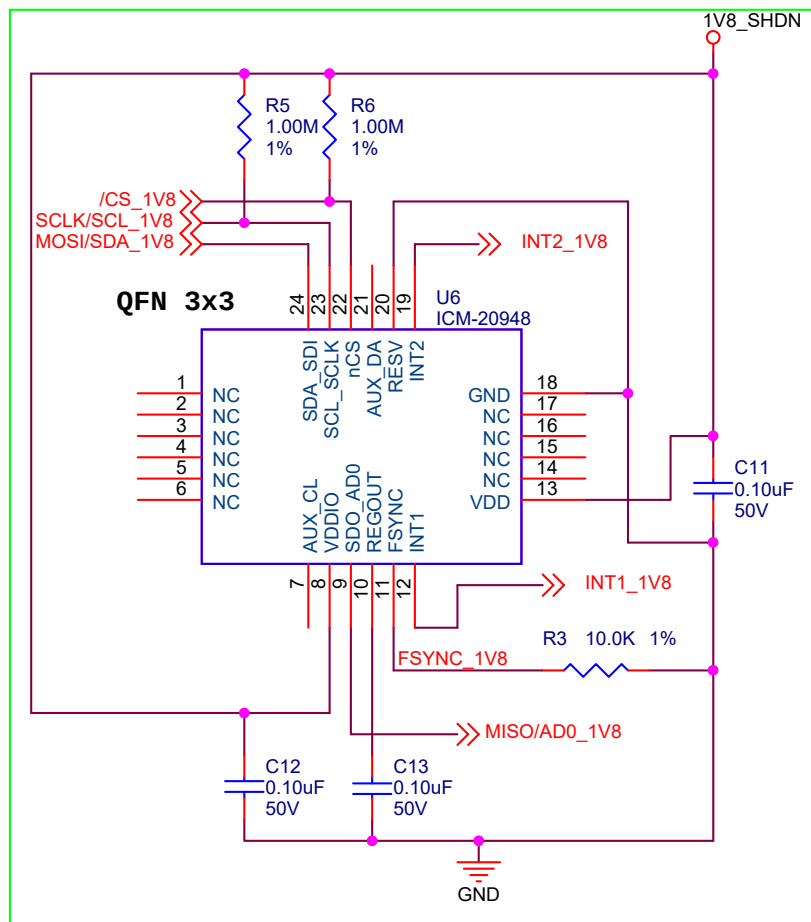
1V8 power, always ON



1V8_SHDN power, controlled by /IMU_ENBL signal. POK signal is generated when 1V8_SHDN power drops to 90% of nominal

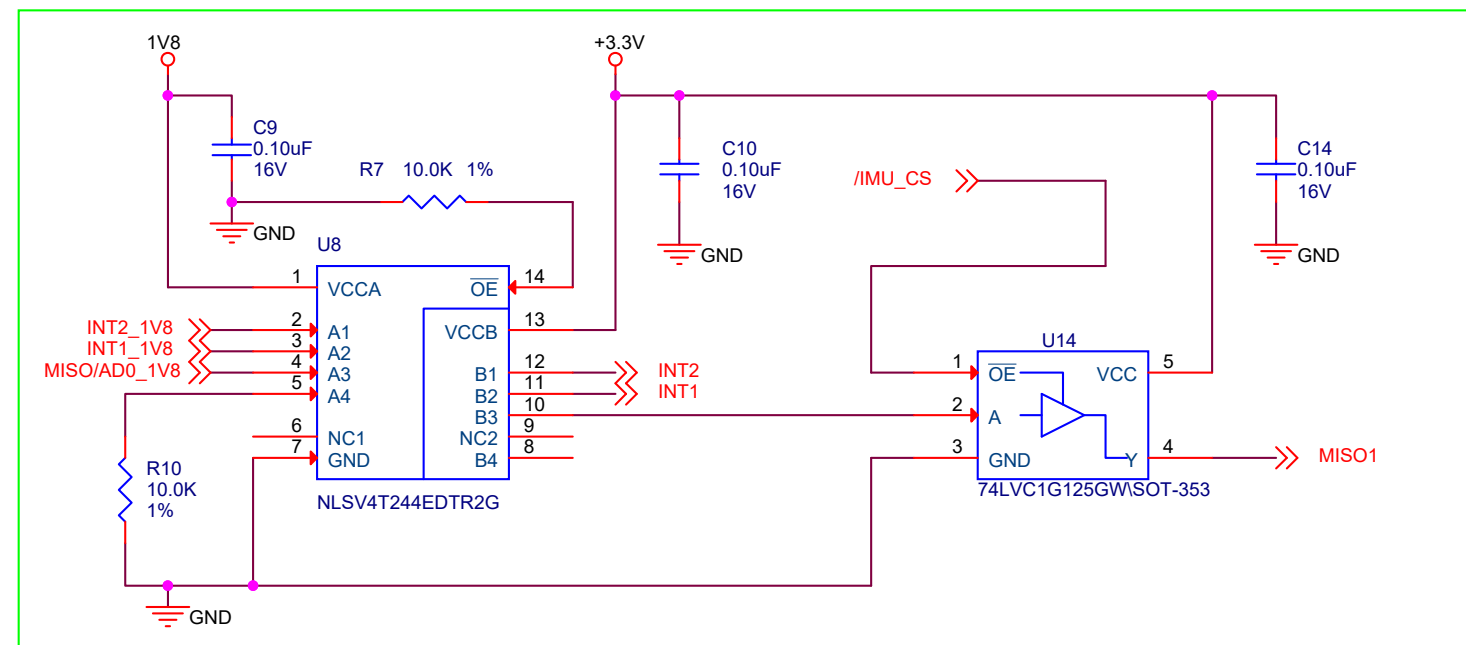


Level shifter 3.3V > 1.8V
Always ON
Outputs are controlled to tri-state
when 1V8_SHDN goes to 90% of
nomimnal



IMU powered by 1V8_SHDN power
(can be shut down)

ICM-20948 currents at 1.8V VDD/VDIO
3.11mA in 9-Axes mode
1.23mA gyro only
68.9 uA accelerometer only
90 uA magnetometer only
8 uA full chip sleep mode



Level shifter 1.8V > 3.3V. This part is always-on.
Output buffer enable MIS01 on /IMU_CS signal only

DRAFT

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831-775-1914		Project: 901023	
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Design Modify Date (auto):		Monday, March 09, 2020	1 of 1
Full path (auto):			
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