

Tips & Tricks
Allegro PCB Editor
April 2011

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Tips & Tricks – Allegro PCB Editor

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Allegro Tips and Tricks - SPB 16.3

This document provides Tips and Tricks recommended by Ed Hickey of Cadence.

Reducing Mouse Clicks & Travel

Tips to help reduce the number of mouse clicks & travel to the options panel.

Adding Vias – Use of Space Bar

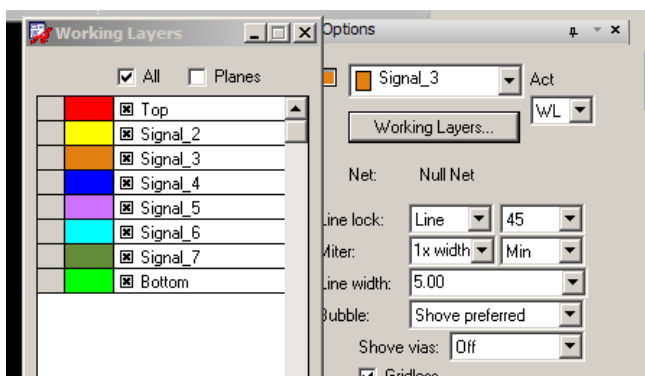
Adding a via has traditionally been done with a double click of the LMB. The use of the space bar can save you 1000's of mouse clicks per year. I suggest adding the function key below to your local env file. The spacebar entry is represented by “ ”. Other keys can be assigned but space bar is easy to click without looking down.

```
funckey " " "pop bbdriill -cursor"
```

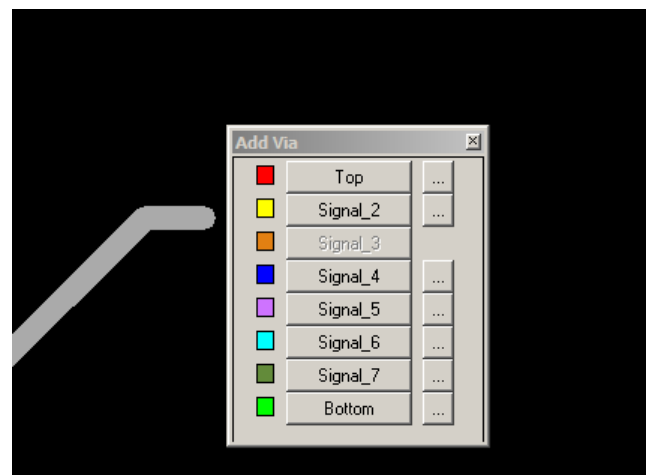
Adding Vias – “Working Layer” model

The Working Layer “WL” model to add vias is now available (16.3) in all backend products. It can be used to add conventional through-hole or staggered/stacked HDI vias. Select your target routing layer from a popup GUI that appears adjacent to the via insertion area.

- Order the vias in each Physical Cset
- Click on the “...” to access alternative (least preferred) vias



In Add Connect – Select ‘WL’



Select Target Layer during routing

Add Connect – Single click execution

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In Etch Edit Application Mode, make a single pick on a pin, via or rat to begin Add Connect. Ensure “Enable Single Pick Execution” mode is enabled. Access this option from the RMB – Customize menu.

Add Connect – Add a vertex using a Funckey key

Instead of using the LMB to add a vertex point during Add Connect, consider using a Function Key. Simply click the X key every time you want to add a vertex during routing.

```
funckey x "pick_to_grid -cursor"
```

Add Connect – Toggle between line-types of line and arc during route

```
alias a1 'FORM mini lock_mode Arc;FORM mini lock_direction 45'  
alias a2 'FORM mini lock_mode Arc;FORM mini lock_direction 90'  
alias a3 'FORM mini lock_mode Line;FORM mini lock_direction 45'  
alias a4 'FORM mini lock_mode Line;FORM mini lock_direction 90'  
alias F2 'settoggle CMD a1 a2 a3 a4;$CMD'
```

Slide – Single Click Execution

In Etch Edit Application Mode, make a single pick on a cline segment to slide it.

Change Active/Alternate Layers using Function Keys

Increment or decrement the active or alternate layer

```
funckey + subclass -+ (use of “+” character to increment active subclass)
```

```
funckey - subclass -- ( use of “-” character to decrement active subclass)
```

```
funckey a altsubclass -+ (use of “a” character to increment alternate subclass)
```

Directly change the active layer

```
funckey 1 options subclass TOP
```

```
funckey 2 options subclass SIGNAL_2
```

```
funckey 3 options subclass SIGNAL_3
```

```
funckey 4 options subclass SIGNAL_4
```

```
funckey 5 options subclass SIGNAL_5
```

```
funckey 6 options subclass SIGNAL_6
```

```
funckey 7 options subclass SIGNAL_7
```

```
funckey 8 options subclass BOTTOM
```

Another method to change the active subclass is from the RMB:

RMB – Quick Utilities – Change Active Subclass

Deleting Elements using a Function Key

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I think I use this function key more than any other one. Just pass your cursor over a cline, segment, via, text or shape and click “d” to delete it. No click of the mouse!

```
funckey d "prepopup; pop dyn_option_select @:@Delete"
```

Moving Components – Single click execution

In Placement Application Mode, make a single LMB pick on a component to move it.

Rotating a Component using a Function key

Press the function key R to rotate a component during movement

```
funckey r iangle 90
```

Mirror a Component using a Funckey Key

Press the function key M to mirror a component during movement

```
funckey m "pop mirror"
```

Snapping using a Funckey Key

While moving an object, use a function key to snap to various elements.

```
funckey f "prepopup;pop dyn_option_select 'Snap pick to@:@Figure'"
```

```
funckey i "prepopup;pop dyn_option_select 'Snap pick to@:@Intersection'"
```

```
funckey c "prepopup;pop dyn_option_select 'Snap pick to@:@Arc/Circle Center'"
```

```
funckey v "prepopup;pop dyn_option_select 'Snap pick to@:@Via'"
```

Snap a Rat T to a Pin/Via

While moving a Rat T, try setting rotation point to “Body Center”



Alias commands to the Middle Mouse Wheel

The “button” command can be used to alias the Middle Mouse Wheel to commands; works with SHIFT, CONTROL and SHIFT-CONTROL combinations.

Examples:

```
button Swheel_up subclass -+
button Swheel_down altsubclass -+
button Cwheel_up "roam y -$roamInc"
button Cwheel_down "roam y $roamInc"
button SCwheel_up "roam x -$roamInc"
button SCwheel_down "roam x $roamInc"
```

In Constraint Manager, those of us with “older eyes” can use the <Control> Middle Mouse Wheel to increase the font size.

Replacing Padstacks by window selection

In General Edit Application Mode, window around the set of pins or vias you wish to change then use the RMB – Symbol Pin or Via – Replace Padstack – Selected Instances command.

Changing Net Names on Vias

A skill application called “change_net_on_vias.il” is located in your install directory - /share/pcb/examples/skill/cmds

Polygon selection window

If a polygon selection window is desired;

RMB – Selection Set – Select by Polygon

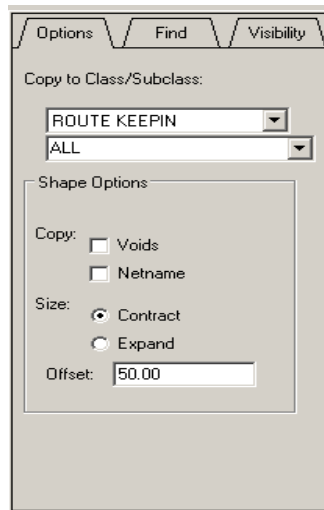
Z-Copy – a powerful utility

This is a basic function that everyone should know.

Create a Route Keepin area derived from the board outline

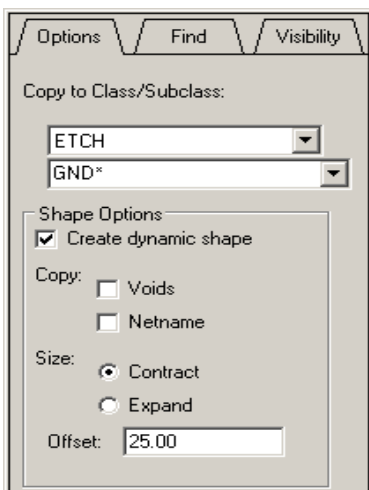
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Select Edit Z-COPY, adjust the options settings to class = ROUTE KEEPIN, Subclass = ALL. Enable 'contract' then enter value the route keepin will be offset from the board outline. Last step is to select the board outline.



Did you know Z-Copy can be used to copy to multiple layers at once?

For example you wish to create multiple GND planes but the names of each subclass end in GND_5, GND_8, GND_13, etc. Use Z-Copy then in the options tab select one of the GND layers. Edit the numerical part of the layer then enter the wildcard '*' as shown in the graphic below.



Fix/Unfix Elements in the Design

Tips to add and remove the Fixed Property

Quickly unfix all elements

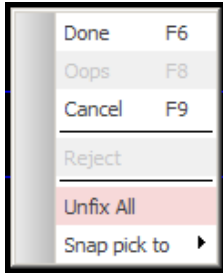
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The quickest method to remove the fixed property from all elements in the Design is to:

1. Click the Unfix Icon

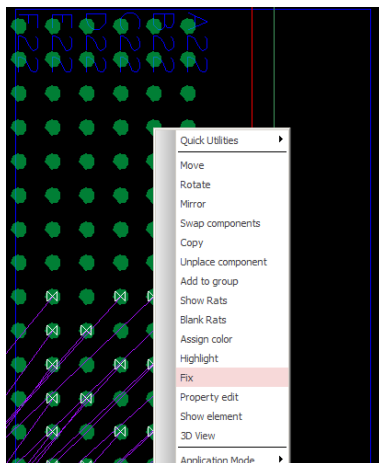


2. Then RMB – Unfix All



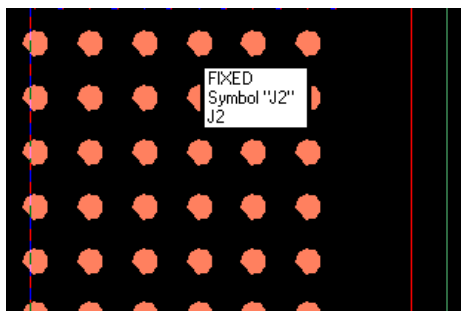
Fix a Symbol's location

Simply mouse over the symbol then select the “Fix” command from the context sensitive RMB menu.



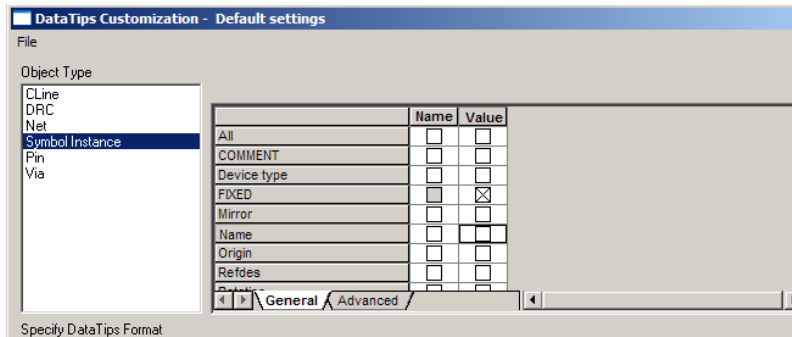
Datatip display of the Fixed property

You would like to know whether an element is fixed while hovering over it.



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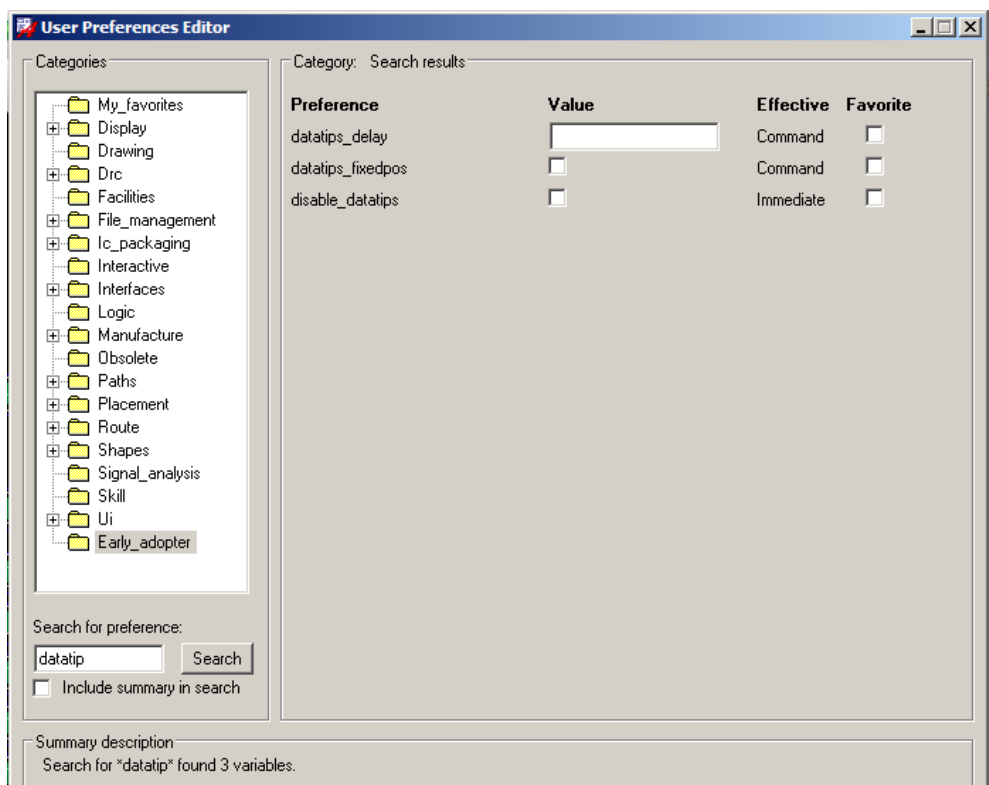
Configure the datatip setting as shown below. You may want to enable other symbol related properties to also display in the datatip window. This is done from the Setup – Datatip Customization menu. Select symbol Instance under Object Type then enable the value “FIXED”.



Datatip Options

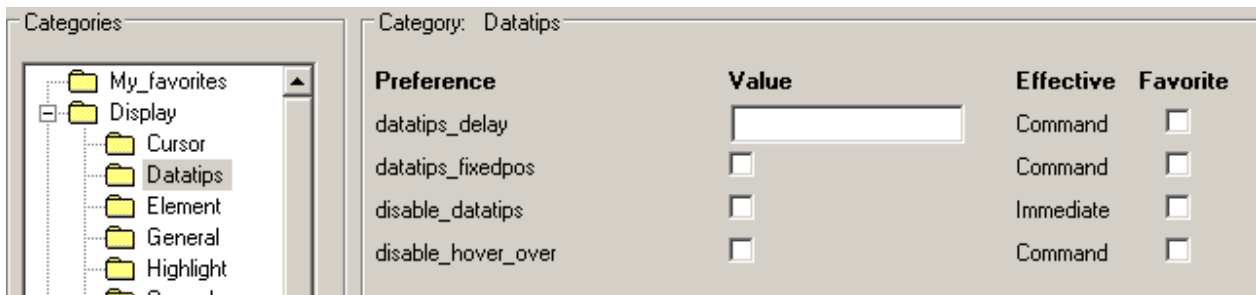
Controls for delay, location and disablement

The User Preference Editor contains hundreds of variables. The “Search” box is a very handy mechanism. In this example, I type in the word “datatip” then click Search. Variables to delay, locate and disable datatips are available to you.



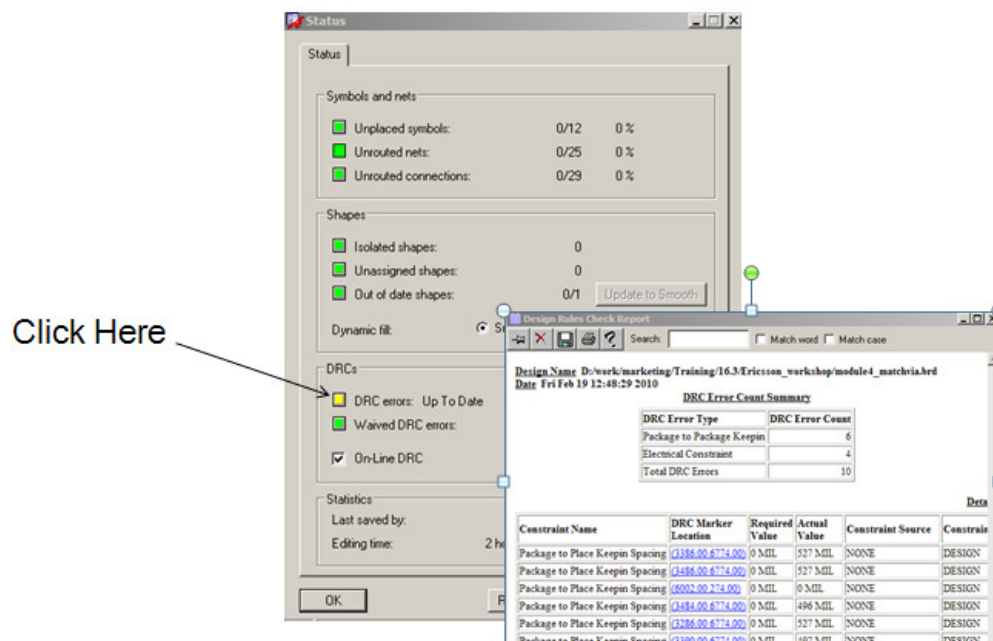
The complete set of variables related to Datatips is located in the Display-Datatips Category

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Status Form traffic lights

Did you know you can click on the indicators to obtain a report?



“Go to” X,Y Location

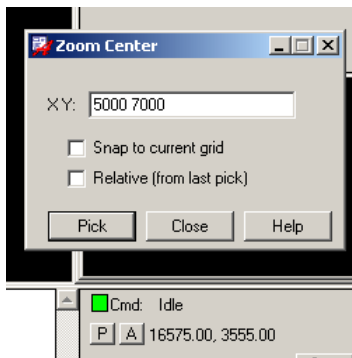
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Navigate to an X,Y location in the Design

You wish to quickly go to a specific X, Y location in the Design; for example go to location x = 5000, y = 7000.

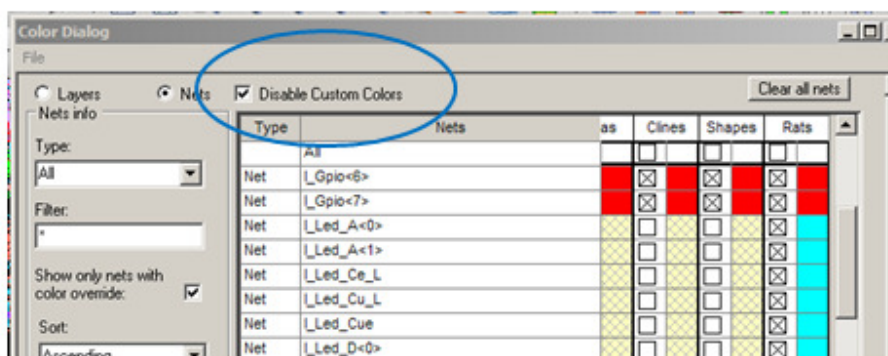
Adjust your zoom level then make a click on the 'P' button located at the base of the canvas display. Enter in your coordinates then click "Pick"

Note – Please set application mode = none for this behavior



Disabling Custom Color Assignments

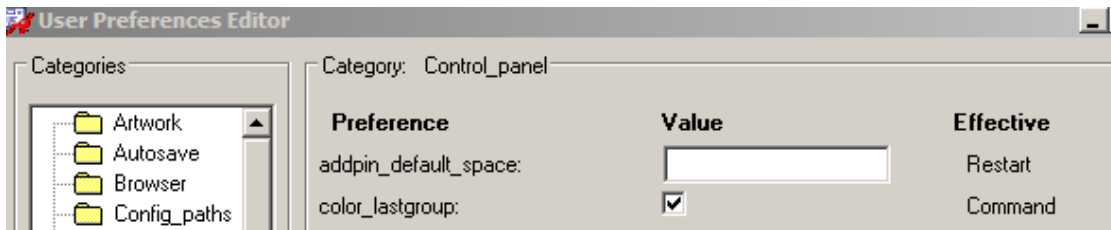
You may inherit a board from another designer or shop and question some of the color assignments. One quick method to disable color overrides that may have been applied to nets, buses, Diff Pairs is to go into the color form. If you select the "Nets" radio button on top, you will see a new button called "Disable Custom Colors." Select that option to revert to "layer" based coloring.



Color Dialog – Open last folder used

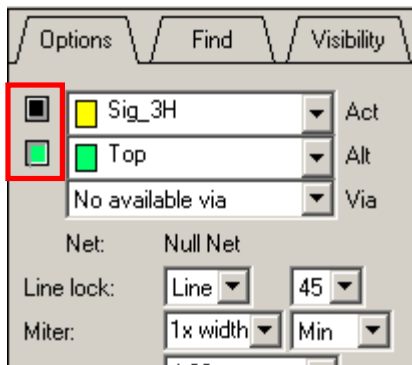
Set the variable "color_lastgroup" located in user preference - UI - control_panel

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Display a Layer

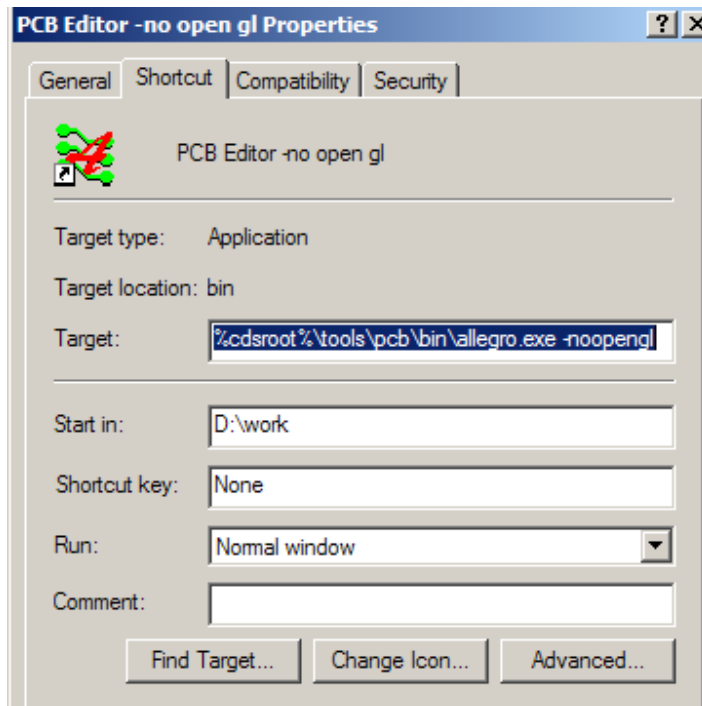
Click the color switch box to the left of the subclass to quickly turn layers on/off during a command.



Invoking Allegro in No OpenGL mode

It may be necessary to run Allegro in No OpenGL mode; for example, hosting a Net Meeting. Consider creating a Desktop Shortcut for Allegro.exe with the `–noopengl` option.

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Display of Properties in the Canvas

Any property attached to an element can be displayed in the canvas. From the Display – Property menu, select the Graphics tab then select properties to display from the left column. Clicking the Create button writes out the property values and/or their name to the subclass properties.

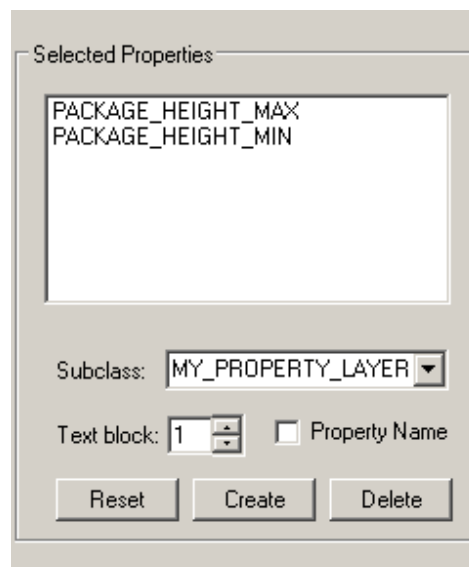


Figure 6-10 shows a photograph of a printed circuit board (PCB) populated with various electronic components. The components are labeled with their part numbers and dimensions. Key components include:

- C802**: 40 MIL capacitor.
- C252**: 69 MIL capacitor.
- R421**, **R900**, **R987**: 40 MIL resistors.
- R988**, **R989**, **R990**: 40 MIL resistors.
- R991**: 69 MIL resistor.

The PCB features a green solder mask and several through-hole components mounted on it.

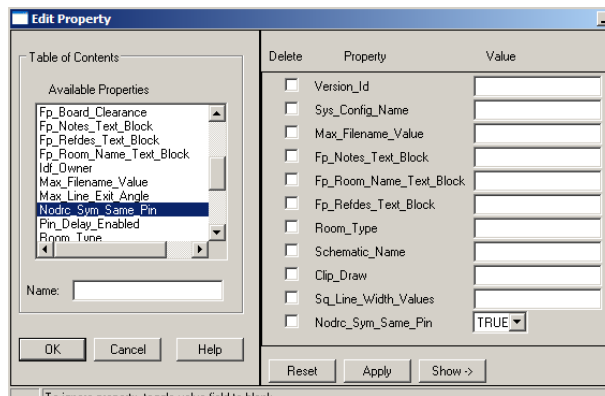
PCB Designers often use Constraint Regions to suppress pin to pin violations within the same symbol. The property “nodrc_same_sym_pin” can be applied to the symbol but did you know it can also be applied to the drawing? Applying it at the drawing level has a global impact to all symbols.

-
- Find by Name or Property**
- Object type: **Drawing**
- Available objects
- Name filter:
- Value filter:
- Drawing Select
- All->
- <-All
- Selected objects
- ☐ Use 'selected objects' for a deselection operation
- OK Cancel Apply Help
- Find By Name**
- Drawing** Name
- > More...

-
- Find by Name or Property**
- Object type: Drawing
- Available objects
- Name filter: *
- Value filter: *
-
- All->
<-All
- Selected objects
- DRAWING SELECT
- ☐ Use 'selected objects' for a deselection operation
- OK
Cancel
Apply
Help

- Product Revision 16.3

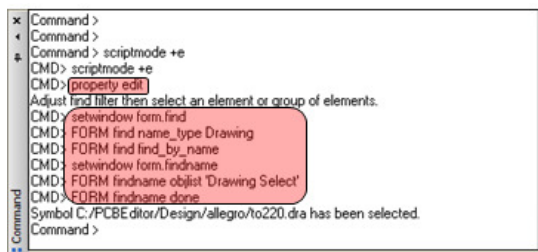
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Creating a Command Shortcut with Scriptmode +e

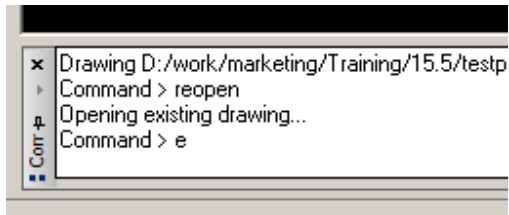
Adding a drawing level property in the previous tip was labor intensive. Let's create a shortcut.

1. Enter scriptmode +e in the command window. This echoes the commands you enter.



2. Copy all the commands above to a single line
3. Separate with semi-colons
4. Map to a function key. When using multiple commands in a function key or alias they must be surrounded by double quotes.
5. funckey e "property edit;setwindow form.find;FORM find name_type Drawing;FORM find find_by_name;FORM findname objlist 'Drawing Select';FORM findname done"
6. Click the "e" key then press "ENTER" in the command window to launch the property editor dialog!!

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Scriptmode +i

"scriptmode +i" is really a shorthand for "scriptmode +invisible"

Let's say you want to create an alias for toggling end cap display but don't want to see the parameter form appear while executing the script.

```
alias ec "scriptmode +i; prmed; FORM prmedit display_enhance toggle; FORM prmedit done"
```

DFA Dynamics – Controlling spacing bubble resistance

Issue - When placing components to DFA rules, it's difficult to pull back the component when the bubble appears.

The resistance can be controlled by the "dfa_pause_level" variable located in user preference – DFA_DRC. Set to 0 for no resistance.

“Locked” property

The Locked property was designed for Re-Use modules but did you know

The property can be applied to a place replicate circuit to prevent accidental movement of its members.

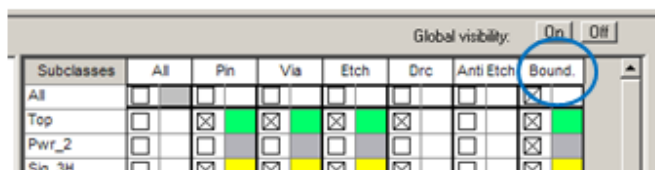
It can also be applied to a package symbol to prevent editing/deletion of the assembly outline or other data elements.

Suggestion – consider applying the locked property to the symbol definition (at the library level).

Shape has “No Etch” Status – find it

A dynamic shape is "out of date" BUT has a "No Etch" status.

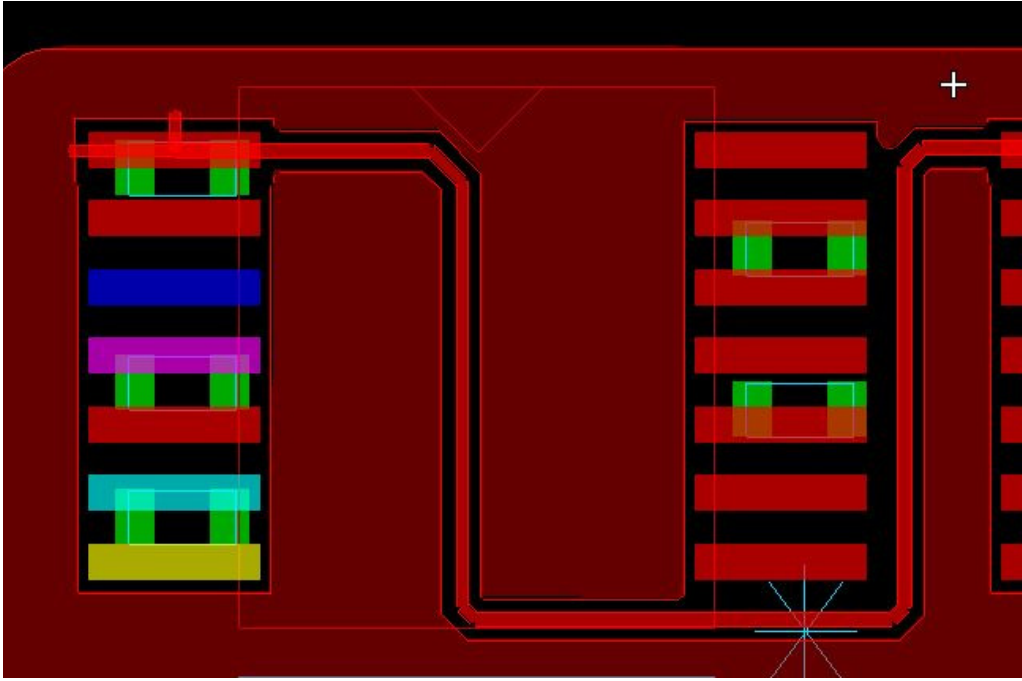
You cannot find the shape in question. It says it's at a particular XY location, on a particular layer, but there is nothing there. Turn on the “Boundary” color item for that layer to locate the shape outline.



Voiding of GND clines through GND Plane

A method to auto-void GND sense lines being routed through a GND plane.

In the example below, a GND net is routed through a GND shape. The property 'void_same_net' was applied to the cline.



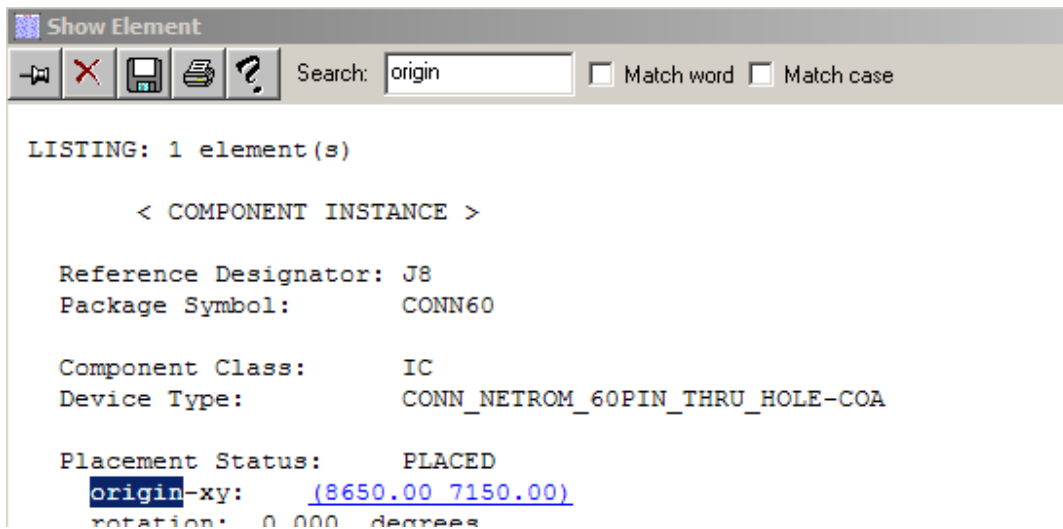
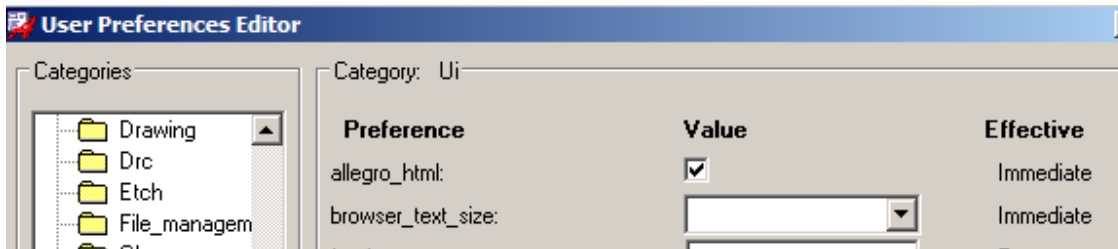
How to convert non acute angles for shapes to round corners?

This feature is available in 16.3. Go to the dynamic shapes parameter and set the Acute angle control to "Full Round". Static shapes get the same capability in a future ISR with the eap_static_newsmooth env variable.

HTML Reports

Reports and messages can be displayed in HTML format when the variable 'allegro_html' is set. One advantage of using HTML is the ability to search through the report.

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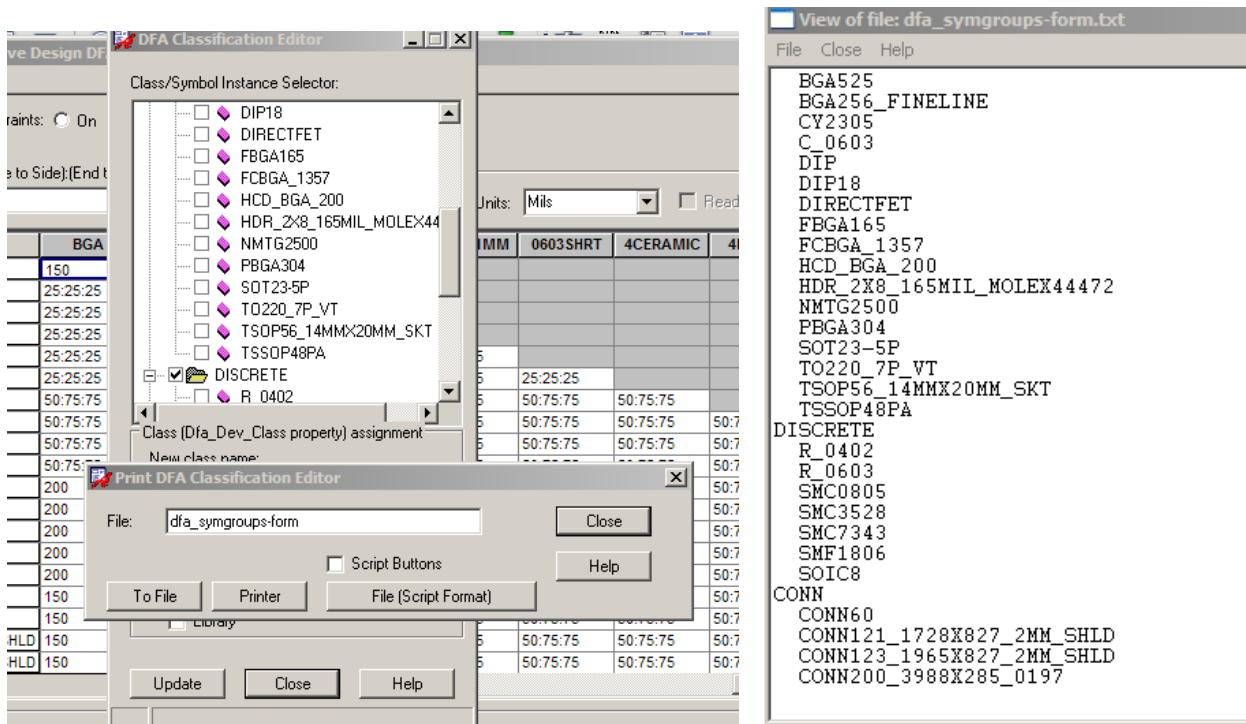


Control P “Print form”

Did you know Control P can be used to print a form?

For example, inside of the DFA Symbol classification window click Cntrl-P to open up the Print form window then hit the "To File" button to save the information to a text file.

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Control P the click “to file”

example of file

Adding Text (from a file)

Tip to add text from a .txt file on disk

- Run the add text command: Add > Text
 - Set the desired text options on the Control Panel “Options” tab
 - Set the Active class to Board Geometry
 - Set the Subclass to Dimension (or any other subclass)
 - Set Marker Size:
 - Set Rotation:
 - Set Text Block:
 - Set Text Justification:
- Position the cursor/Left Mouse Button (LMB) at the location that you want the first line of text to start.
- Right Mouse Button (RMB) and choose: Read from File

Method to renumber all ref des from a starting number

Use the variable `fst_ref_des`. You must ensure that the refdes digits in the Sequential Renaming section of the UI allows for the number of digits in the `fst_ref_des` variable.

** Variable located in User Preference – Manufacturing - Silkscreen

Why does the system react slowly when moving a comp?

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Most likely your large pin-count nets are scheduled Min Tree. Add a voltage property or RATSNEST_SCHEDULE = POWER_AND_GROUND to large pin-count nets like Vcc and GND.

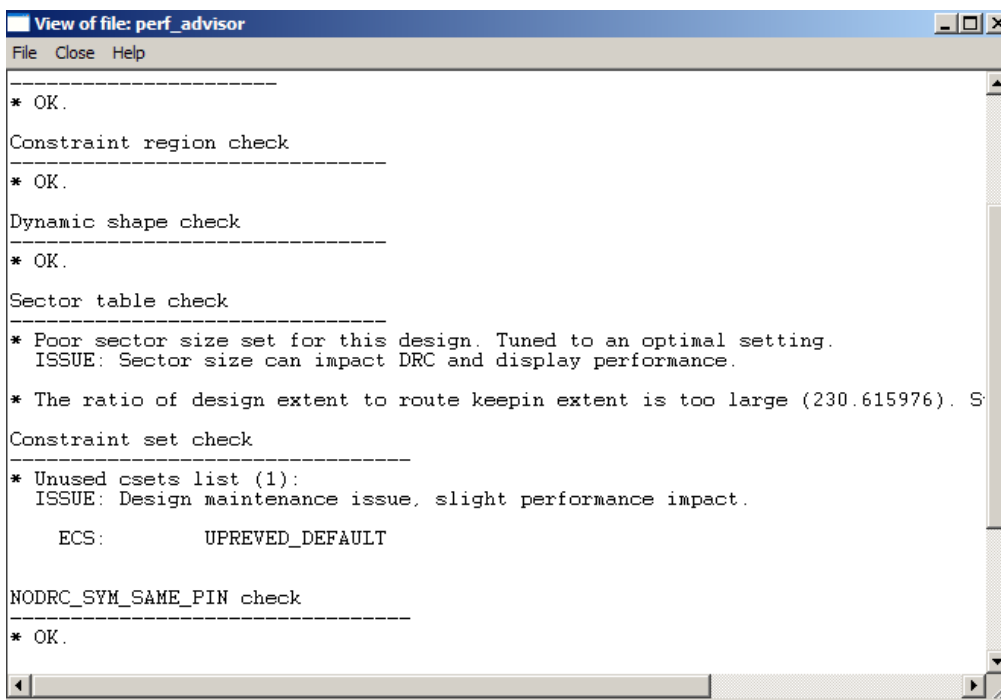
Why does DRC Update take hours to run?

Usually the slower checking is a result of a large number of constraint areas and/or a large number of overlapping constraint areas. If that is not the case, then it may be the larger spacing values that you have to satisfy your high voltage requirements or simply fudge values entered as no-ops. Large spacing values result in more neighboring items being examined for each item being checked.

** Review your extra large spacing values. I have seen the value of 999 entered on many occasions. When reduced, DRC update went from 1 hour to 3 minutes.

Is my Database Optimized for Performance?

The performance advisor provides suggestions to increase database performance. The command can be run from Tools – Database Check – Performance Advisor



Read Only Variables

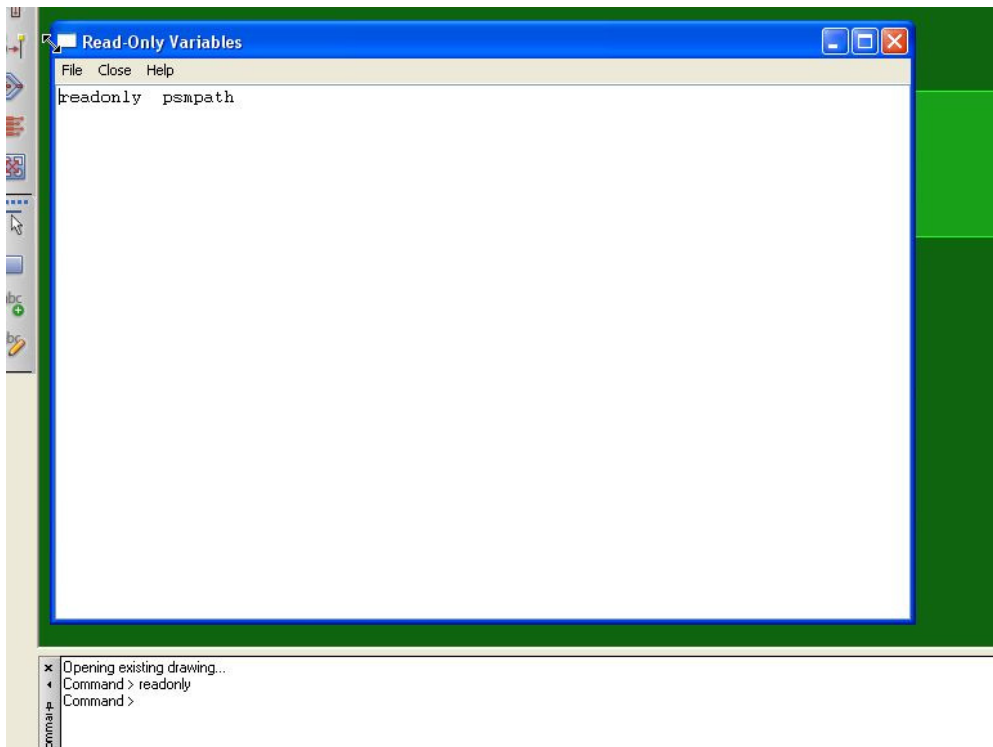
A method for a site administrator to make variables read only

Add a "readonly" entry under the respective variable. In the example below, I wish to prevent the end user from modifying PSMPATH. The command readonly psmpath is added just below the "set psmpath" variable.

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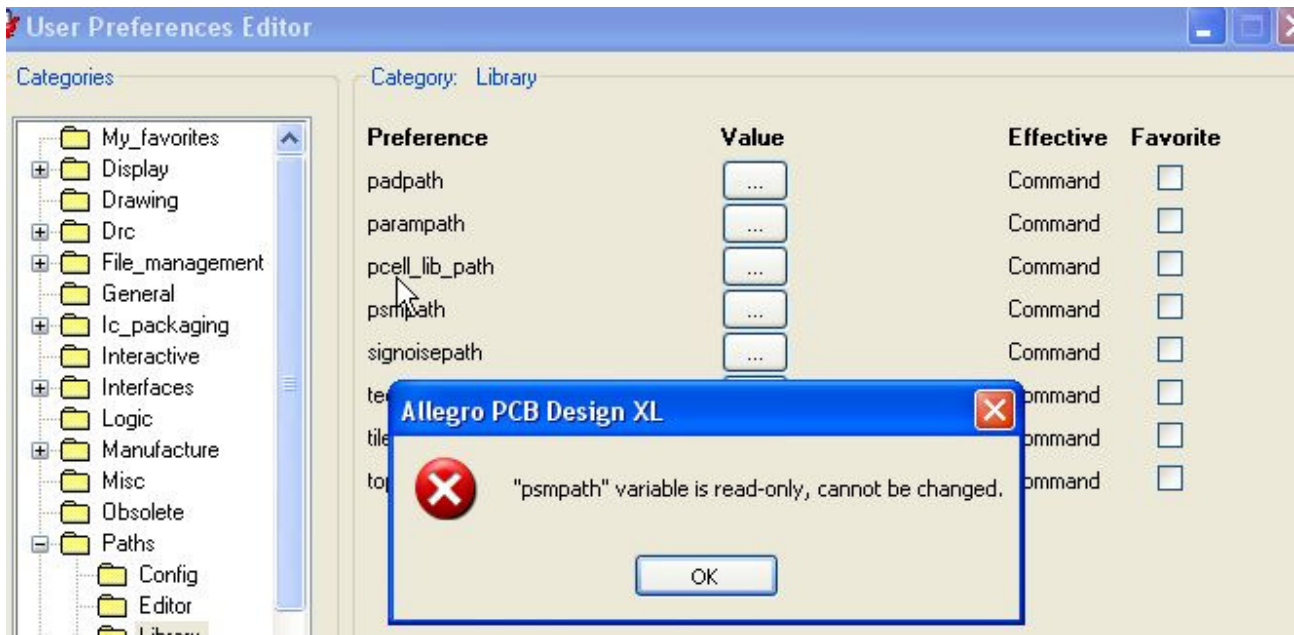
```
unset datatips_delay
set psmppath = D:\work\customers\IBM\ D:\work\marketing\Training\16.2\symbols\
readonly psmppath
set padpath = D:\work\marketing\Training\16.2\symbols\ D:\work\customers\IBM\
unset allegro_etch_length_on
set accon_olddb1t = all
```

To verify in Allegro, type `readonly` at the command line prompt. The window lists variables that are readonly



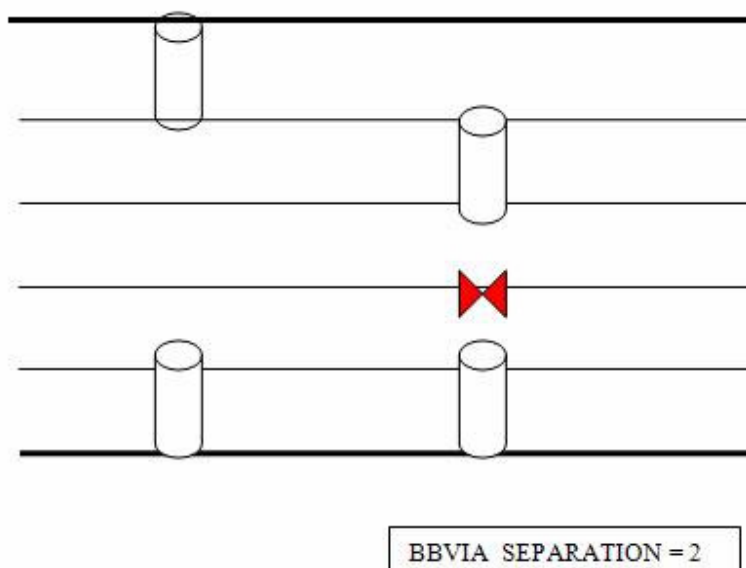
When an attempt is made to change `psmpath` in the user preferences editor, the following warning will appear:

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BBVIA GAP DRC reporting too many DRCs

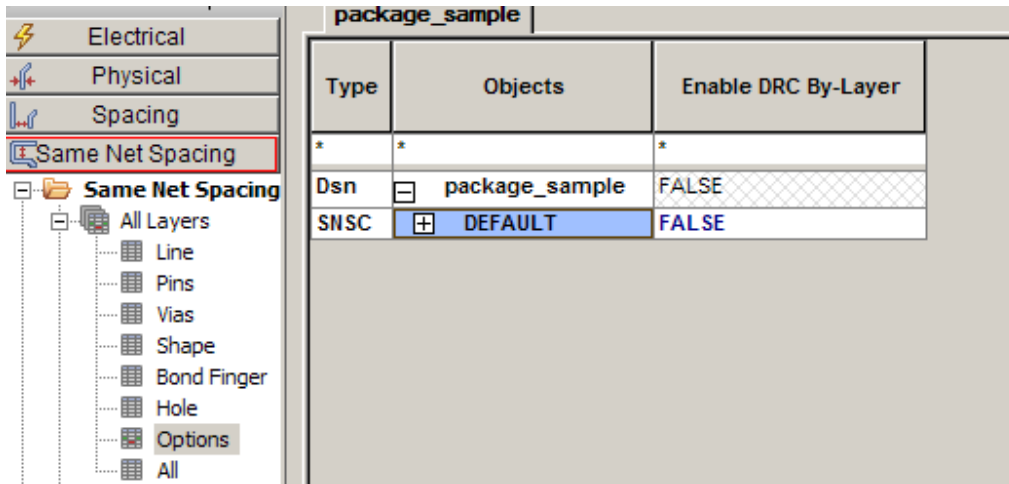
Under normal circumstances, Buried/Blind vias separated by less than the minimum bbvia gap rule will generate a DRC no matter how many layers separate them. A drawing level property, BBVIA_SEPARATION, suppresses the DRC if the vias are separated by the specified layer span value or more. In the example below, the property value of "2" suppresses the DRC on the left side.



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Same Net DRCs not being generated?

Most likely your “DRC by-layer” setting is set to “False”



Working with Xnets

Tips for working with Xnets

Create them – Assign Espice models to the discrete devices from the Analyze – SI/EMI Sim - Model Assignment menu.

Prevent them on an Instance basis - Assign the property no_xnet_connection at the component level.

Display them – In CM, Go to Object – Filter and enable “net” to see the nets of the Xnet

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Type	Objects	Referenced Electrical C Set	Topology				Stub Leng	
			Verify Sched	Schedule	Actual	Margin	Max mil	Actual mil
FLTR	*	*						
Dsn	module4_matchvia							
NCIs	DP_CLASS (6)	DP						
Bus	BUS1 (8)							
XNet	LONG_SIDE							
Net	LONG_SIDE							
Net	SHORT_SIDE							

Filter

Object types filter:

- ☒ Design
- ☒ XNet
- ☒ Net
- ☒ Diff Pair
- ☒ Bus
- ☒ Net Class

Advanced filters:

- ☐ Selected nets/xnets only
- ☐ Highlighted nets/xnets only
- ☐ Failed only
- ☐ Constrained only
- ☐ Active partition only
- ☒ Active DRCs
- ☒ Waived DRCs

Naming Convention – The Xnet name defaults to the Net name with the lowest alphanumeric character. In the above example, L is before S.

Change the Xnet Name – In CM, select the Xnet then use RMB – Rename

Type	Objects	Referenced Electrical C Set	Verify Sched	Schedule	Actual	Margin	Max	Actual	Margin	Max	Actual
							mil	mil	mil	mil	mil
FLTR	*	*	*	*	*	*	*	*	*	*	*
Dsn	module4_matchvia										
NCIs	DP_CLASS (6)	DP									
Bus	BUS1 (8)										
XNet	LONG_SIDE										
Net	LONG_SIDE										
Net	SHORT_SIDE										

Rename XNet LONG_SIDE

XNet name:

SHORT_SIDE

OK

Cancel

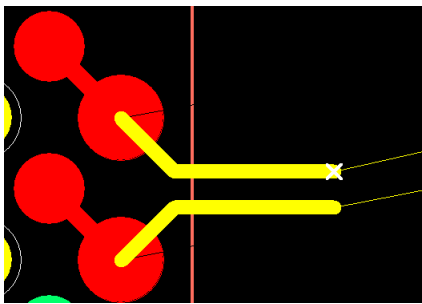
Diff Pair Gathering

Looking for tighter coupling at the gathering location?

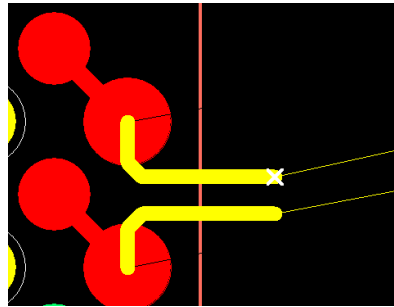
Decrease the value of the “padentry_factor” variable located in ETCH category of the User Preference Editor.

padentry_factor:	10	Immediate
rats_factor:		Command
set off if connected:		Default

Tips & Tricks – Allegro PCB Editor



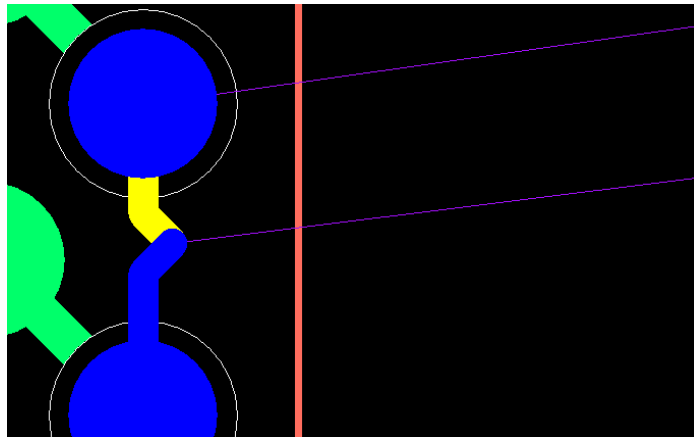
Default



Padentry set to 10

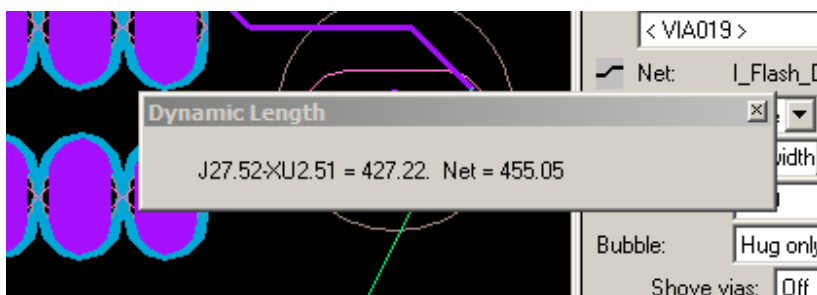
Route a Tandem Diff Pair

Pre route each member as shown in the graphic below then use add connect to route as a tandem pair. This assumes a Diff Pair constraint object has been created.



Display Length Meter for non constrained nets

Set the variable “allegro_etch_length” located in user preference > etch category

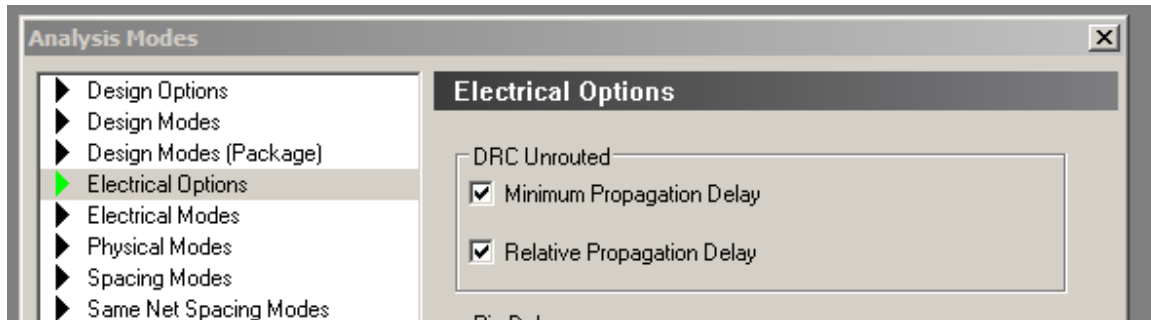


Constraint Manager

A few tips related to Constraint Manager

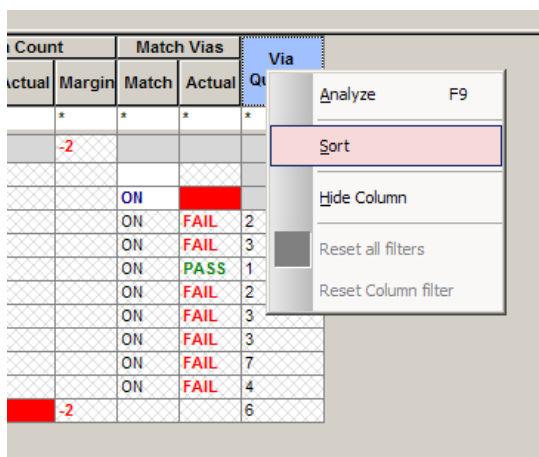
You wish to display propagation delay analysis for unrouted nets in CM.

Enable the “unrouted” modes in Analyze > Analysis Modes > Options Tab



Sort results from worst case to best case margin

Double Click Margin Column Header or RMB Sort



Display just the Failed Results

Click Object Header then RMB > Filter, enable failed only. Or simply click on this Icon



Display just nets that have constraints on them?

Click Object Header then RMB > Filter, enable constrained only. Or simply click on this Icon



Tips & Tricks – Allegro PCB Editor

Segregate sections of CM with divider lines, i.e. identify the transition between buses and matched groups.

In CM, View > Options > Enable ‘Object Type Dividers’ or simply click on this Icon



Type	Objects	Referenced Electrical CSet	Via Count			Match Vias		Via Quantity
			Max	Actual	Margin	Match	Actual	
*	*	*	*	*	*	*	*	*
Dsn	☐ module4_matchvia				-2			
NCIs	☒ DP_CLASS (6)	DP						
Bus	☐ BUS1 (8)					ON		
Net	AD0					ON	FAIL	2
Net	AD1					ON	FAIL	3
Net	AD2					ON	PASS	1
Net	AD3					ON	FAIL	2
Net	AD4					ON	FAIL	3
Net	AD5					ON	FAIL	3
Net	AD6					ON	FAIL	7
Net	AD7					ON	FAIL	4
XNet	☒ LONG_SIDE				-2			6

You wish to change a constraint, min line width for example, across a certain layer of all available PCSETS.

Consider working in the “By Layer” worksheet to change the Top layer constraint across all available Csets

Type	Objects	Line Width		Neck		Min Line Spaci	Primary Ga
		Min	Max	Min Width	Max Length		
		mil	mil	mil	mil	mil	mil
*	*	*	*	*	*	*	*
Dsn	☐ module4_matchvia	5.00	0.00	0.00	0.00	0.00	0.00
Lyr	☐ TOP	5.00	0.00	0.00	0.00	0.00	0.00
PCS	DEFAULT	5.00	0.00	0.00	0.00	0.00	0.00
PCS	DP_DR	6.00	0.00	3.90	1000.00	3.80	6.00
Lyr	☒ SIGNAL_2	5.00	0.00	0.00	0.00	0.00	0.00
Lyr	☒ SIGNAL_3	5.00	0.00	0.00	0.00	0.00	0.00
Lyr	☒ SIGNAL_4	5.00	0.00	0.00	0.00	0.00	0.00
Lyr	☒ SIGNAL_5	5.00	0.00	0.00	0.00	0.00	0.00
Lyr	☒ SIGNAL_6	5.00	0.00	0.00	0.00	0.00	0.00
Lyr	☒ SIGNAL_7	5.00	0.00	0.00	0.00	0.00	0.00
Lyr	☒ BOTTOM	5.00	0.00	0.00	0.00	0.00	0.00

Export to Excel

Constraint Manager can export to a native excel spreadsheet (Windows Only & you must have Excel installed)

Tips & Tricks – Allegro PCB Editor

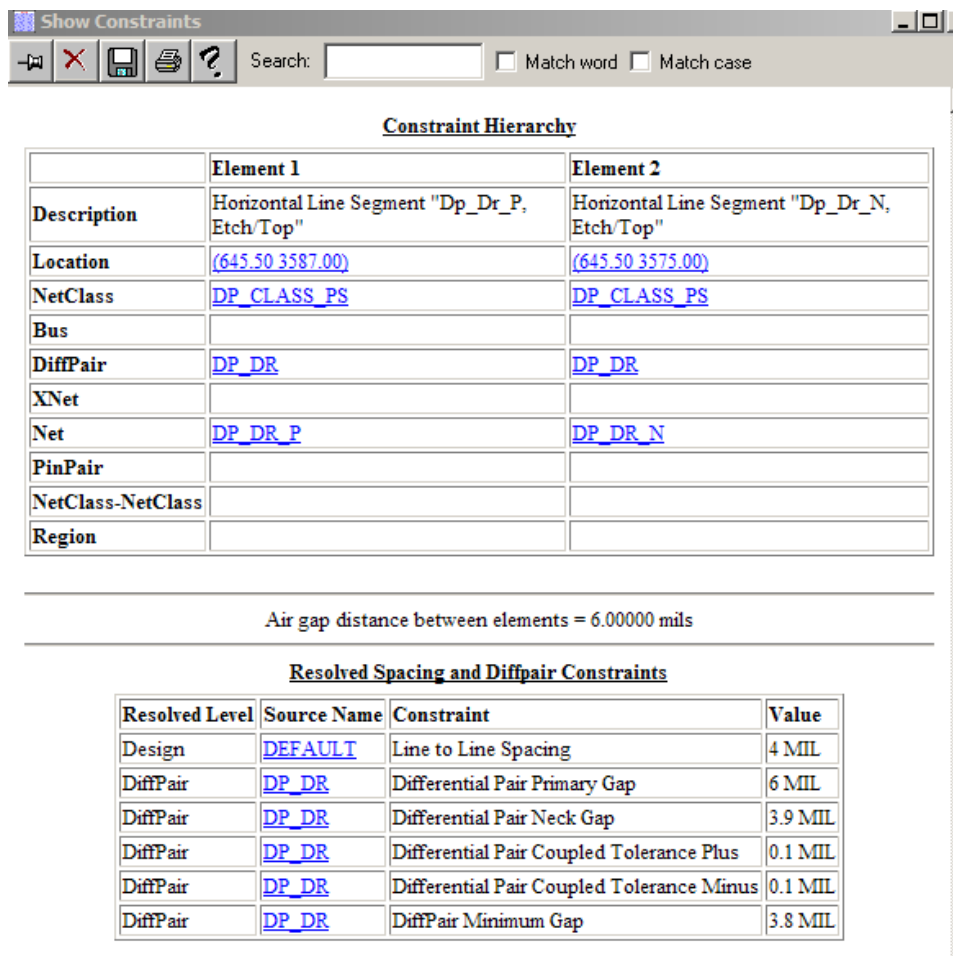
In CM: Tools – Excel – Active Worksheet/Workbook

Constraint Resolution

You want to see how the constraint resolves between 2 objects?

Use Display - Constraint then window select the 2 elements to obtain spacing resolution; select single element for physical resolution.

In the example below, I window selected the 2 clines of a Diff Pair to obtain spacing resolution. The actual measured gap is supplied in the middle section of the report.



Constraint Hierarchy

	Element 1	Element 2
Description	Horizontal Line Segment "Dp_Dr_P, Etch/Top"	Horizontal Line Segment "Dp_Dr_N, Etch/Top"
Location	(645.50 3587.00)	(645.50 3575.00)
NetClass	DP_CLASS_PS	DP_CLASS_PS
Bus		
DiffPair	DP_DR	DP_DR
XNet		
Net	DP_DR_P	DP_DR_N
PinPair		
NetClass-NetClass		
Region		

Air gap distance between elements = 6.00000 mils

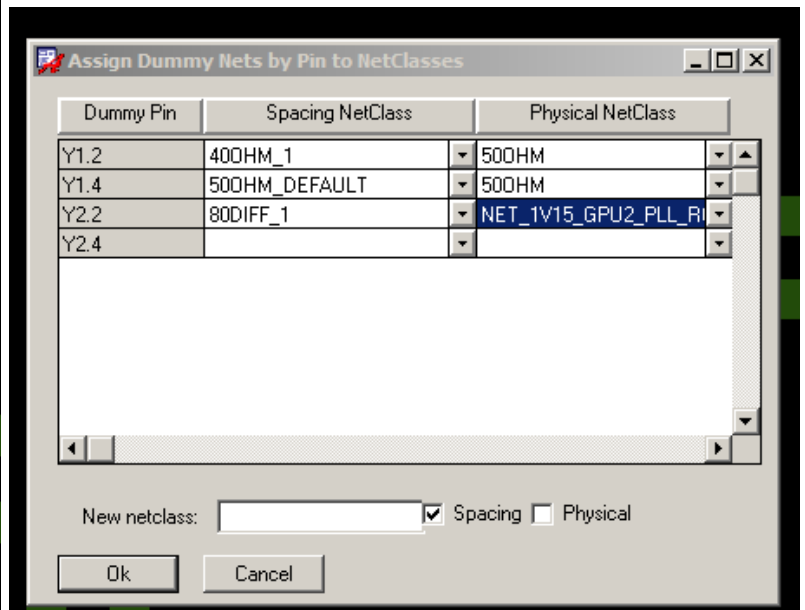
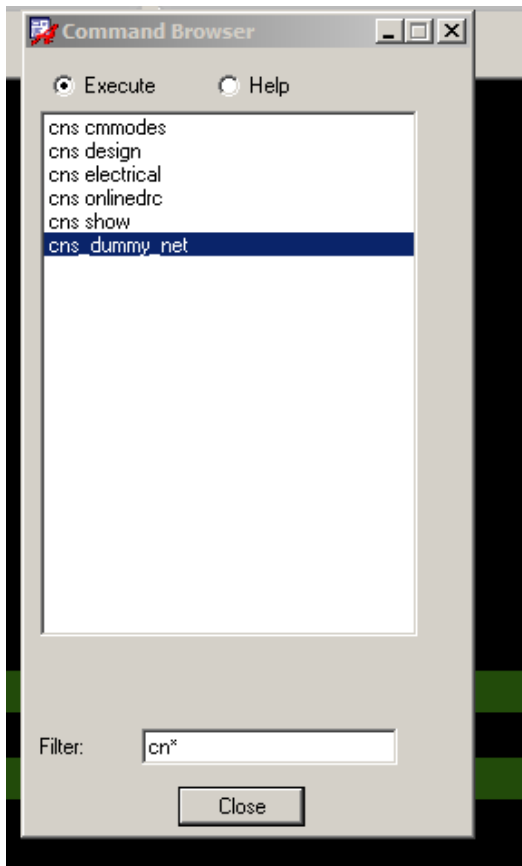
Resolved Spacing and Diffpair Constraints

Resolved Level	Source Name	Constraint	Value
Design	DEFAULT	Line to Line Spacing	4 MIL
DiffPair	DP_DR	Differential Pair Primary Gap	6 MIL
DiffPair	DP_DR	Differential Pair Neck Gap	3.9 MIL
DiffPair	DP_DR	Differential Pair Coupled Tolerance Plus	0.1 MIL
DiffPair	DP_DR	Differential Pair Coupled Tolerance Minus	0.1 MIL
DiffPair	DP_DR	DiffPair Minimum Gap	3.8 MIL

Tips & Tricks – Allegro PCB Editor

Assign pins of dummy nets to Net Classes

In 16.3 (June 2010 ISR), enter the command `cns_dummy_net` or type `helpcmd` to get the complete Allegro command set.



Preventing Shorts

A few suggestions I have seen on the SIG forum over the years

- ✓ In Allegro, It is possible to override a DRC with properties, make sure that the nets involved don't have the "NO_DRC" property on a pin(s).
- ✓ Make sure there are no "waived DRCs" that are not valid.
- ✓ Make sure your DRC modes are enabled. Be especially careful with newly released DRCs (ex Microvia based) as they are disabled by default.
- ✓ Run DBDoctor to keep the design in sync and up to date
- ✓ Make sure the plane layers in the cross section form agree with the artwork control form (negative or positive)
- ✓ Make sure the padstacks are designed properly; both Antipads and thermals

Tips & Tricks – Allegro PCB Editor

- ✓ Include the IPC-D-356 netlist in your Fab Package; insist the Fabricator does a netlist to Gerber compare.

Suppress SIGNOISE Warnings and Error messages

To suppress Signoise warning and error messages from appearing in the message window, set the variable,

set sigsuppress ERROR WARNING

Write vs Save

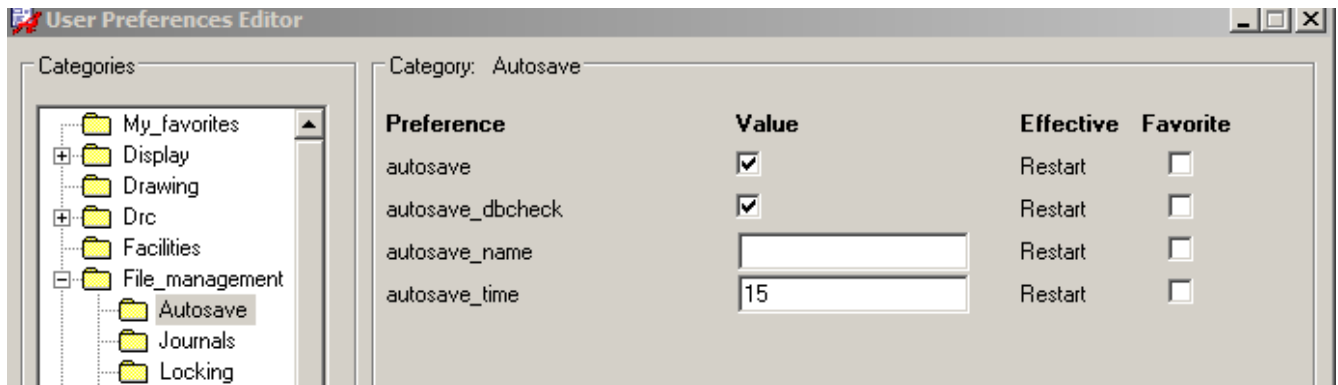
You want to save the current board file you are working on as a new name but want to keep the current file open using its current name. The “Save As” function will make the saved file the active file.

Enter “WRITE” in the command window followed by a design name.



Autosave the Database

Key variables to autosave the database

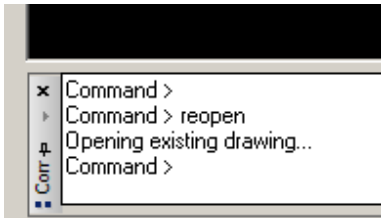


Tips & Tricks – Allegro PCB Editor

Reopen command

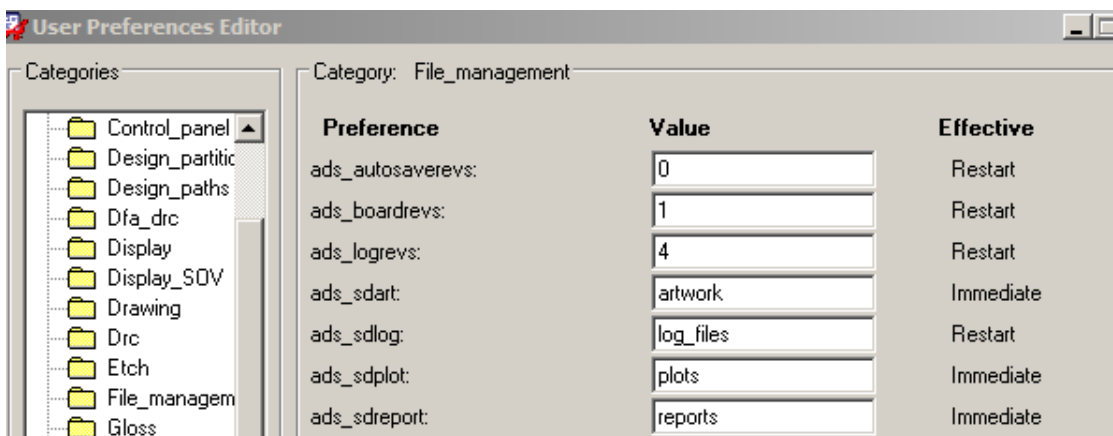
Most people do not know about this convenient command which does what it says, reopens a design file.

Enter “reopen” in the command window to reopen the same file.



File Management (artwork, plots, log files, reports)

Looking to manage your artwork, plots and reports more efficiently? There is a suite of variables beginning with ADS designed to create subdirectories for common data files.



Open File Manager from Allegro

Quick method to explore to your working directory

Tools – Utilities – File Manager

Looking for a particular pad stack based on holesize?

There are 2 methods to obtain a report of library padstacks:

1. Manufacturing > NC > Drill Customization > Library Drill Report
2. PadDesigner > Reports > Library Drill Report

Sort by column type by placing cursor in one of the cells then RMB – Sort by

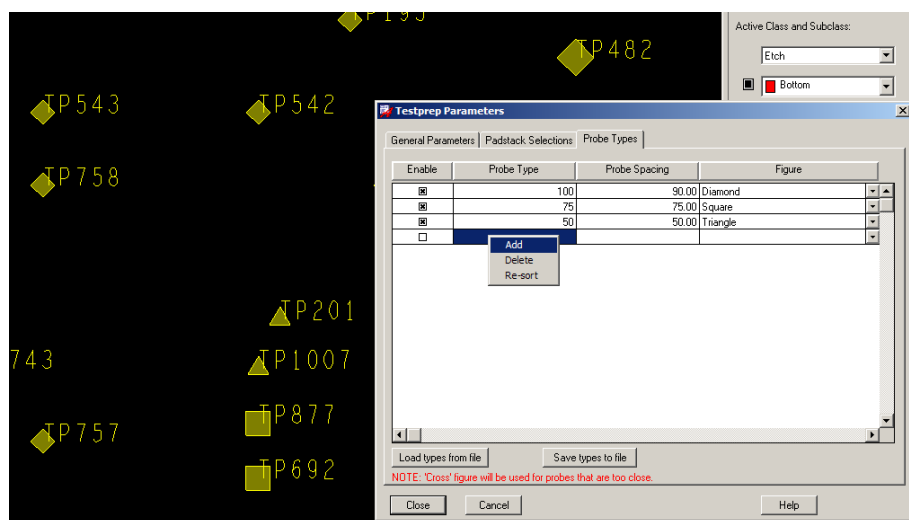
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Type	Size X	Size Y	+ Tolerance	- Tolerance
Micro Drill	8.00		0.00	
Micro Drill			0.00	
Micro Drill	10.00		0.00	
Micro Drill	10.00		0.00	
Micro Drill	10.00		0.00	
Micro Drill	14.00		0.00	
Micro Drill	16.00		0.00	
Micro Drill	16.00		0.00	

Testability – Multiple Probe-Type Support

The Testprep parameter form supports entries for multiple probe-types. Typically the names are 100, 75 and 50 MIL. The names and center to center spacing is user definable.

To add a row in the form, select a Probe Type cell then RMB – Add.



Drill Legend Support for INCHES and METRIC

Update your default .dlt file as shown below

```
?AlternateUnits "millimeters"
?ColumnDefinitions '(
    ("Figure" "FIGURE" 7)
    ("Holesize" "SIZE" 15)
    ("Tolerance" "Tolerance" 15)
    ("Holesize2" "SIZE MM" 15)
    ("Tolerance2" "TOLERANCE MM" 15)
```

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```
("PlateStatus" "PLATED"    10)
("NonStandard" "NONSTANDARD" 15)
("Quantity"   "QTY"        6)
)
```

Uprev Symbols

The command “uprev_overwrite” can be used to uprev library files to the current software revision.

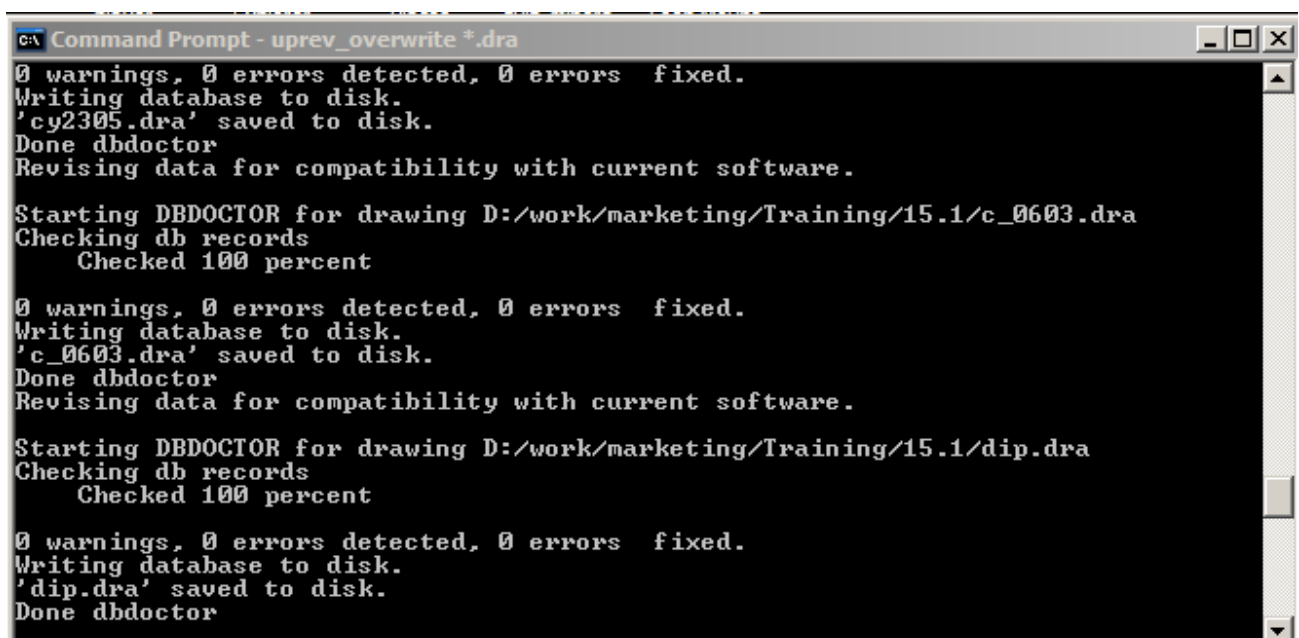
Syntax – uprev_overwrite n where n = *.dra; *.psm; *.ssm, etc

Ex – in a command window, enter uprev_overwrite *.dra to update all .dra files to the current software level.

For more flexibility in upreving in a DOS command window do a:

```
uprev -help
```

**** Be sure to make a backup copy of your library before uprev**



Tips & Tricks – Allegro PCB Editor

Troubleshooting – Running Allegro in Safe Mode

Many Allegro programs support the "-safe" command line option. This starts the program without any user customizations or extensions. This can be used a debug mechanism to eliminate if user configurations are the cause of the problem with the product.

It disables loading at startup

- local env file (<HOME>/pcbenv/env)
- cds_site configuration data
- any user skill code
- pre-register scripts
- ini file which stores window size/position information
- most recent used files (MRU)
- remembered Windows positions (.geo files)

UNIX: Currently it cannot disable X window resource settings.

In addition graphic programs support the "-noopengl" option which will disable the enhanced Graphics based upon OpenGL.

Example: Run an "out of the box" allegro without opengl

```
allegro -safe -noopengl
```

Tips from Allegro PCB Users

Join the Cadence Community Website and see what other users are saying

<http://www.cadence.com/community/forums/27.aspx?CMP=home>

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Topics	Replies	Views	Last Post
Purpose of set auto_remove off started by Neha Anu on Today at 03:16 PM	0	12	By Neha Anu Today at 03:16 PM
"Ref_Des" alignment of data, "place.txt" wanted in the file. started by min sook on 26 Feb 2010 04:46 AM	3	112	By Ejlersen Today at 12:34 PM
Question about curved routing started by Tanveer on 04 Mar 2010 01:41 PM	7	144	By Ejlersen Today at 12:30 PM
Verbose and Quiet mode started by Neha Anu on Today at 11:38 AM	0	16	By Neha Anu Today at 11:38 AM
Length of a net started by Neha Anu on 11 Mar 2010 03:28 PM	2	89	By Neha Anu Yesterday at 06:00 PM
Editing in Constraints started by Tanveer on 14 Mar 2010 11:04 PM	1	39	By steve Yesterday at 03:16 PM
Constraint Manager - Diff Pair (Cadence 16.2) started by mike78 on 13 Mar 2010 02:02 AM	3	62	By gchrisdesign 13 Mar 2010 03:07 AM
How can I make a package with two pads shorted together? started by Allan M on 10 Mar 2010 08:33 PM	11	166	By Allan M 12 Mar 2010 09:55 PM

Unanswered

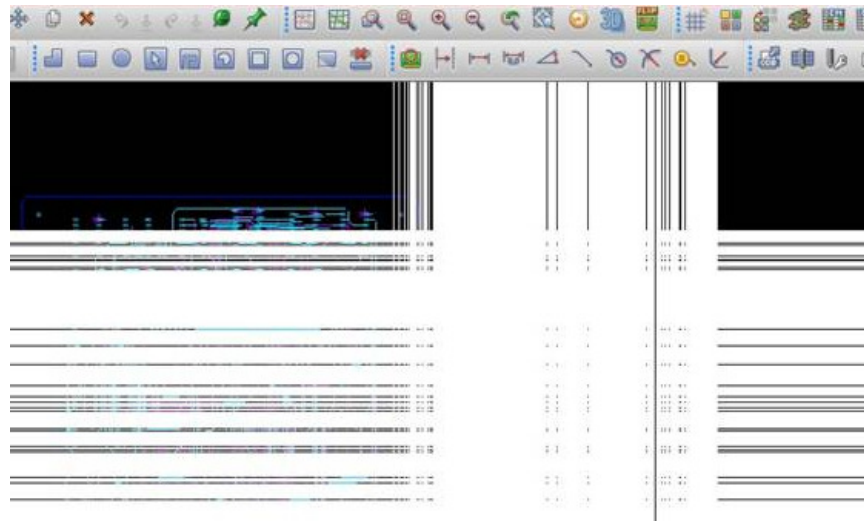
- Purpose of set auto_remove off
- Verbose and Quiet mode
- Looking for a PCB designer
- Same Net DRC for Staggered (Buried) vias
- Footprint qfn48/qfn16
- Placement grid setup
- Constraint Manager resetting my Ground...
- cross probing - Placement
- launch allegro_free_viewer_16-3 with...
- Cannot update NET_SPACING_TYPE

Popular Tags

16.3 Allegro PCB **Allegro**
PCB Editor Allegro PCB

Windows 7 – Graphic issues

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Try disabling Aero (desktop composition) for allegro only:

1. Navigate to the executable or shortcut for the program you wish to disable desktop composition using Explorer.
2. Right click on it and select Properties.
3. Select the Compatibility tab and check Disable desktop composition, then click on OK."
4. As always, ensure your graphic driver is up to date.